

Compal Confidential

ECQ60 Schematics Document

Desktop LGA-775 Package with Grantsdale + ICH6 + ATI M24-P

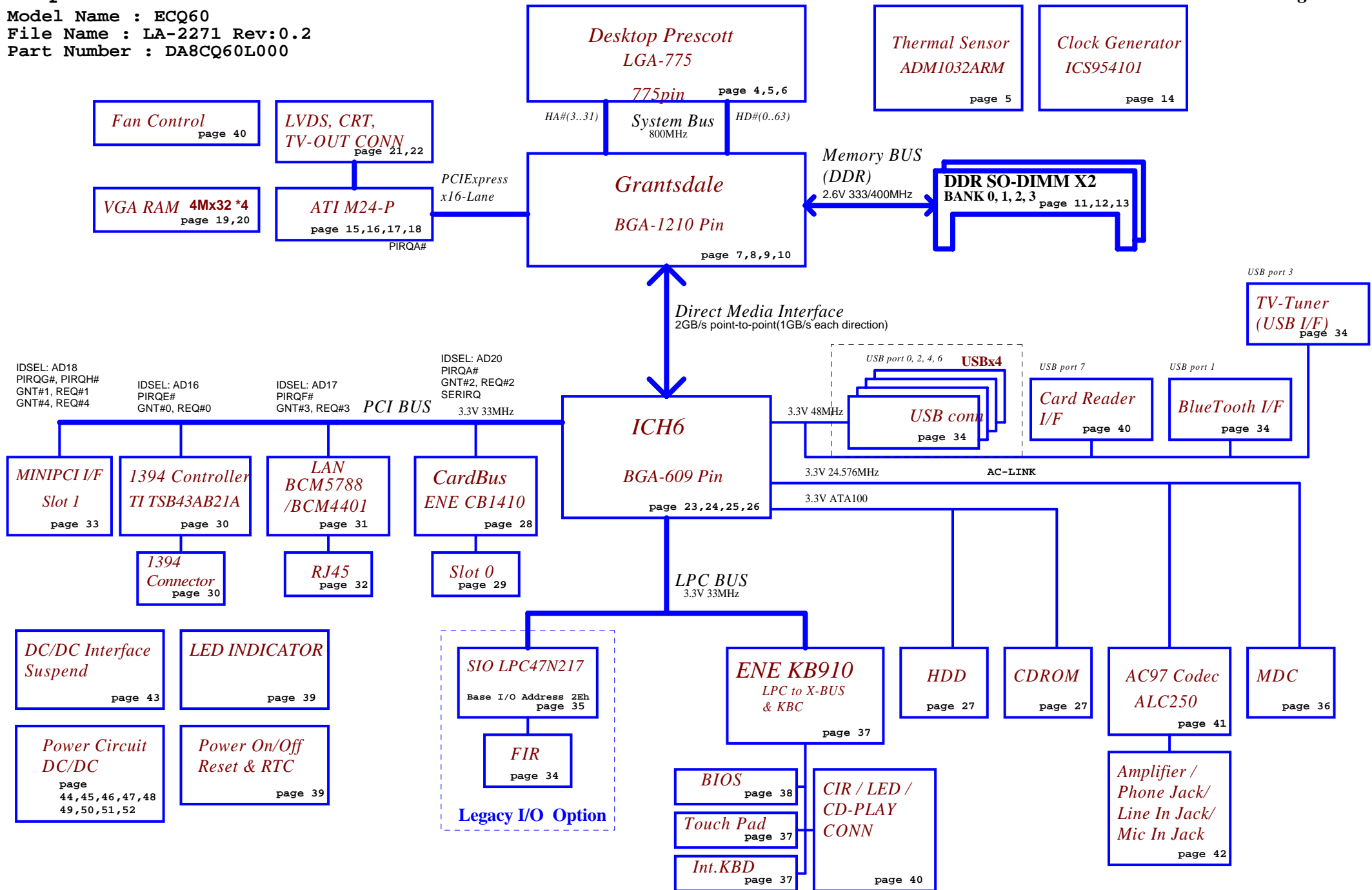
2004-08-09-C

REV: 1.0

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Compal Electronics, Inc.			
Title			
Cover Sheet			
Size	Document Number		Rev
Customer	ECQ60 LA-2271		1A
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Model Name : ECQ60
File Name : LA-2271 Rev:0.2
Part Number : DA8CQ60L000



Compal Electronics, Inc.		
Title		
Block Diagram		
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Voltage Rails

Power Plane	Description	S0-S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+V_FSB_VTT	1.2V rail for Processor I/O & GTL Termination	ON	OFF	OFF
+PCIE_1.2VS	+PCIE_1.2VS power rail for VGA PCIExpress	ON	OFF	OFF
+1.3VS	1.3VS for DDR1 Termination	ON	OFF	OFF
+VGA_CORE	VGA Core Power	ON	OFF	OFF
+1.5VS	MCH & ICH Core Power	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.6V	2.6V power rail for DDR1	ON	ON	OFF
+2.6VS	2.6VS switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+12VALW	12V always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
VGA			PIRQA
CardBus	AD20	2	PIRQA
LAN	AD17	3	PIRQF
Mini-PCI	AD18,AD22	1	PIRQG/PIRQH
1394	AD16	0	PIRQE

EC SM Bus1 address

Device	Address	Device	Address
Smart Battery	0001 011X b	ADM1032	1001 100X b
EEPROM(24C16/02)	1010 000X b		
(24C04)	1011 000Xb		

EC SM Bus2 address

ICH6 SM Bus address

Device	Address
Clock Generator (ICS954101)	1101 001Xb
DDR DIMM0	1010 000Xb
DDR DIMM1	1010 010Xb

Board ID Table for AD channel

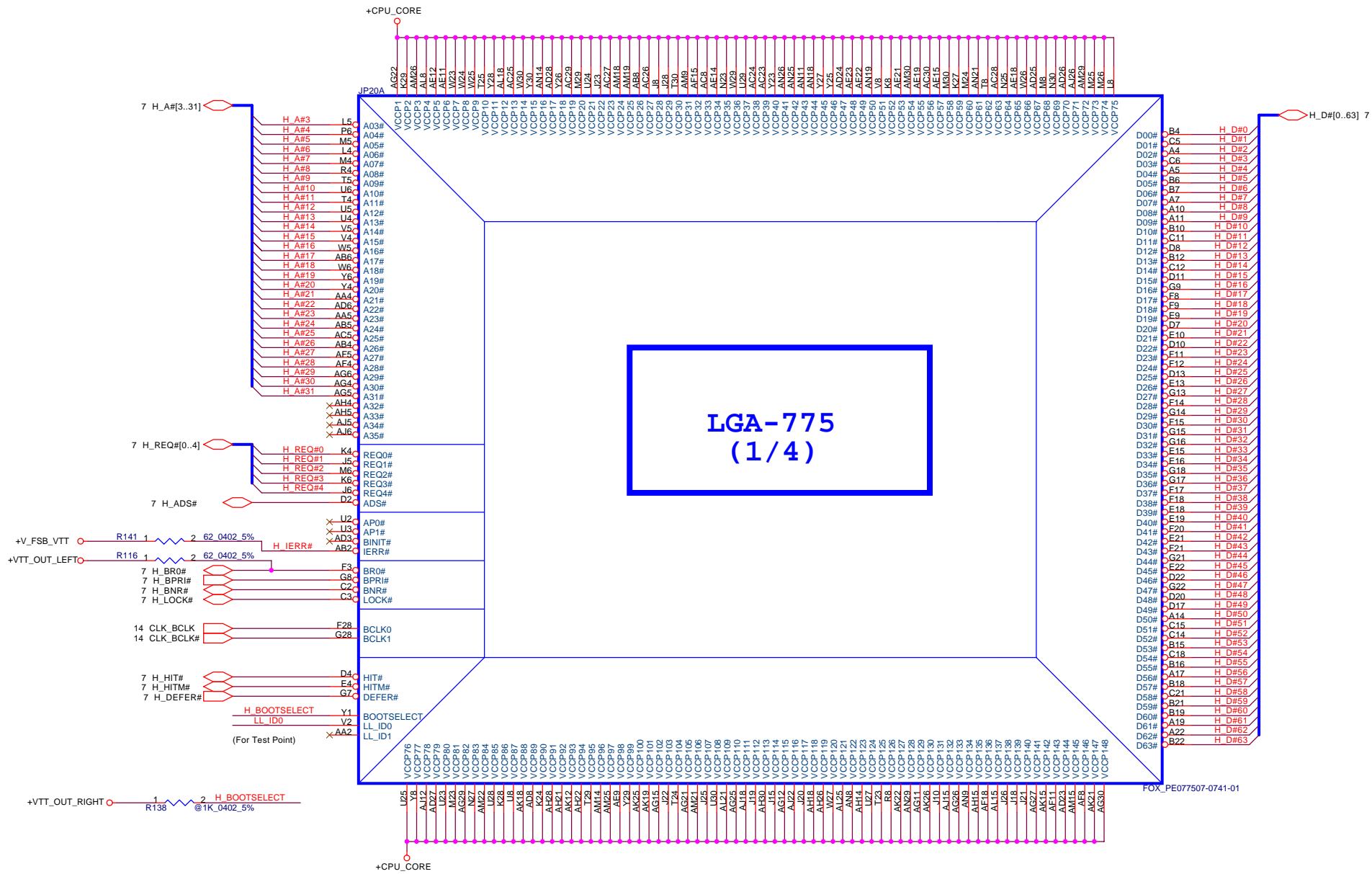
Vcc	3.3V +/- 5%			
Ra	100K +/- 5%			
Board ID	Rb	V _{AD_BID min}	V _{AD_BID typ}	V _{AD_BID max}
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

Board ID	PCB Revision
0	0.1
1	0.2
* 2	0.3
3	0.4
4	
5	
6	
7	

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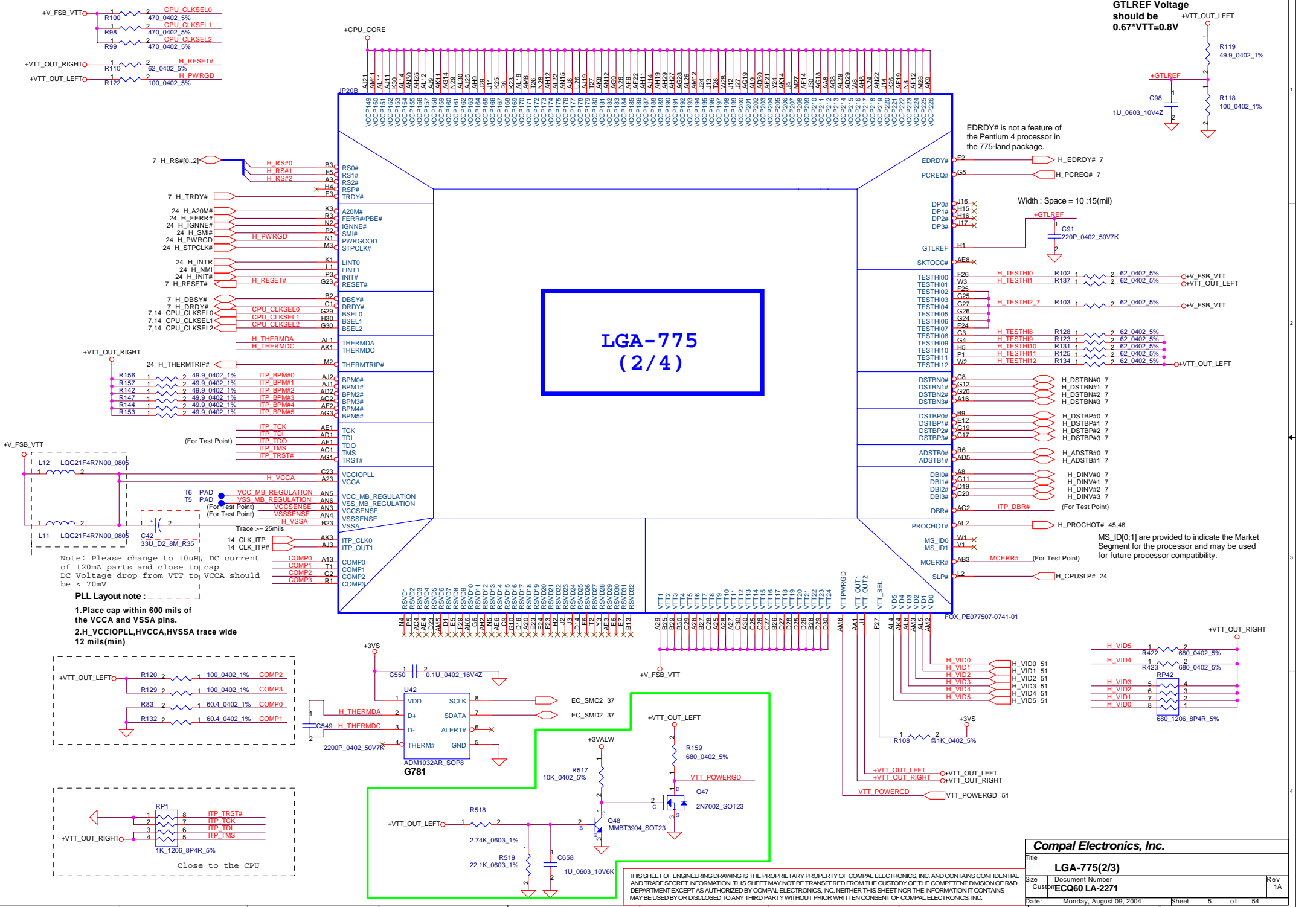
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LGA-775 (2/4)



GTLREF Voltage should be 0.67*VTT=0.8V

+VTT_OUT_LEFT

R119 49.9_0402_1%

R118 100_0402_1%

C98 1U_0603_10V4Z

EDRDY# is not a feature of the Pentium 4 processor in the 775-land package.

Width : Space = 10 : 15(mil)

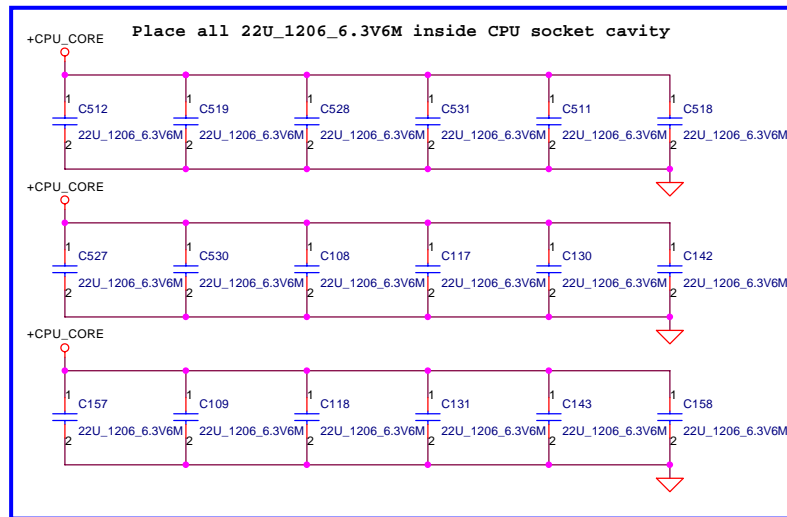
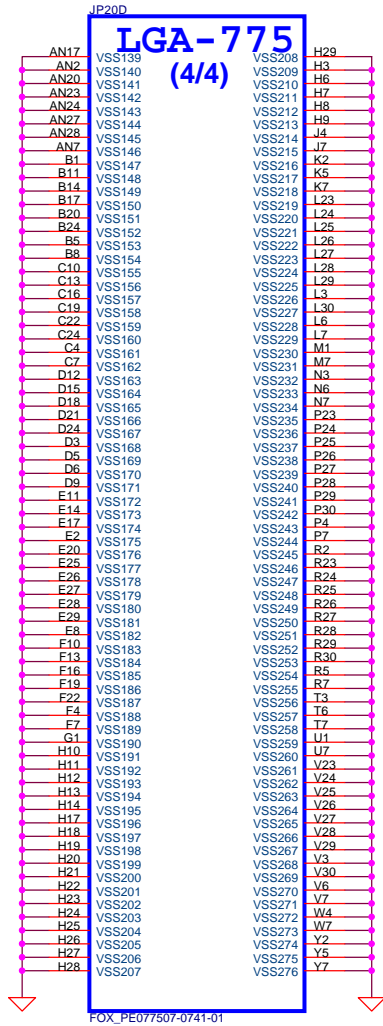
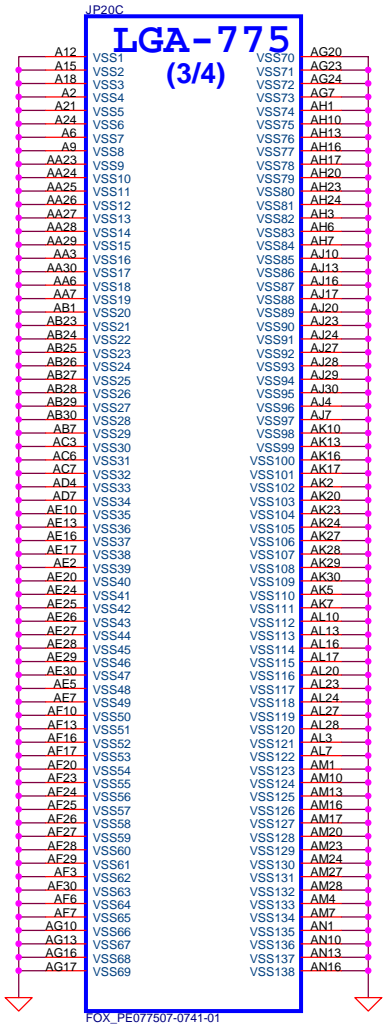


Note: Please change to 10uH, DC current of 120mA parts and close to cap DC Voltage drop from VTT to VCCA should be < 70mV

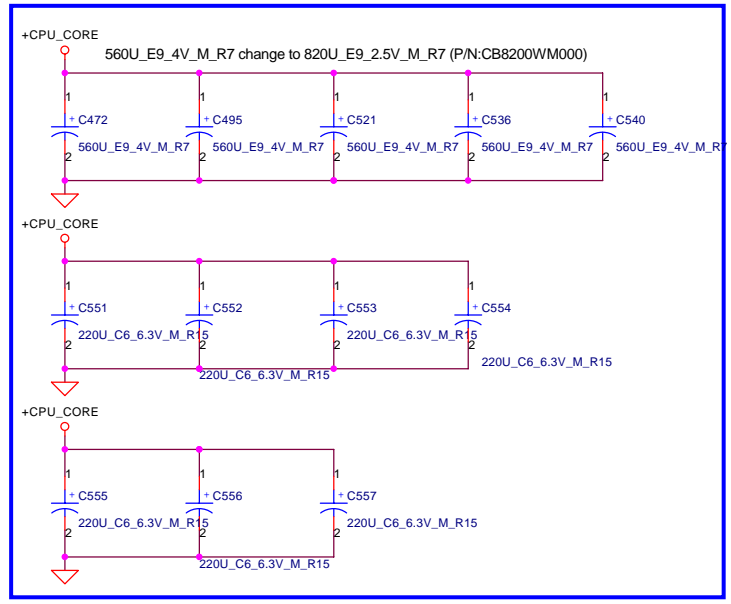
- PLL Layout note :**
1. Place cap within 600 mils of the VCCA and VSSA pins.
 2. H_VCCIOPLL, HVCCA, HVSSA trace wide 12 mils(min)

MS_ID[0:1] are provided to indicate the Market Segment for the processor and may be used for future processor compatibility.

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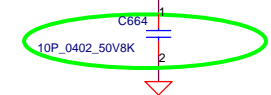
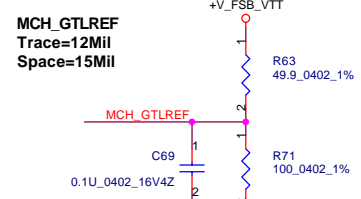
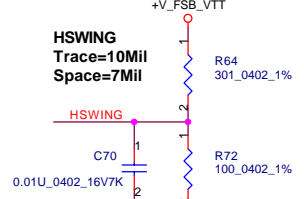
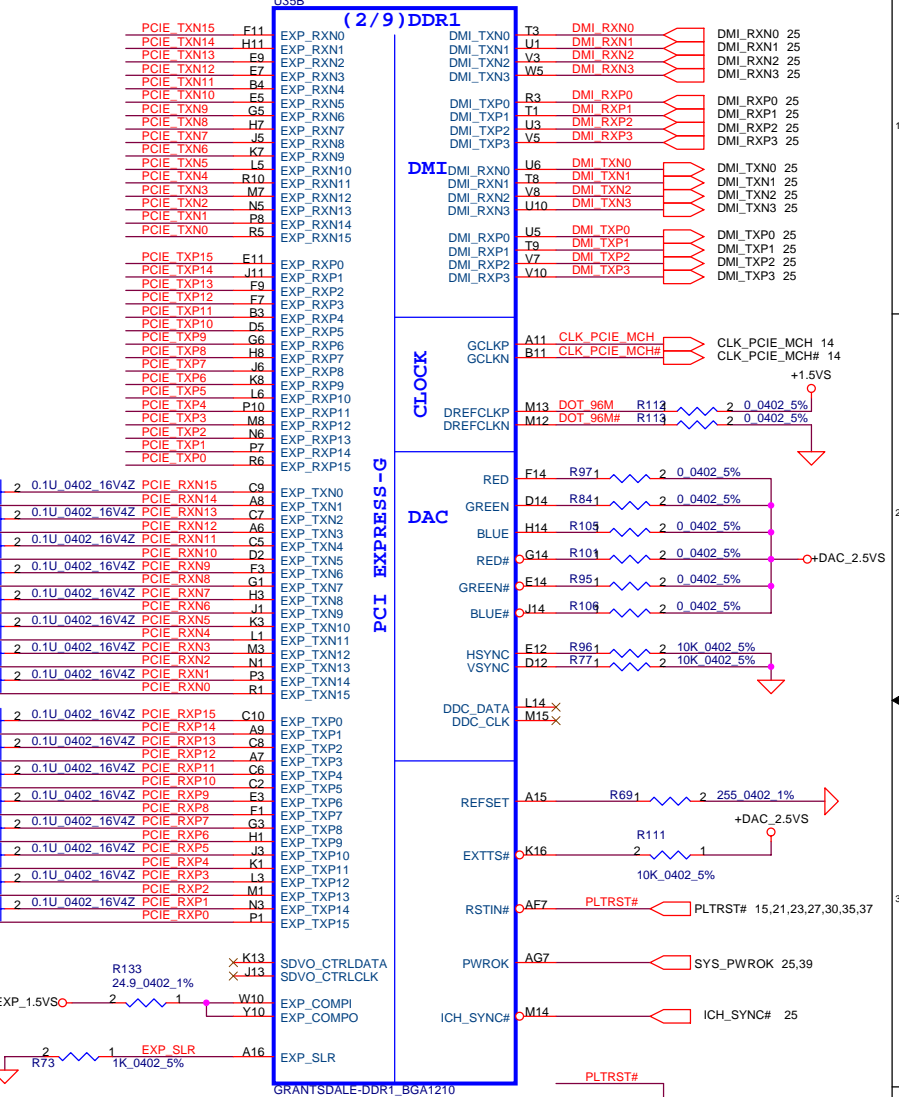
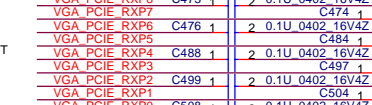
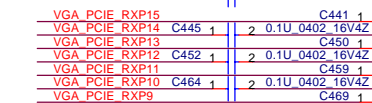
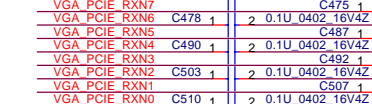
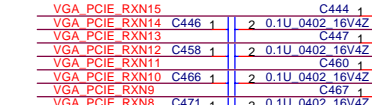
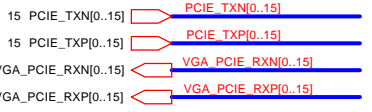
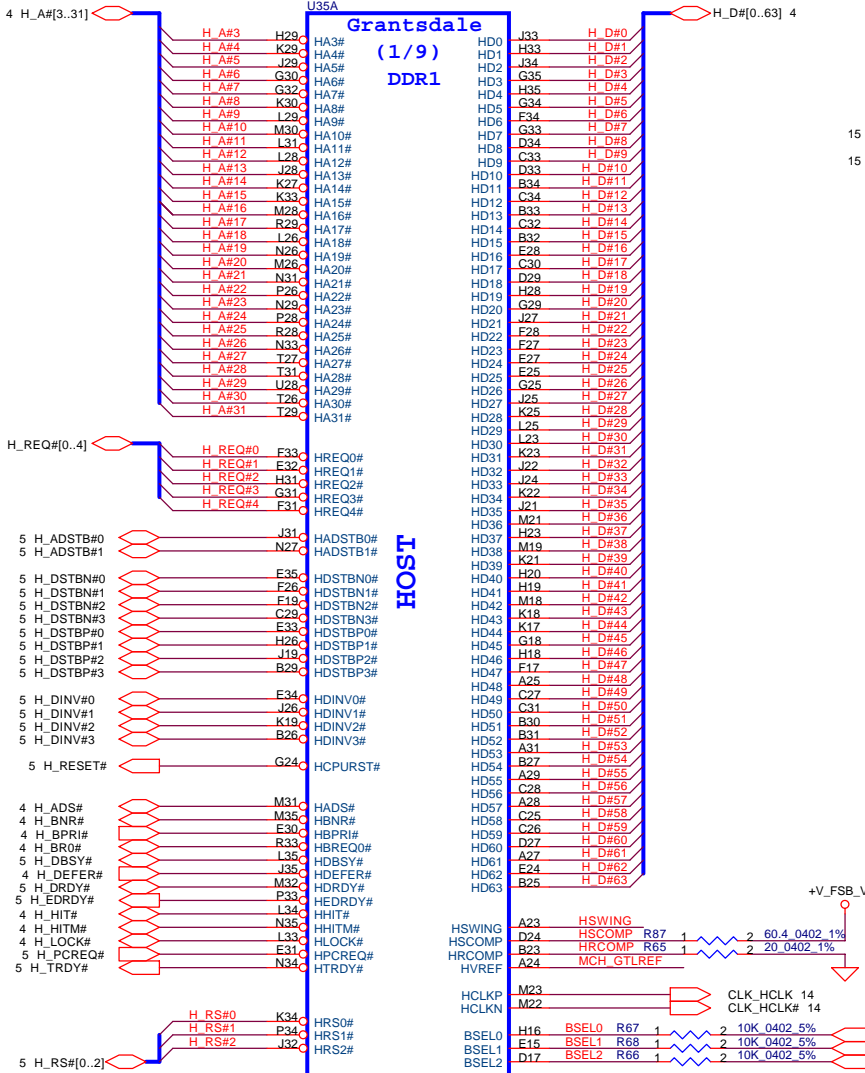


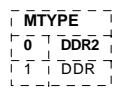
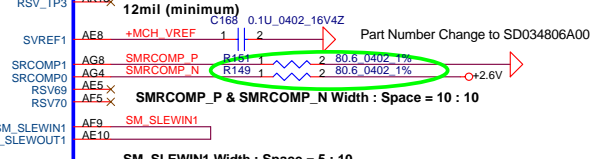
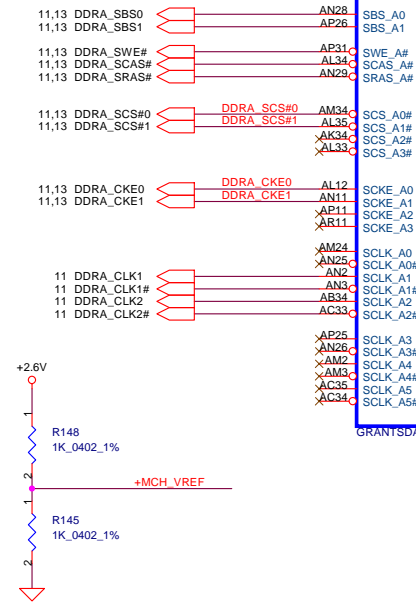
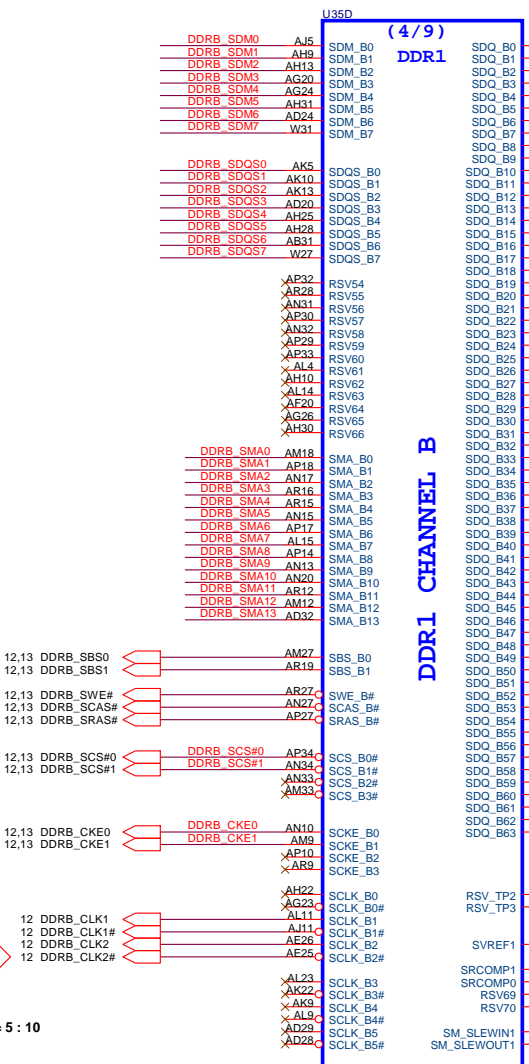
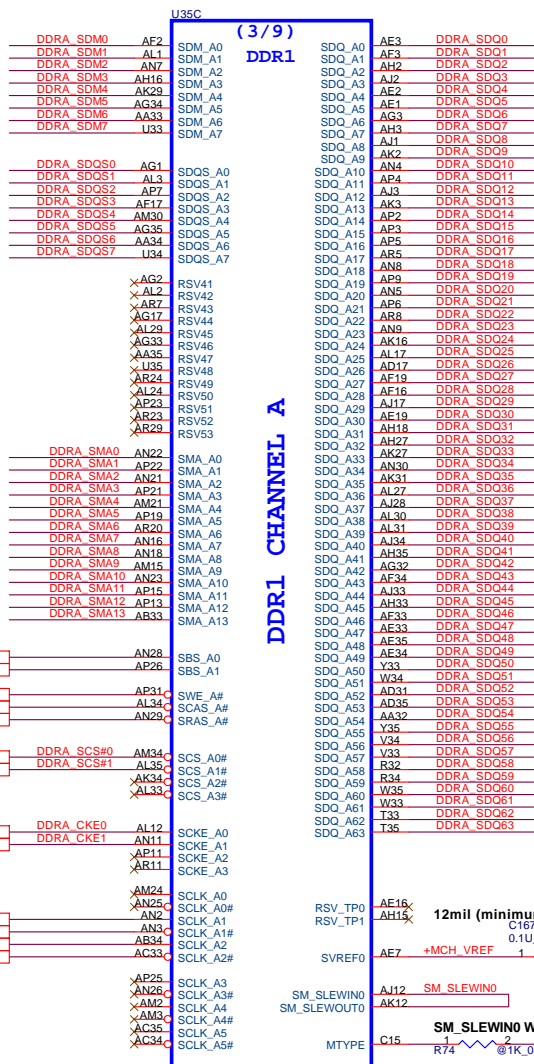
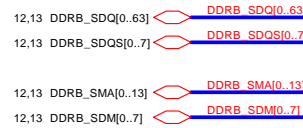
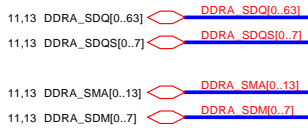
SANYO OS-CON 820uF _ERS7m ohm* 5 H=13mm
 SANYO OS-CON 220uF _ERS13m ohm*7 H=6mm



Decoupling Reference Document:
 Grantsdale Chipset Platform Design guide Rev1.0 (14652)Page269

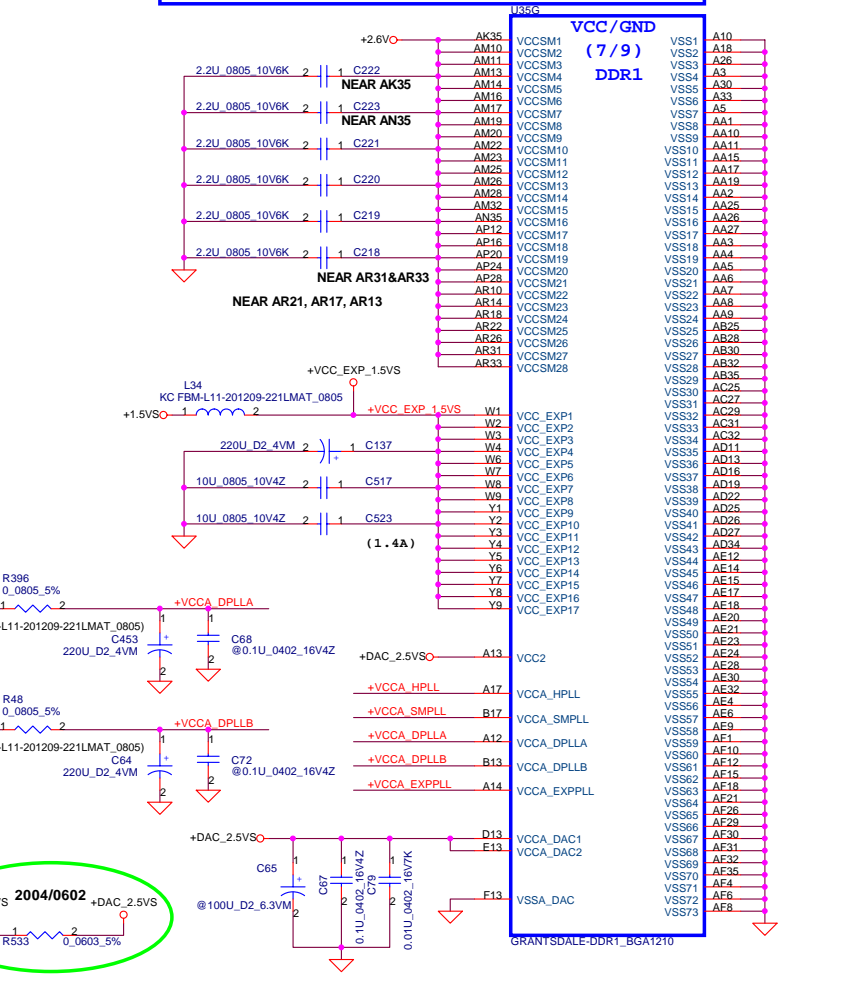
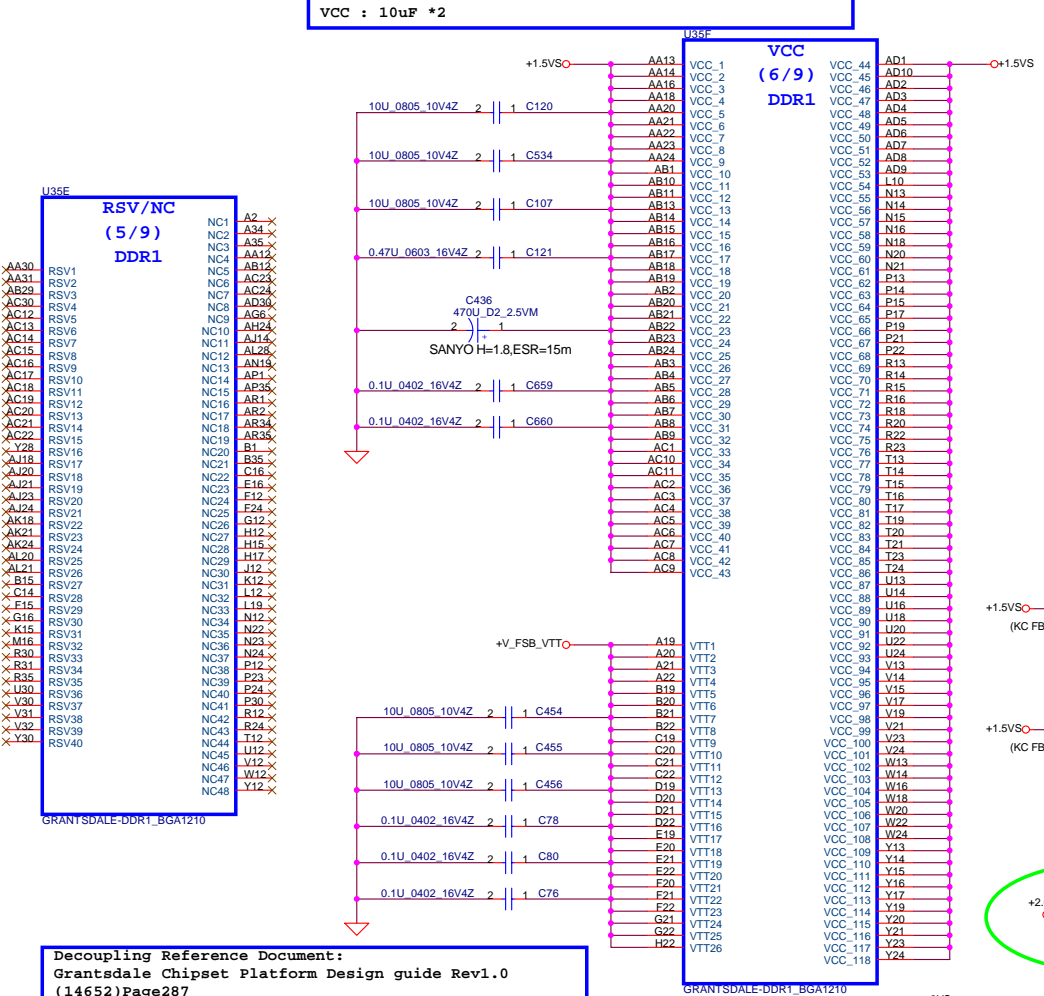
Decoupling Reference Requirement:
 560uF Polymer, ESR:6m ohm(each) * 10
 22uF X5R * 18



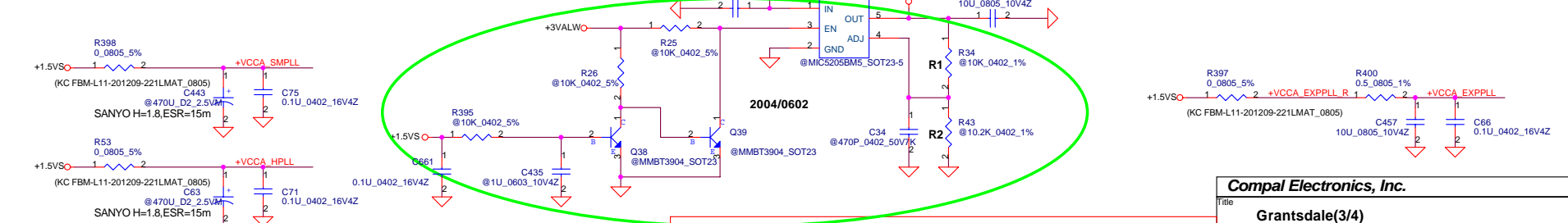


Decoupling Reference Document:
Grantsdale Chipset Platform Design guide Rev1.0
(14652)Page287
VCC : 10uF *2

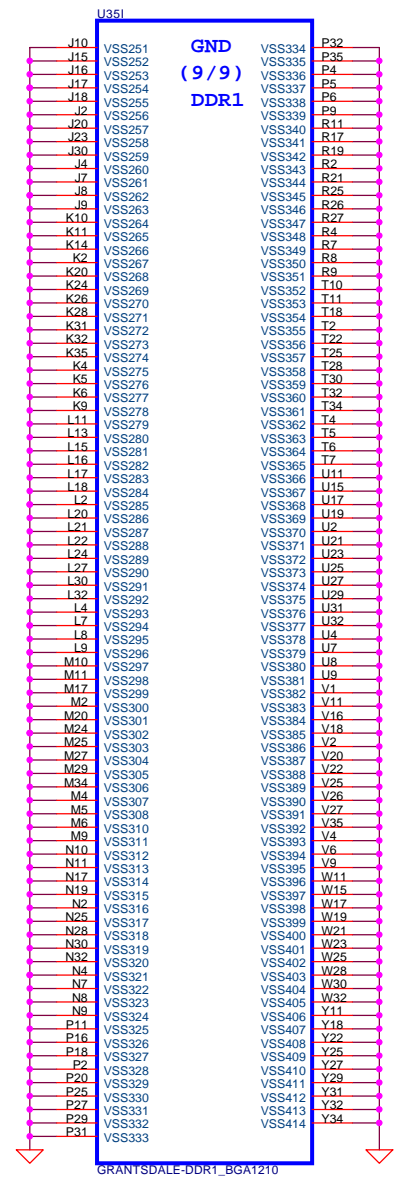
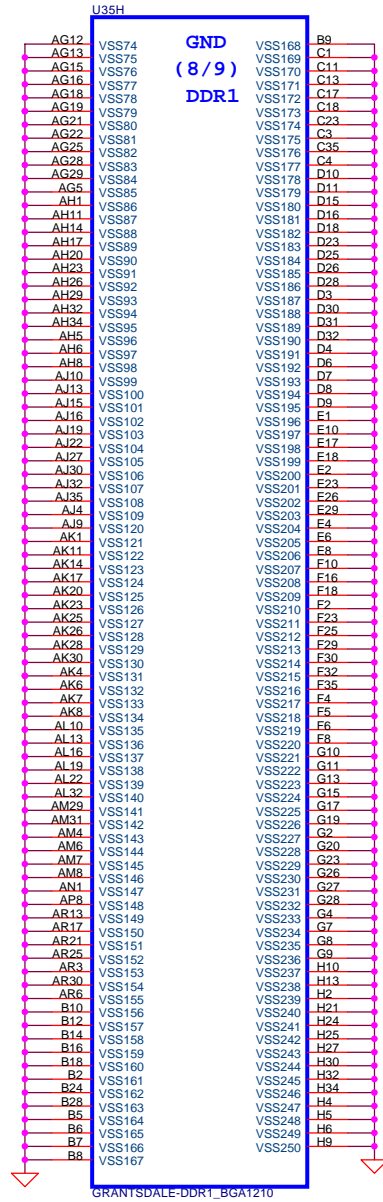
Decoupling Reference Document:
Grantsdale Chipset Platform Design guide Rev1.0
(14652)Page287
VCCSM : 2.2uF *6



Decoupling Reference Document:
Grantsdale Chipset Platform Design guide Rev1.0
(14652)Page287
VTT : 10uF *3



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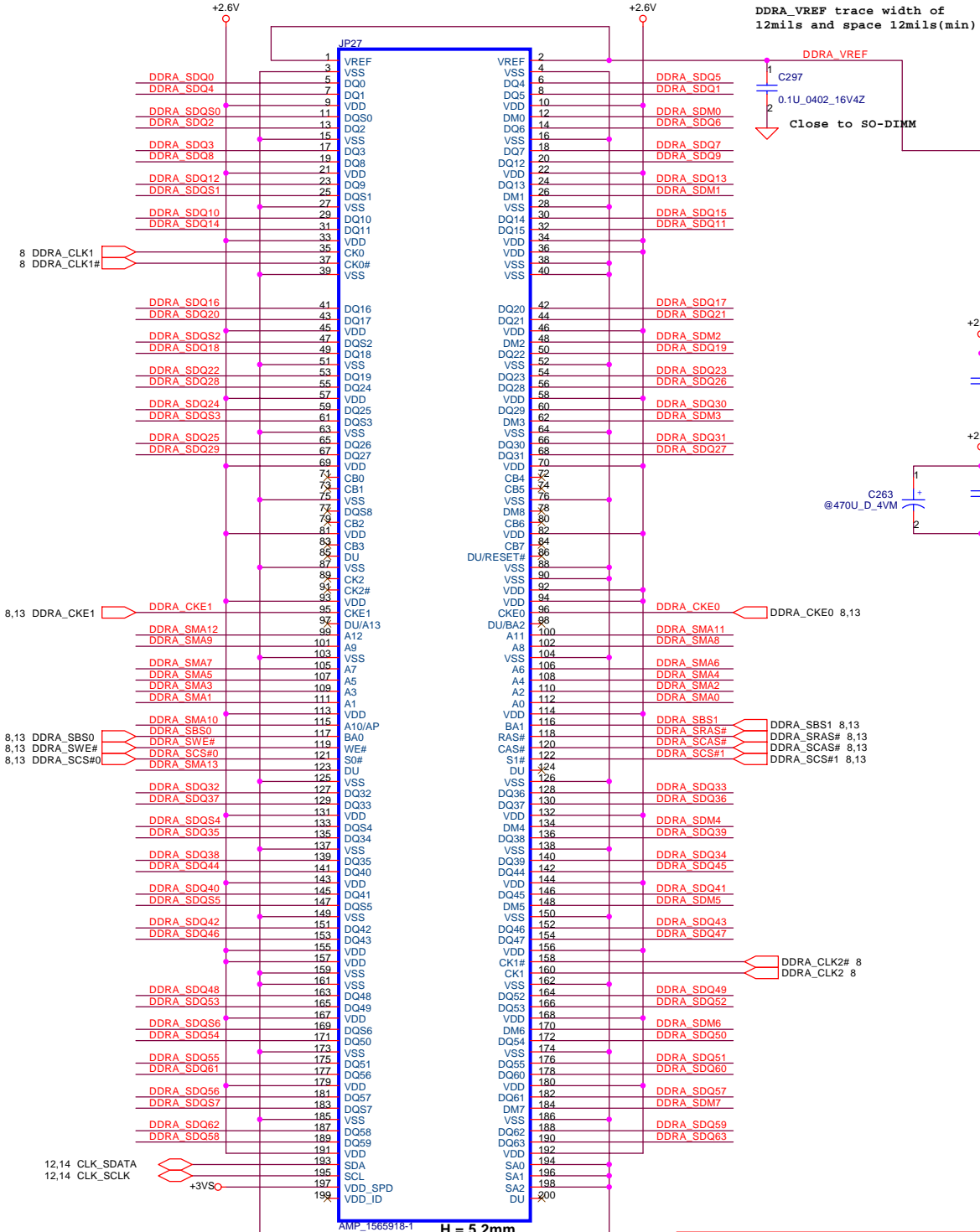


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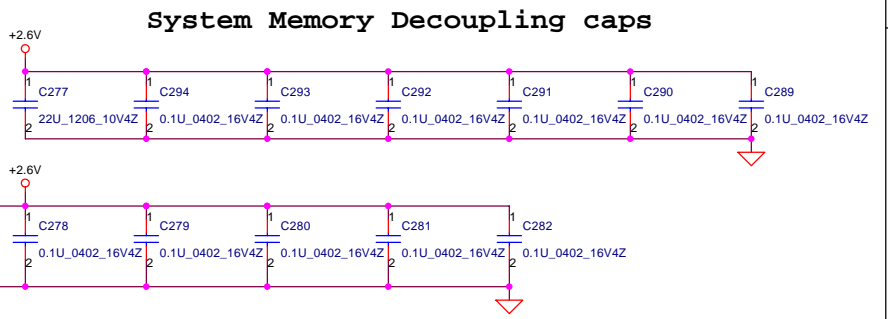
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- 8,13 DDRA_SDO[0..63] **DDRA_SDO[0..63]**
- 8,13 DDRA_SDQS[0..7] **DDRA_SDQS[0..7]**
- 8,13 DDRA_SMA[0..13] **DDRA_SMA[0..13]**
- 8,13 DDRA_SDM[0..7] **DDRA_SDM[0..7]**

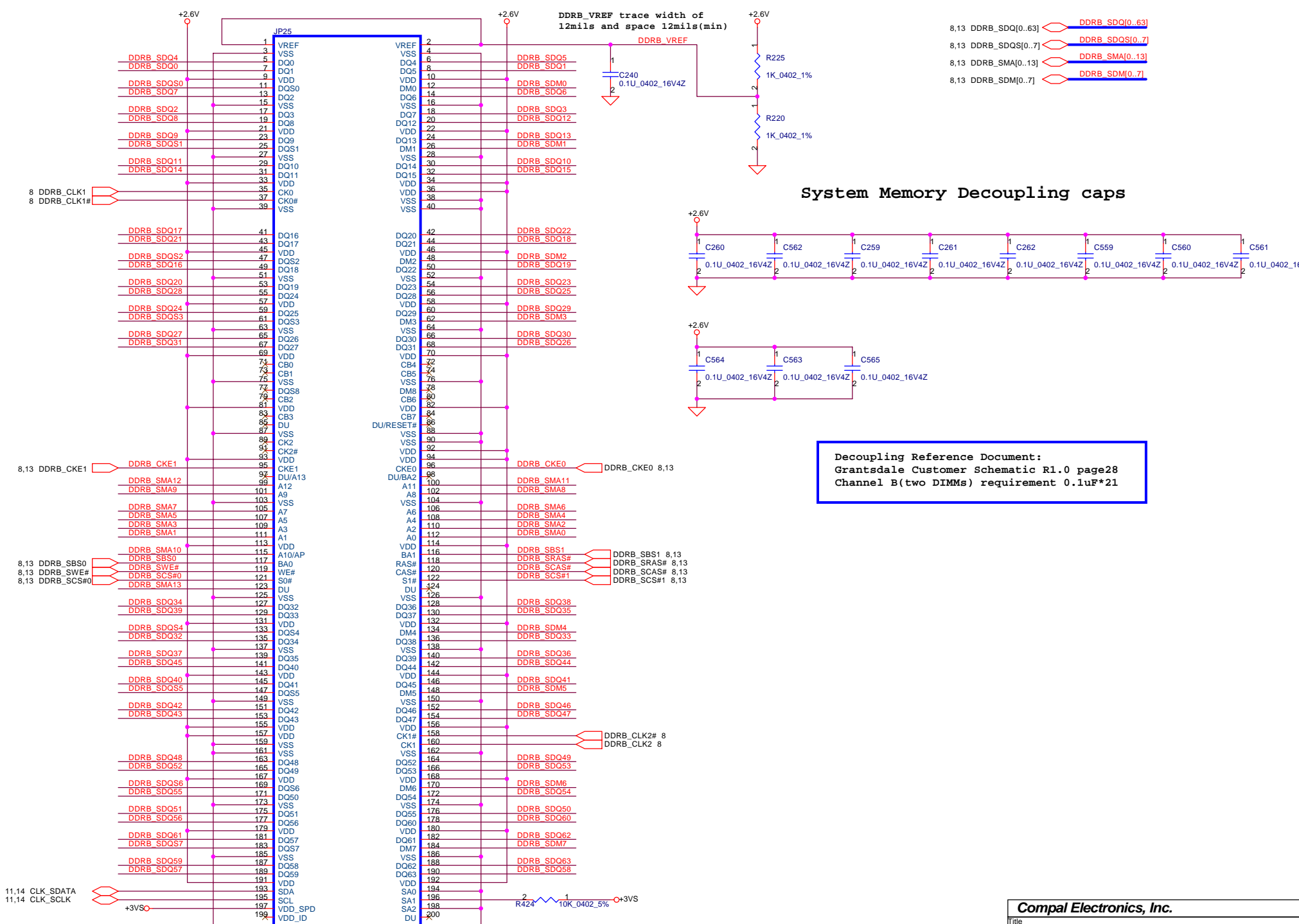


Decoupling Reference Document:
 Grantsdale Customer Schematic R1.0 page24
 Channel A (two DIMMs) requirement 22uF*1 ; 0.1uF*22

**REVERSE
SO-DIMM 0**

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Title DDR SO-DIMM 0 (CHANNEL A)		
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Compal Electronics, Inc.		
DDR SO-DIMM 1 (CHANNEL B)		
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REVERSE SO-DIMM 2

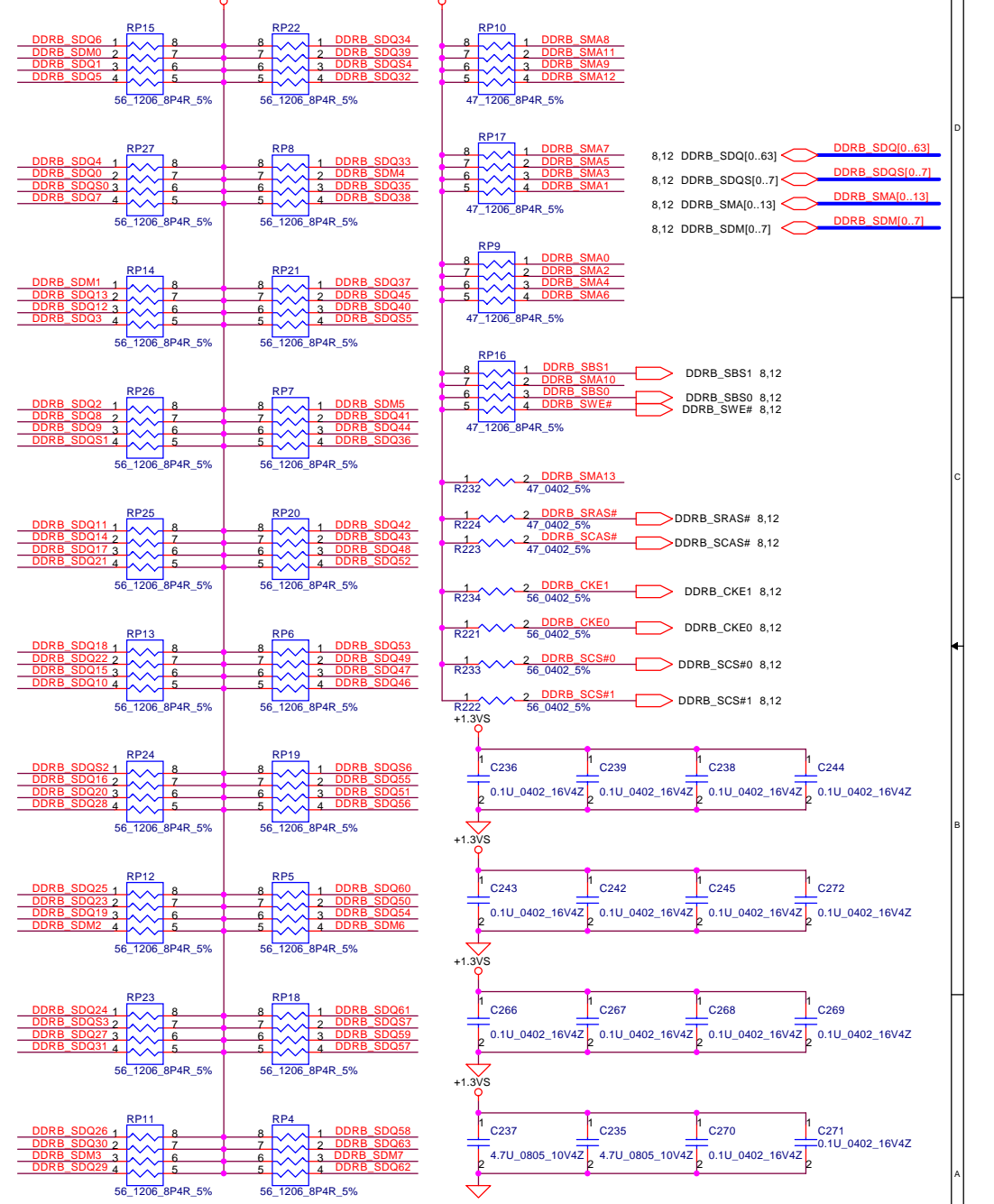
TYCO_1470804-2 H= 9.2mm

Channel A(DIMM0) Termination resistors & Decoupling caps



Decoupling Reference Document:
 Grantsdale Customer Schematic R1.0 page24
 Channel A(two DIMMs) requirement 4.7u*3 ; 0.1uF*26

Channel B(DIMM1) Termination resistors & Decoupling caps

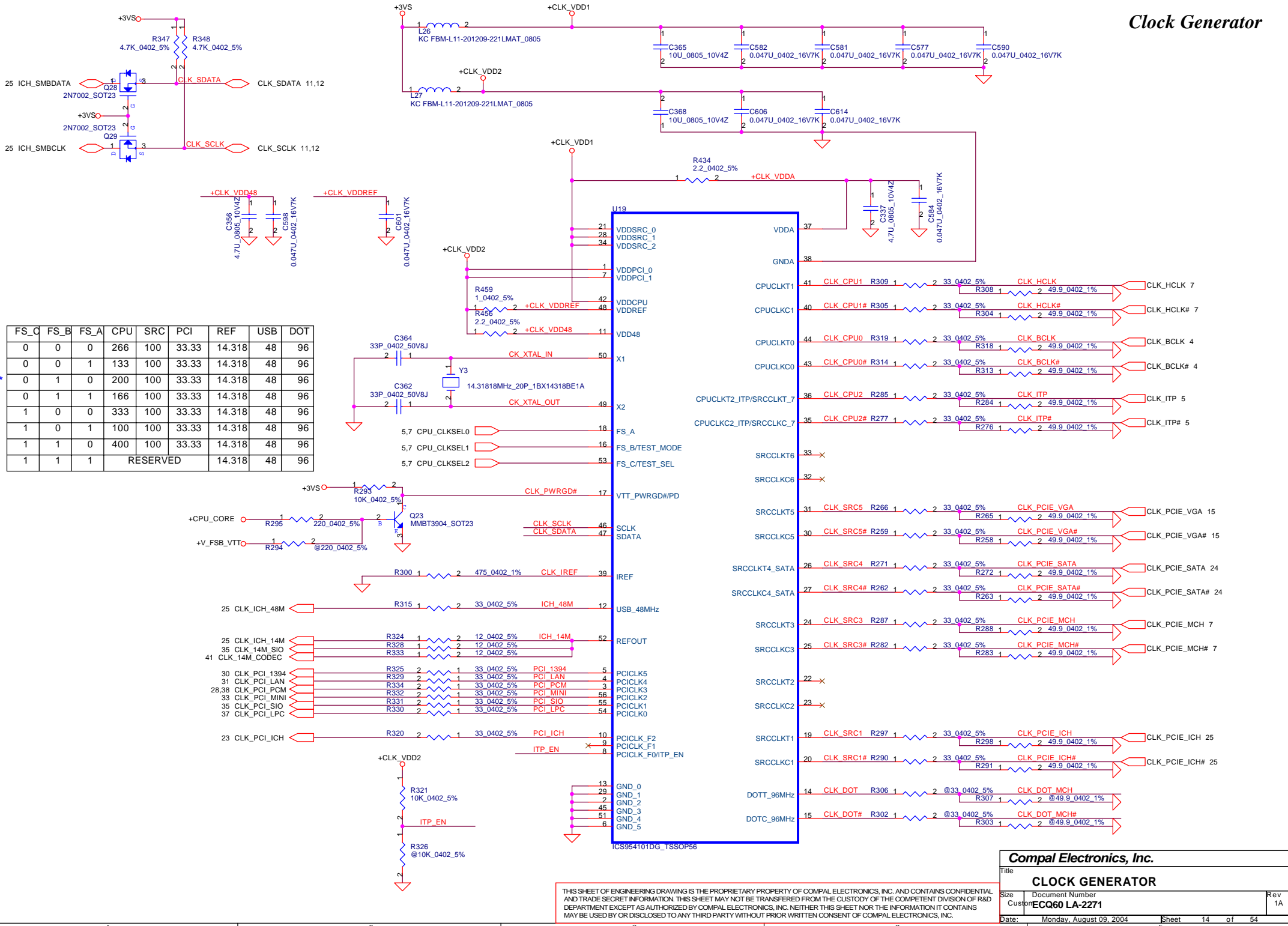


Decoupling Reference Document:
 Grantsdale Customer Schematic R1.0 page28
 Channel B(two DIMMs) requirement 4.7u*3 ; 0.1uF*28

Compal Electronics, Inc.			
Title DDR Termination & Decoupling			
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Clock Generator



FS_C	FS_B	FS_A	CPU	SRC	PCI	REF	USB	DOT
0	0	0	266	100	33.33	14.318	48	96
0	0	1	133	100	33.33	14.318	48	96
0	1	0	200	100	33.33	14.318	48	96
0	1	1	166	100	33.33	14.318	48	96
1	0	0	333	100	33.33	14.318	48	96
1	0	1	100	100	33.33	14.318	48	96
1	1	0	400	100	33.33	14.318	48	96
1	1	1	RESERVED			14.318	48	96

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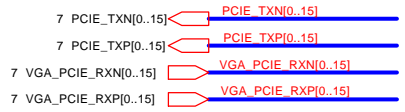
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Size: Custom
 Document Number: **ECQ60 LA-2271**
 Date: Monday, August 09, 2004

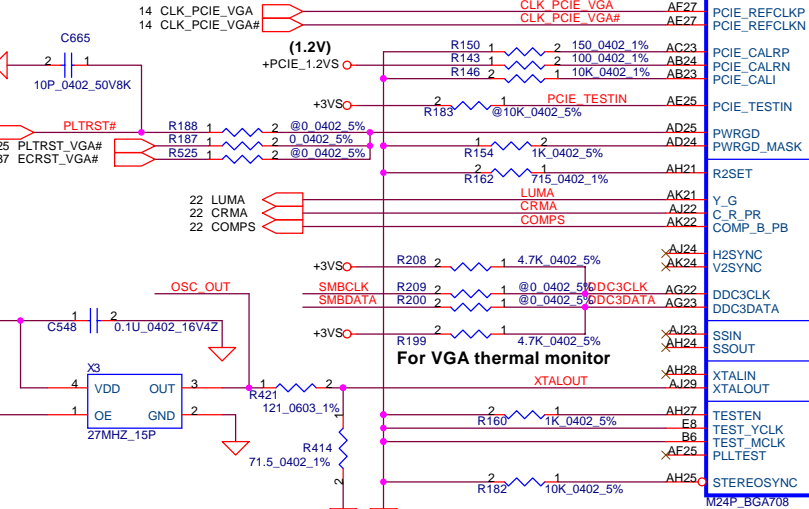
Rev 1A

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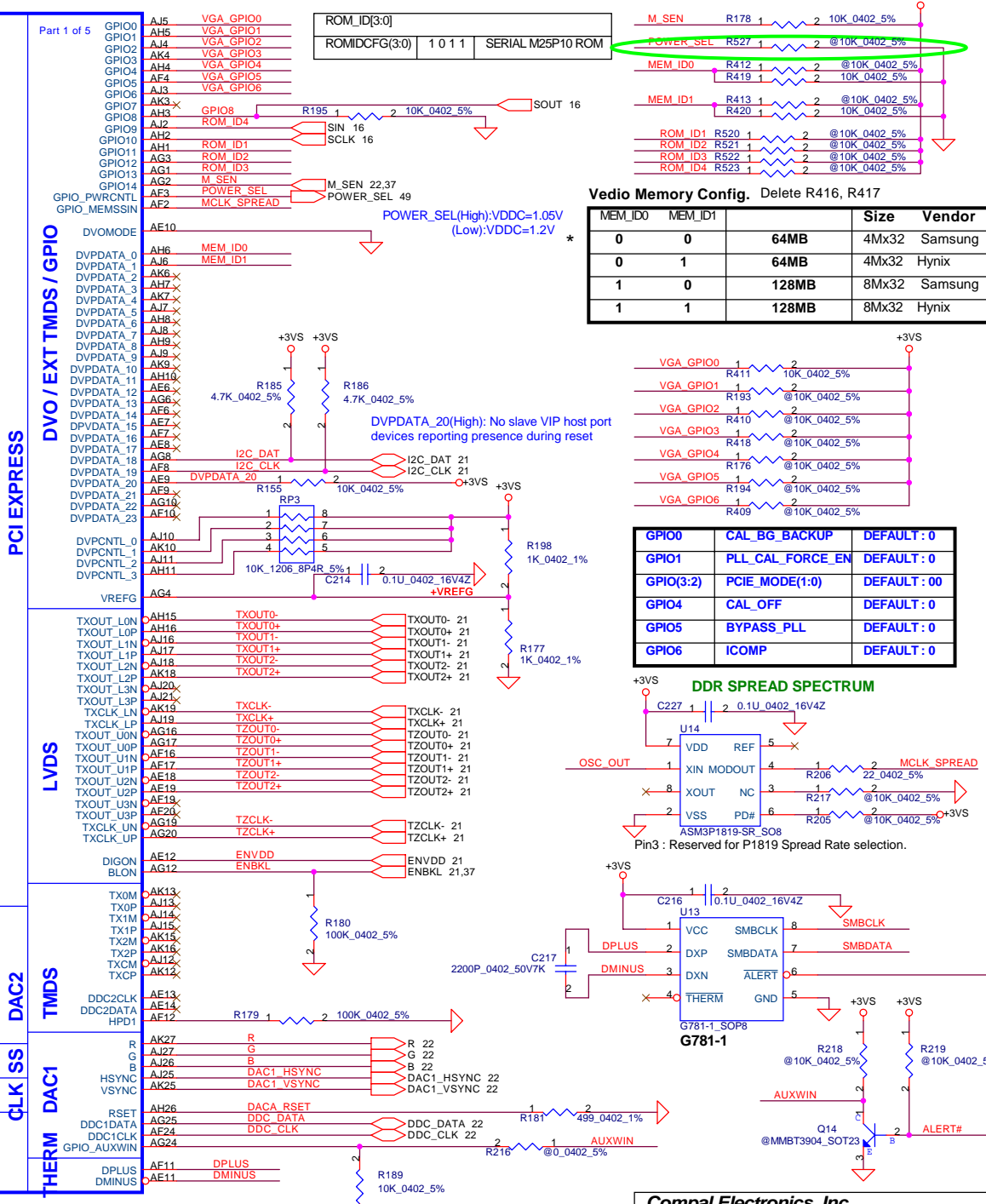
PCIE_TXP0	C197	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP0	AF26	PCIE_TX0P
PCIE_TXN0	C197	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN0	AE26	PCIE_TX0P
PCIE_TXP1	C196	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP1	AC25	PCIE_TX1P
PCIE_TXN1	C191	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN1	AB25	PCIE_TX1N
PCIE_TXP2	C189	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP2	AC27	PCIE_TX2P
PCIE_TXN2	C184	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN2	AB27	PCIE_TX2N
PCIE_TXP3	C184	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP3	AB26	PCIE_TX3P
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PCIE_TXN5	C166	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN5	W27	PCIE_TX5N
PCIE_TXP6	C162	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP6	Y26	PCIE_TX6P
PCIE_TXN6	C153	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN6	W26	PCIE_TX6N
PCIE_TXP7	C151	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP7	U25	PCIE_TX7P
PCIE_TXN7	C144	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN7	T25	PCIE_TX7N
PCIE_TXP8	C141	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP8	U27	PCIE_TX8P
PCIE_TXN8	C136	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN8	T27	PCIE_TX8N
PCIE_TXP9	C133	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP9	T26	PCIE_TX9P
PCIE_TXN9	C128	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN9	T26	PCIE_TX9N
PCIE_TXP10	C127	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP10	P25	PCIE_TX10P
PCIE_TXN10	C122	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN10	N25	PCIE_TX10N
PCIE_TXP11	C119	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP11	P27	PCIE_TX11P
PCIE_TXN11	C113	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN11	N27	PCIE_TX11N
PCIE_TXP12	C112	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP12	P26	PCIE_TX12P
PCIE_TXN12	C105	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN12	N26	PCIE_TX12N
PCIE_TXP13	C102	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP13	L26	PCIE_TX13P
PCIE_TXN13	C101	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN13	K25	PCIE_TX13N
PCIE_TXP14	C99	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP14	L27	PCIE_TX14P
PCIE_TXN14	C93	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN14	K27	PCIE_TX14N
PCIE_TXP15	C92	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXP15	L26	PCIE_TX15P
PCIE_TXN15	C84	1	2	0.1U_0402_16V4Z	VGA_PCIE_TXN15	K26	PCIE_TX15N



Reserved for EC to monitor VGA temprature.



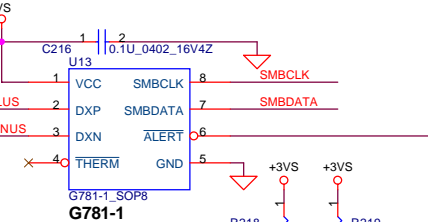
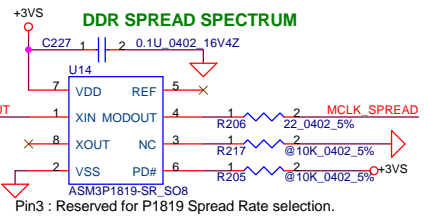
VGA_PCIE_RXP0	AH30	PCIE_RX0P
VGA_PCIE_RXN0	AG30	PCIE_RX0N
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VGA_PCIE_RXN1	AF29	PCIE_RX1N
VGA_PCIE_RXP2	AE29	PCIE_RX2P
VGA_PCIE_RXN2	AE30	PCIE_RX2N
VGA_PCIE_RXP3	AD30	PCIE_RX3P
VGA_PCIE_RXN3	AD29	PCIE_RX3N
VGA_PCIE_RXP4	AC29	PCIE_RX4P
VGA_PCIE_RXN4	AB29	PCIE_RX4N
VGA_PCIE_RXP5	AB30	PCIE_RX5P
VGA_PCIE_RXN5	AA30	PCIE_RX5N
VGA_PCIE_RXP6	AA29	PCIE_RX6P
VGA_PCIE_RXN6	Y29	PCIE_RX6N
VGA_PCIE_RXP7	W29	PCIE_RX7P
VGA_PCIE_RXN7	W30	PCIE_RX7N
VGA_PCIE_RXP8	V30	PCIE_RX8P
VGA_PCIE_RXN8	V29	PCIE_RX8N
VGA_PCIE_RXP9	T29	PCIE_RX9P
VGA_PCIE_RXN9	T30	PCIE_RX9N
VGA_PCIE_RXP10	R30	PCIE_RX10P
VGA_PCIE_RXN10	R29	PCIE_RX10N
VGA_PCIE_RXP11	P29	PCIE_RX11P
VGA_PCIE_RXN11	P29	PCIE_RX11N
VGA_PCIE_RXP12	N29	PCIE_RX12P
VGA_PCIE_RXN12	M30	PCIE_RX12N
VGA_PCIE_RXP13	M30	PCIE_RX13P
VGA_PCIE_RXN13	M29	PCIE_RX13N
VGA_PCIE_RXP14	L29	PCIE_RX14P
VGA_PCIE_RXN14	K29	PCIE_RX14N
VGA_PCIE_RXP15	K30	PCIE_RX15P
VGA_PCIE_RXN15	J30	PCIE_RX15N



Video Memory Config. Delete R416, R417

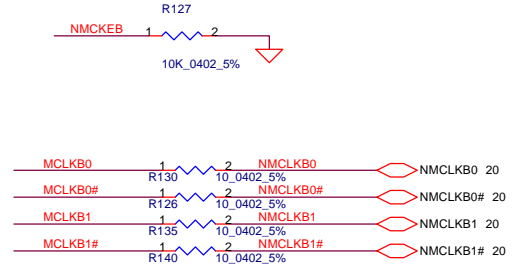
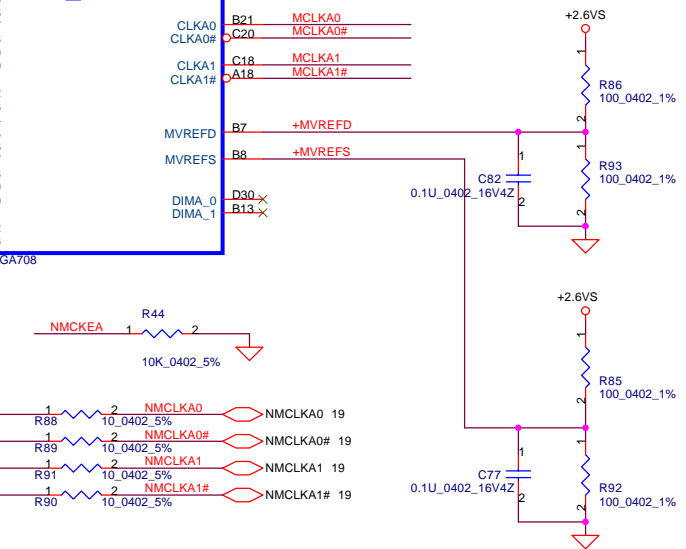
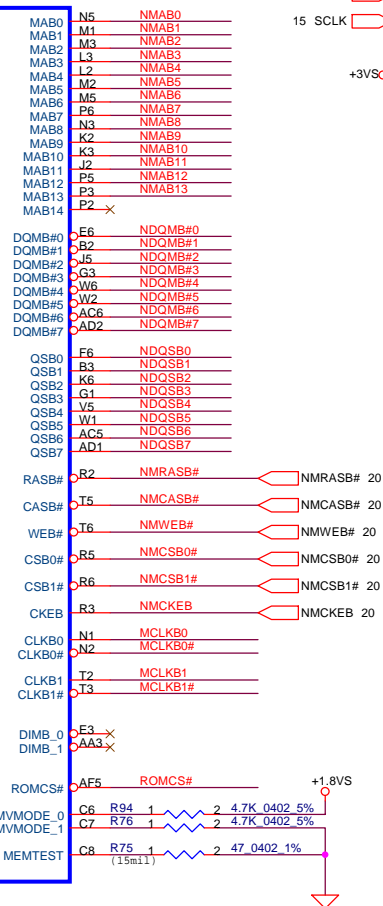
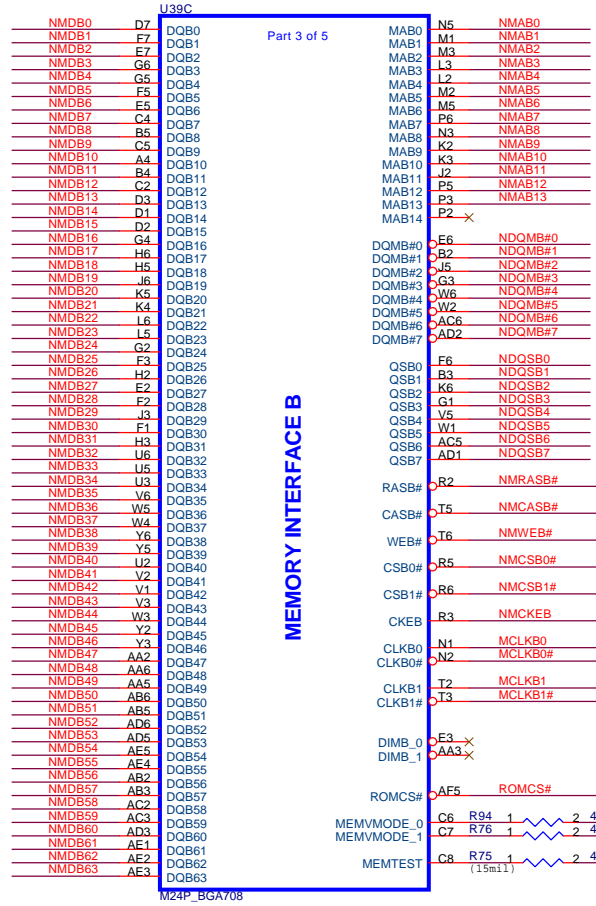
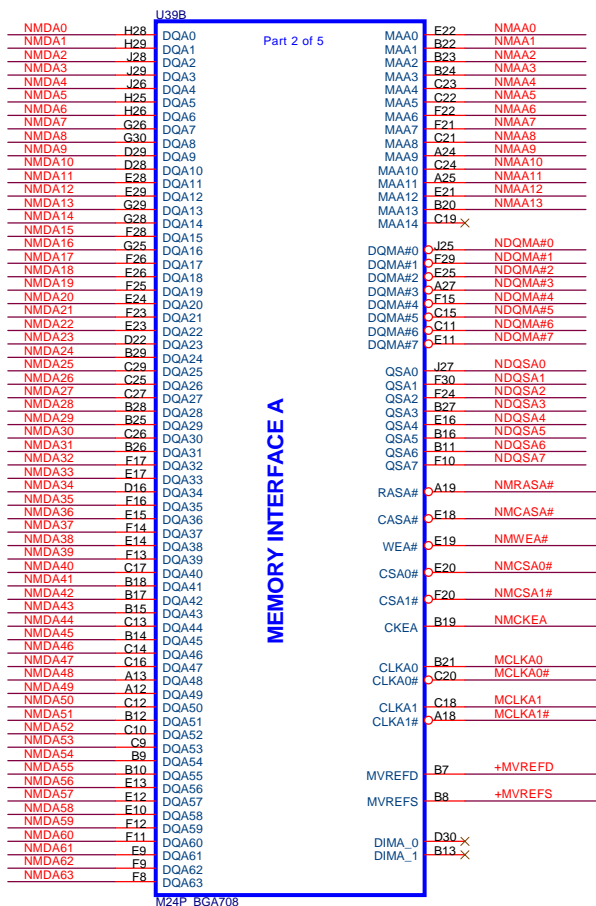
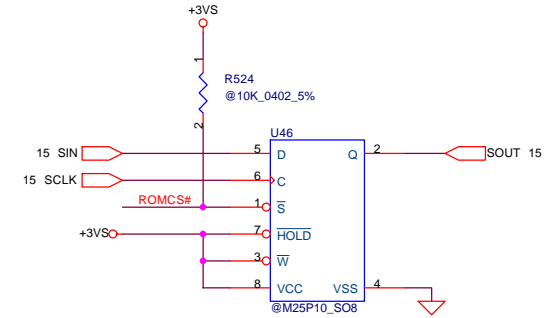
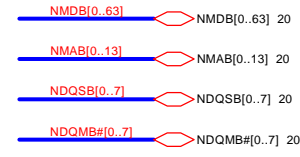
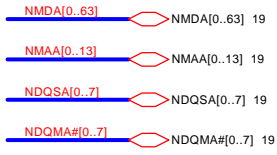
MEM_ID0	MEM_ID1	Size	Vendor
0	0	64MB	4Mx32 Samsung
0	1	64MB	4Mx32 Hynix
1	0	128MB	8Mx32 Samsung
1	1	128MB	8Mx32 Hynix

GPIO0	CAL_BG_BACKUP	DEFAULT: 0
GPIO1	PLL_CAL_FORCE_EN	DEFAULT: 0
GPIO(3:2)	PCIE_MODE(1:0)	DEFAULT: 00
GPIO4	CAL_OFF	DEFAULT: 0
GPIO5	BYPASS_PLL	DEFAULT: 0
GPIO6	ICOMP	DEFAULT: 0



Compal Electronics, Inc.			
Title	M24-P		
Size	Document Number	Rev	1A
Customer	ECQ60 LA-2271		
Date:	Monday, August 09, 2004	Sheet	15 of 54

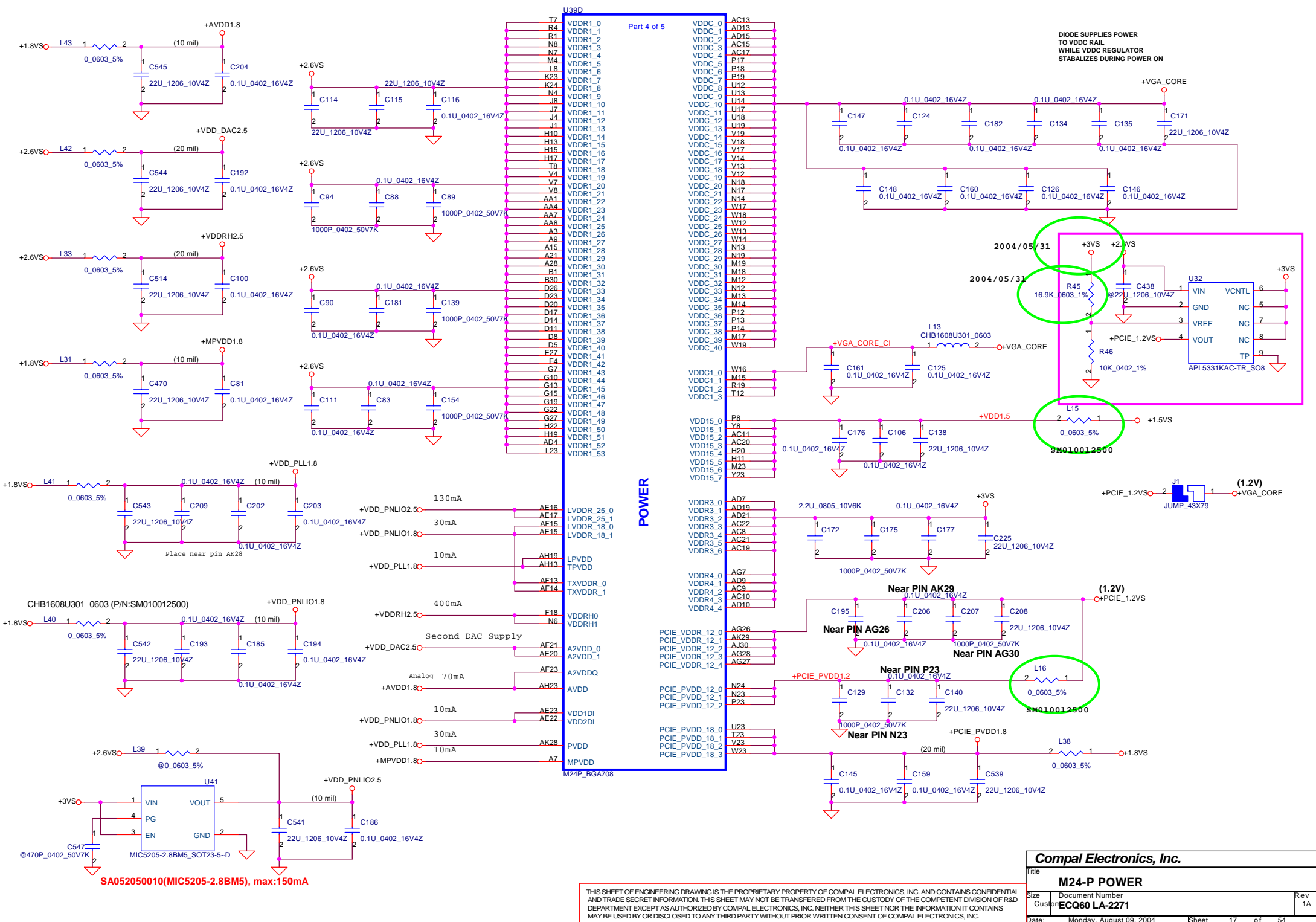
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MEM IO Voltage Selection

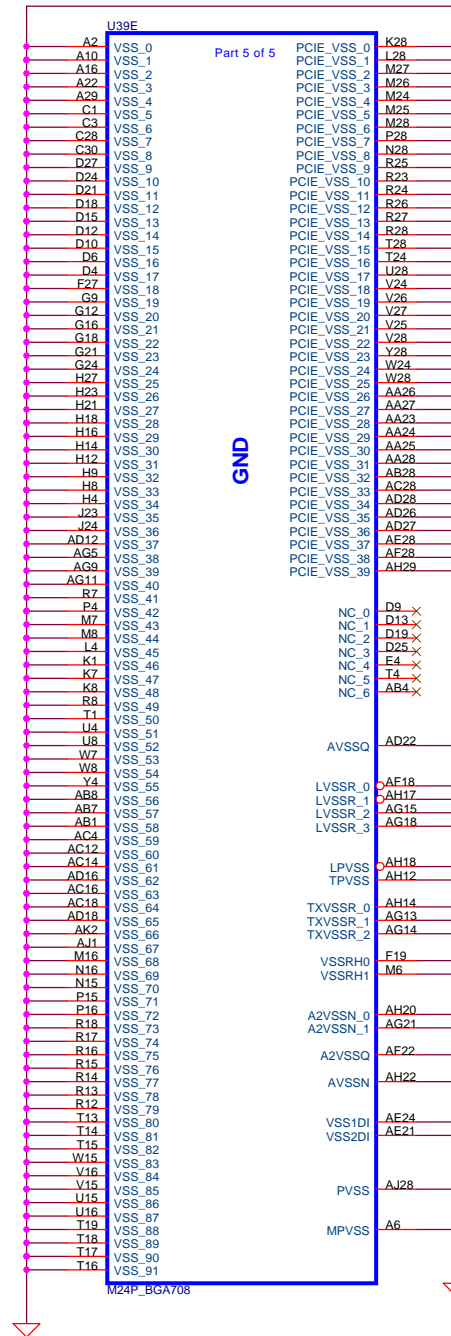
	2.5V VDDR1	1.8V VDDR1 (ELPIDA)
MEMVMODE0	HI	LOW
MEMVMODE1	LOW	HI

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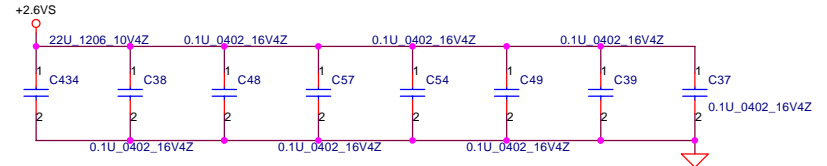
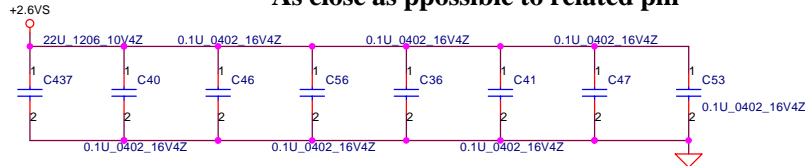
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Title	M24-P POWER	
Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 17 of 54



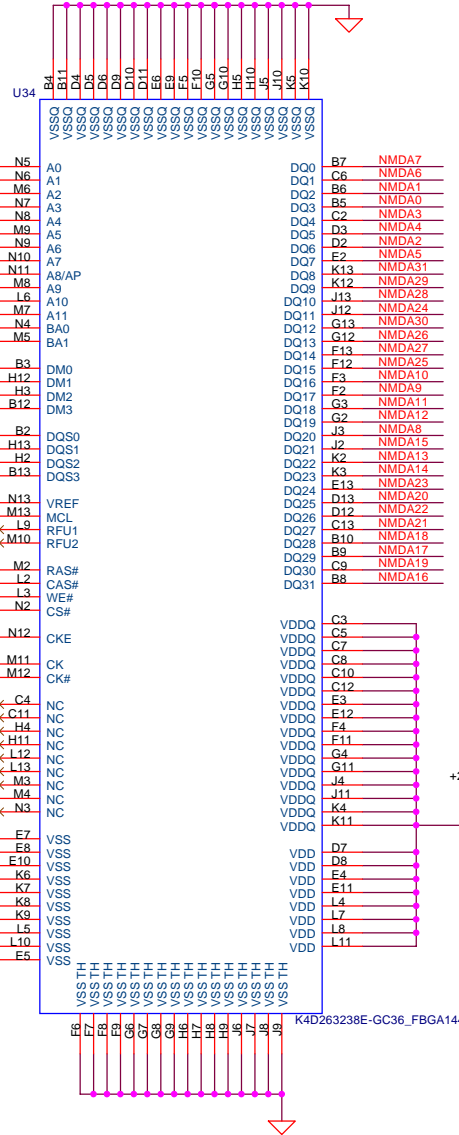
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Compal Electronics, Inc.		
Title		
M24-P GND		
Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 18 of 54

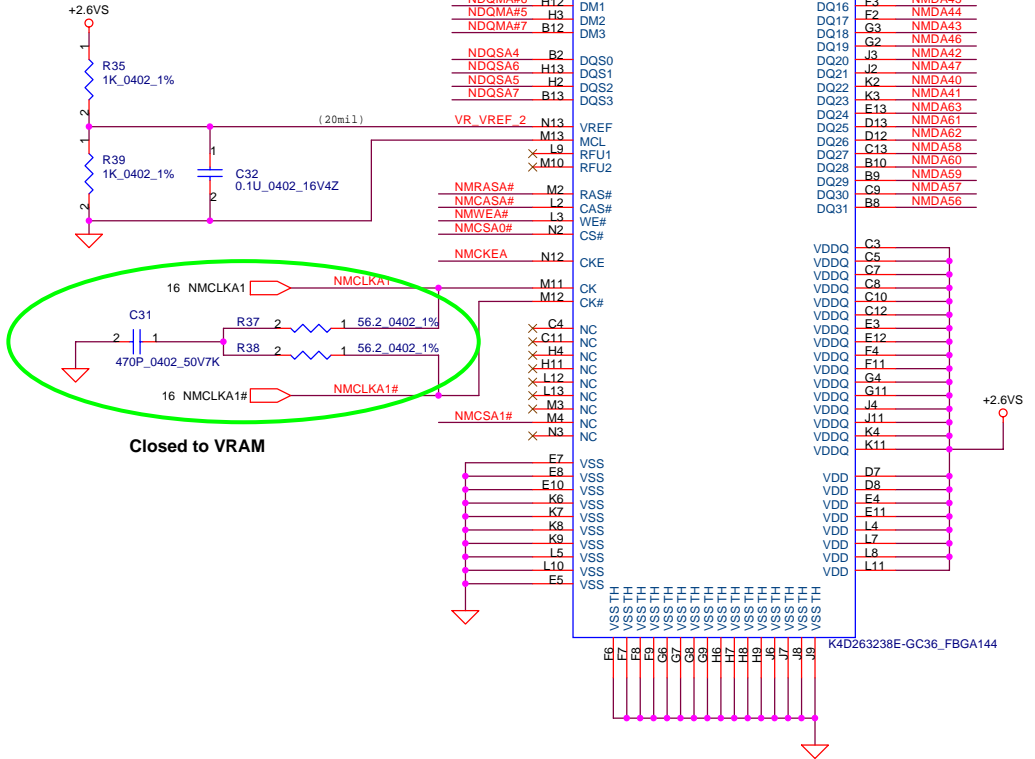
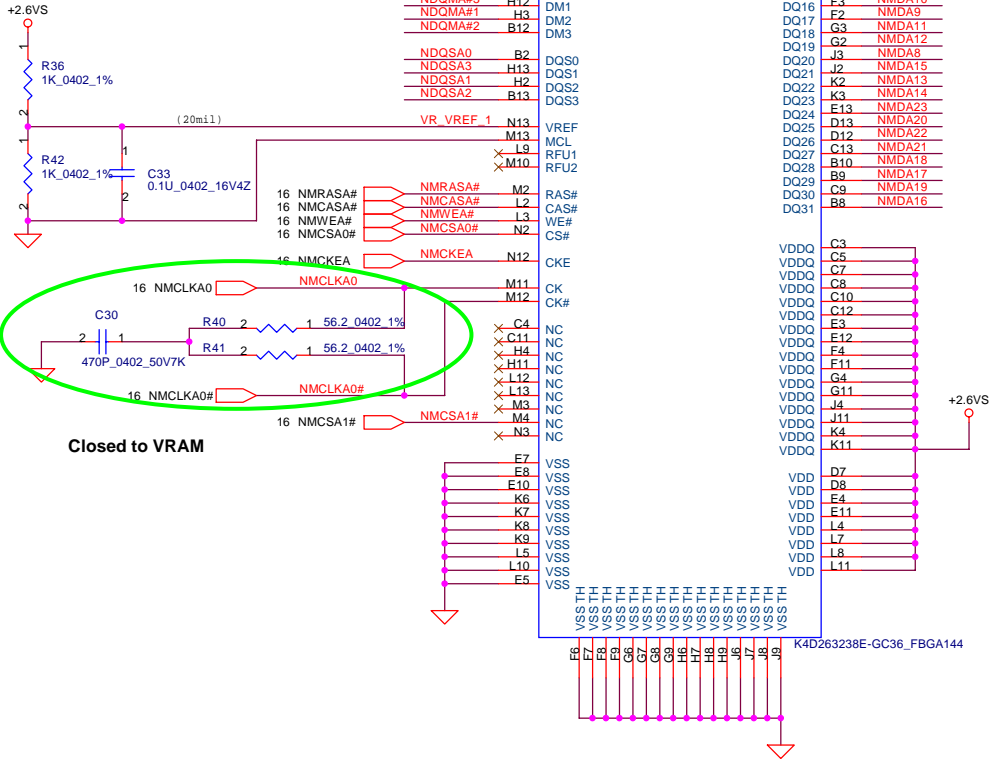
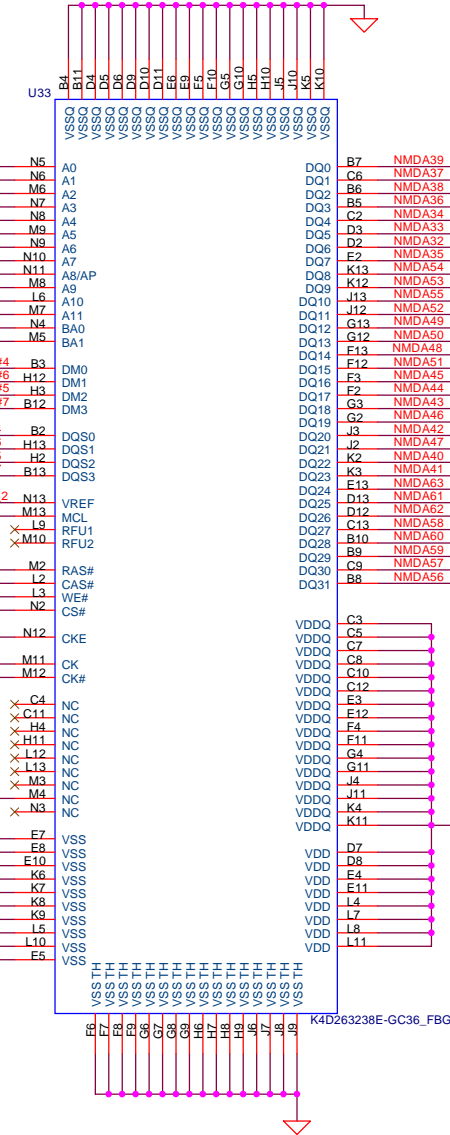
As close as possible to related pin



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- 16 NDQSA#[0..7] \rightarrow NDQSA#[0..7]
- 16 NMAA#[0..13] \rightarrow NMAA#[0..13]
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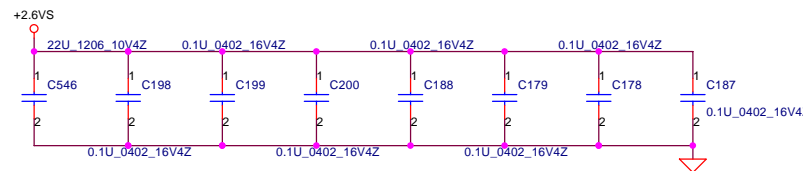
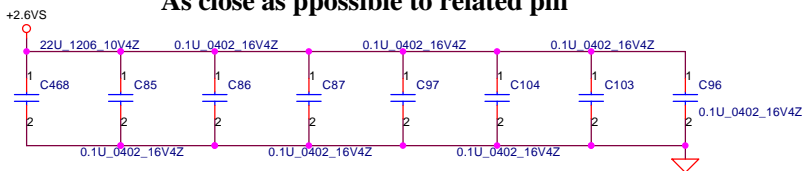
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- 16 NMMA#[0..63] \rightarrow NMMA#[0..63]



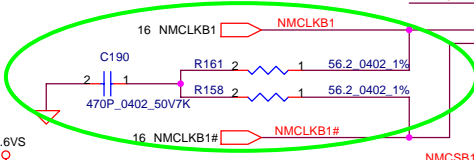
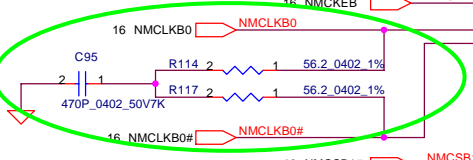
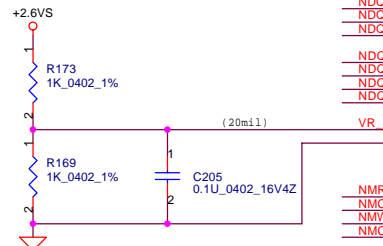
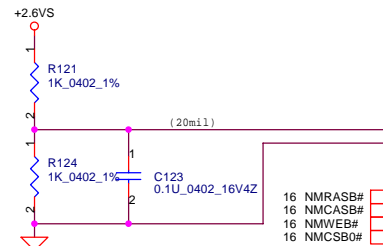
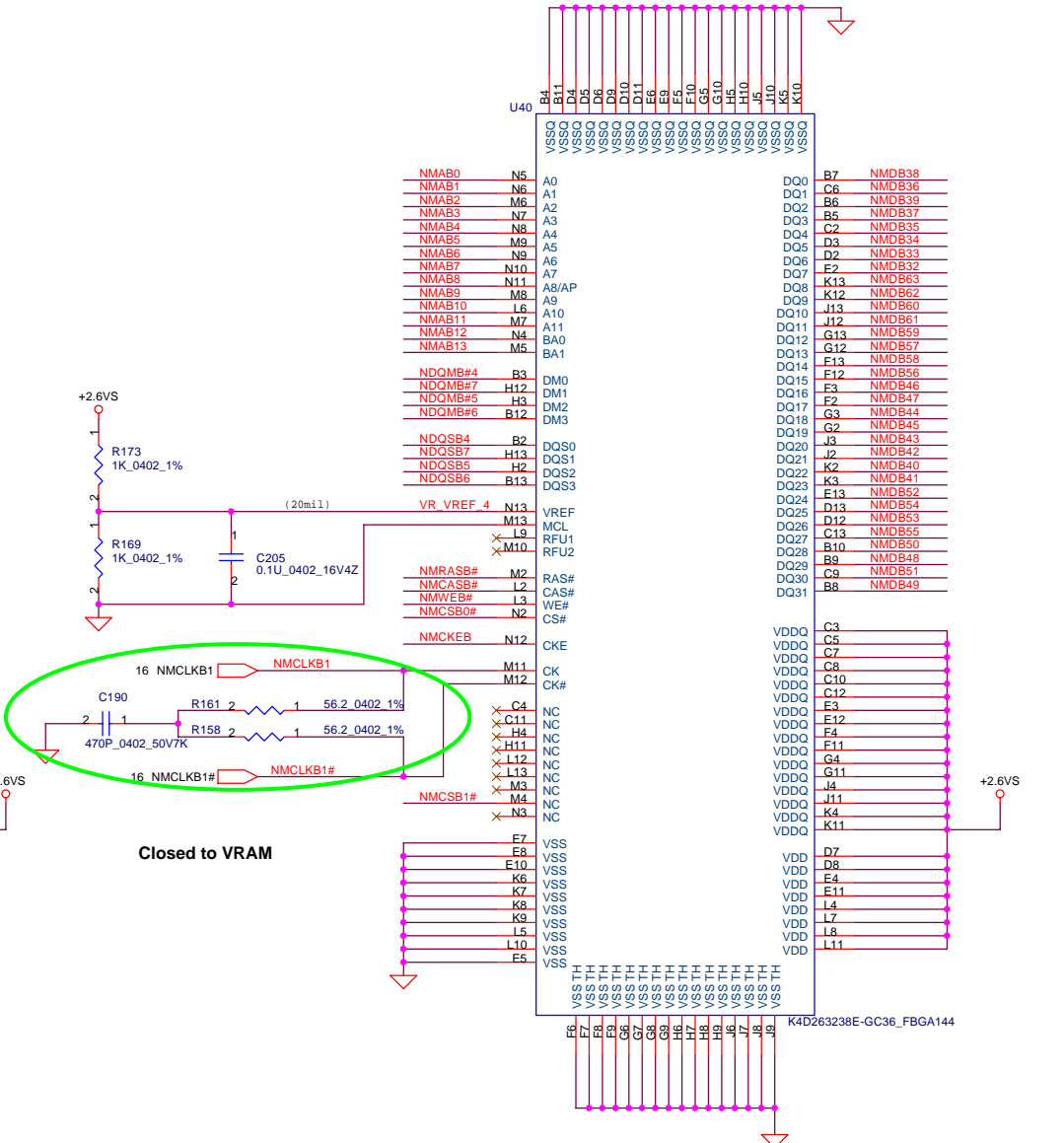
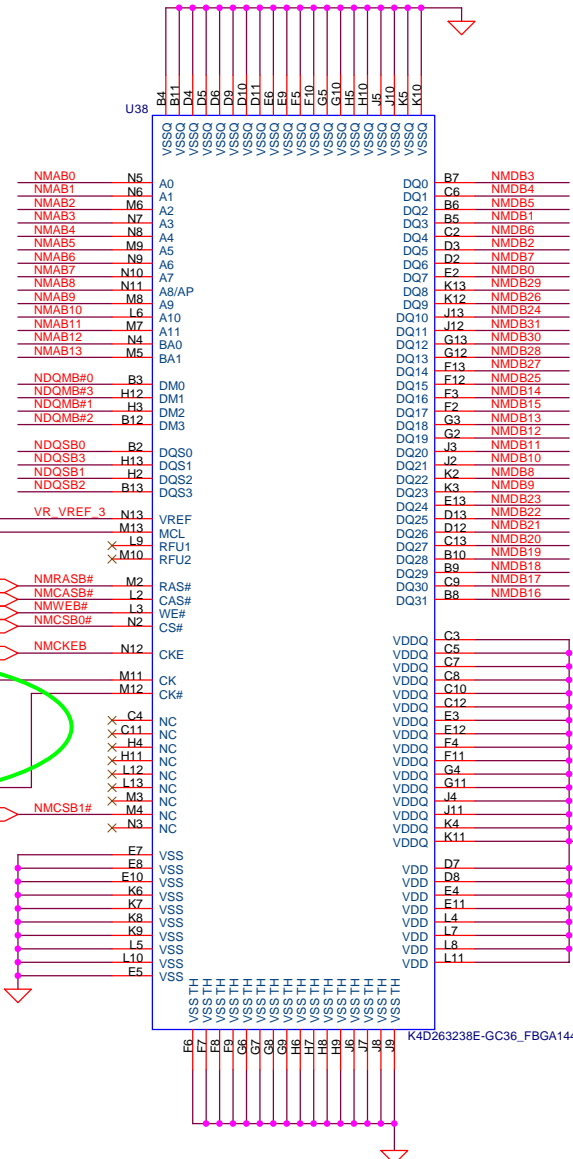
Compal Electronics, Inc.		
Title: GRAPHIC DDR CHANNEL A		
Size: Custom	Document Number: ECCQ60 LA-2271	Rev: 1A
Date: Monday, August 09, 2004	Sheet: 19	of 54

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As close as possible to related pin



- 16 NDQMB#[0..7] NDQMB#[0..7]
- 16 NDQSB#[0..7] NDQSB#[0..7]
- 16 NMAB#[0..13] NMAB#[0..13]
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Closed to VRAM

Closed to VRAM

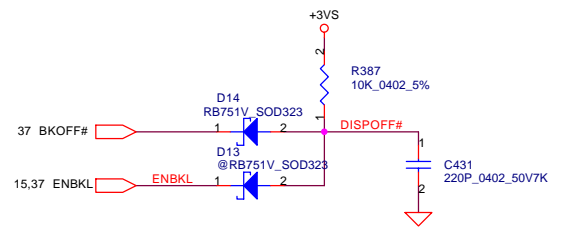
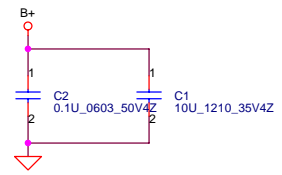
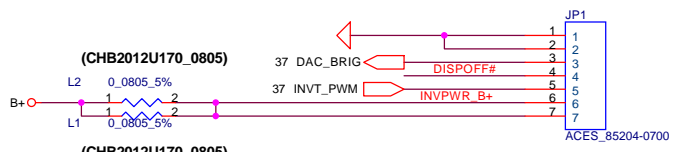
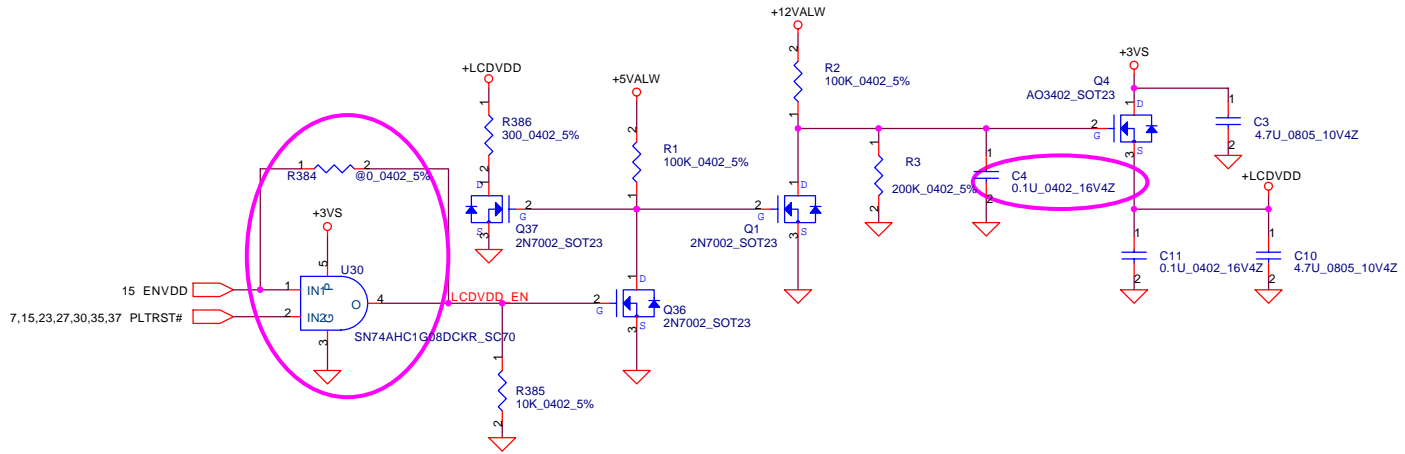
Compal Electronics, Inc.

Title: **GRAPHIC DDR CHANNEL B**

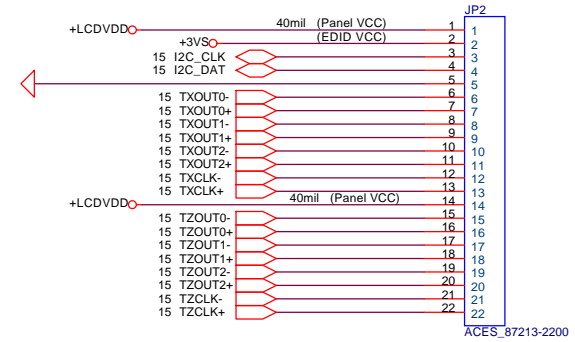
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Date: **Monday, August 09, 2004** Sheet: **20** of **54**

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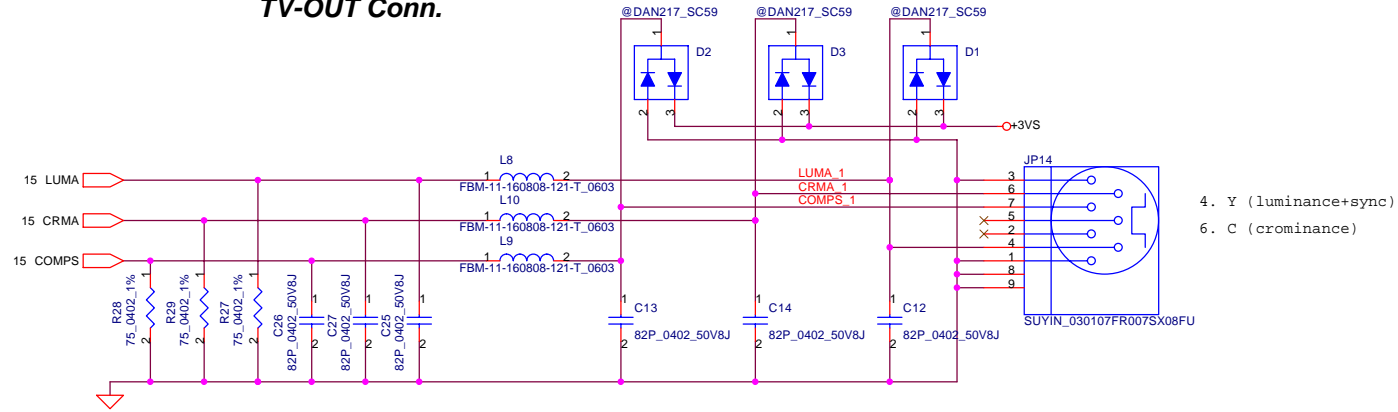
LVDS Conn.



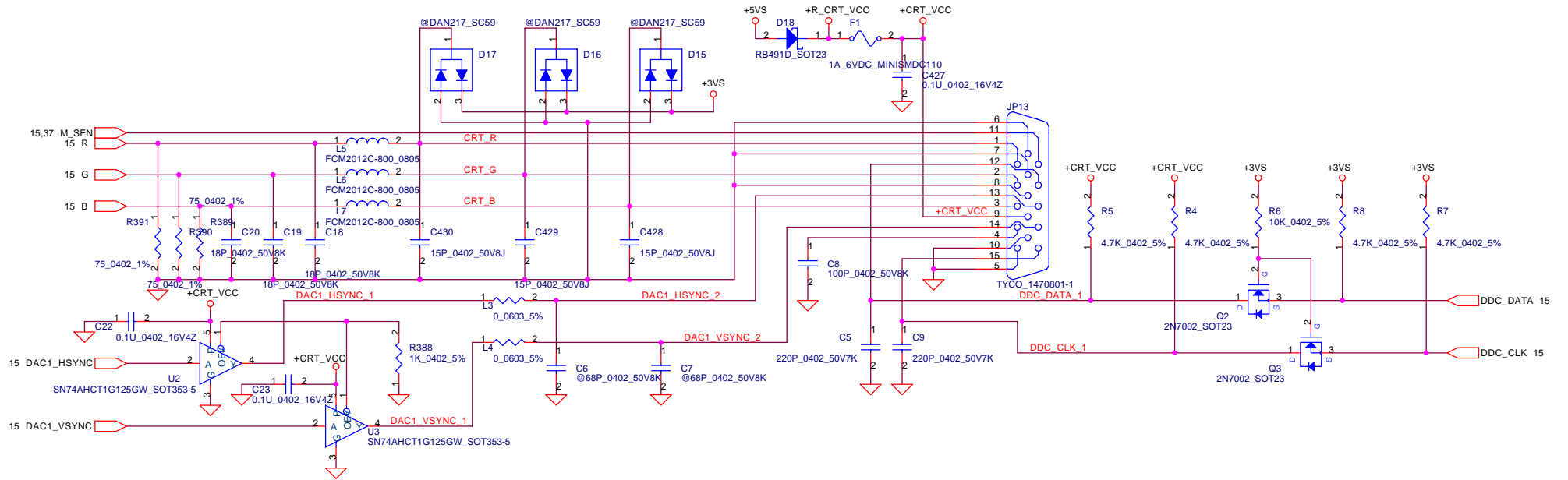
Compal Electronics, Inc.		
Title		
LVDS CONNECTOR		
Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 21 of 54

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TV-OUT Conn.



CRT Conn.



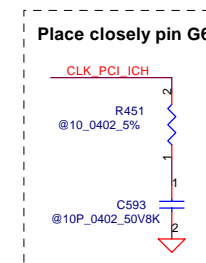
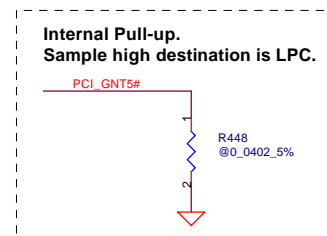
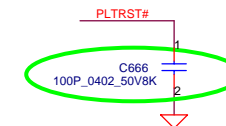
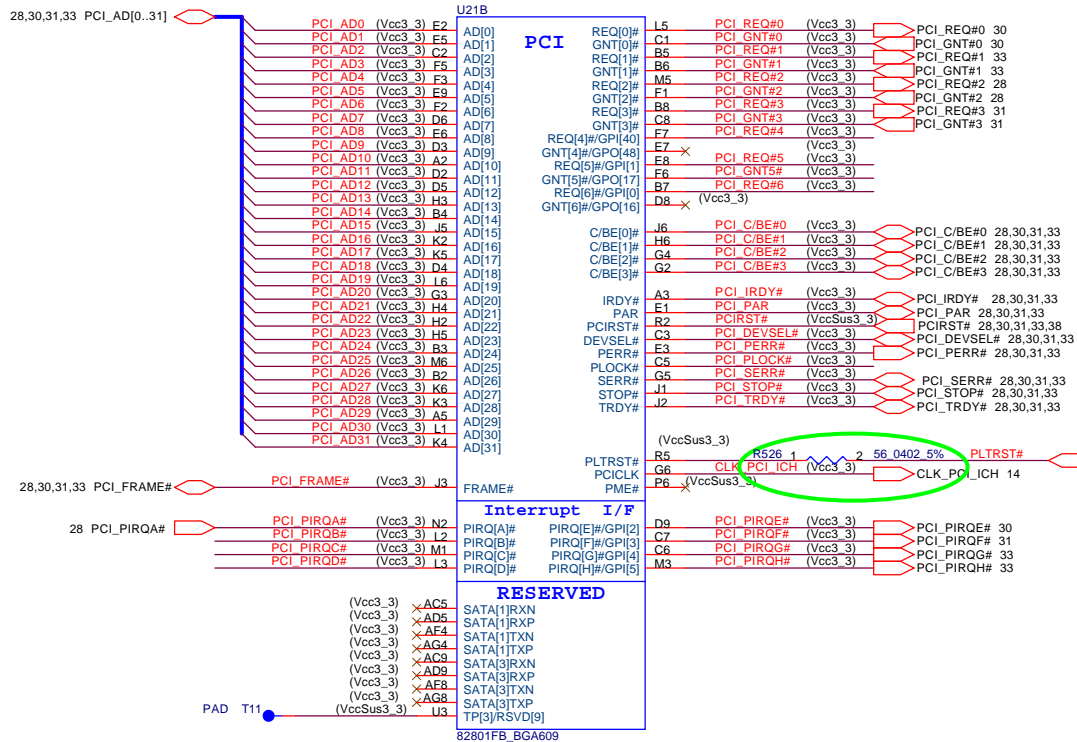
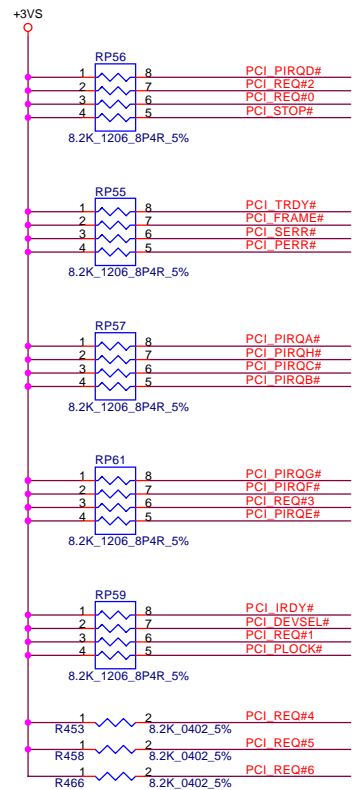
Compal Electronics, Inc.

Title
CRT, TV-OUT CONNECTOR

Size Document Number
Customer **ECQ60 LA-2271** Rev
1A

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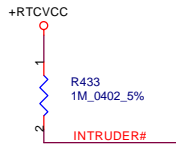


Compal Electronics, Inc.

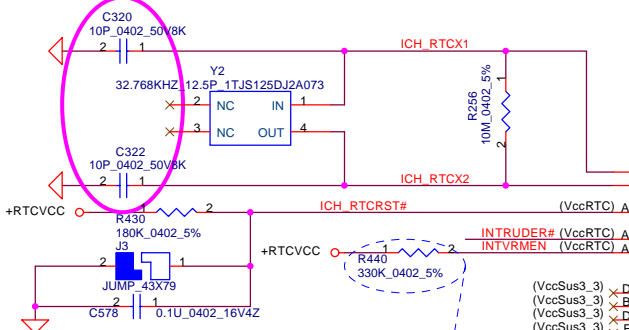
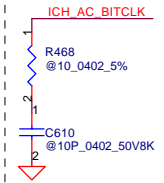
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 Date: Monday, August 09, 2004 Sheet 23 of 54

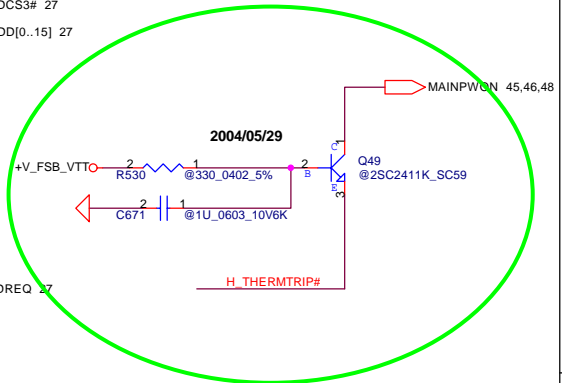
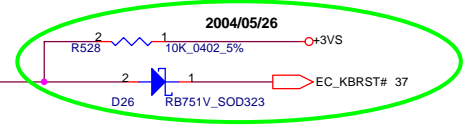
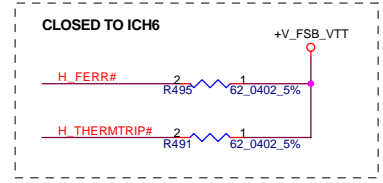
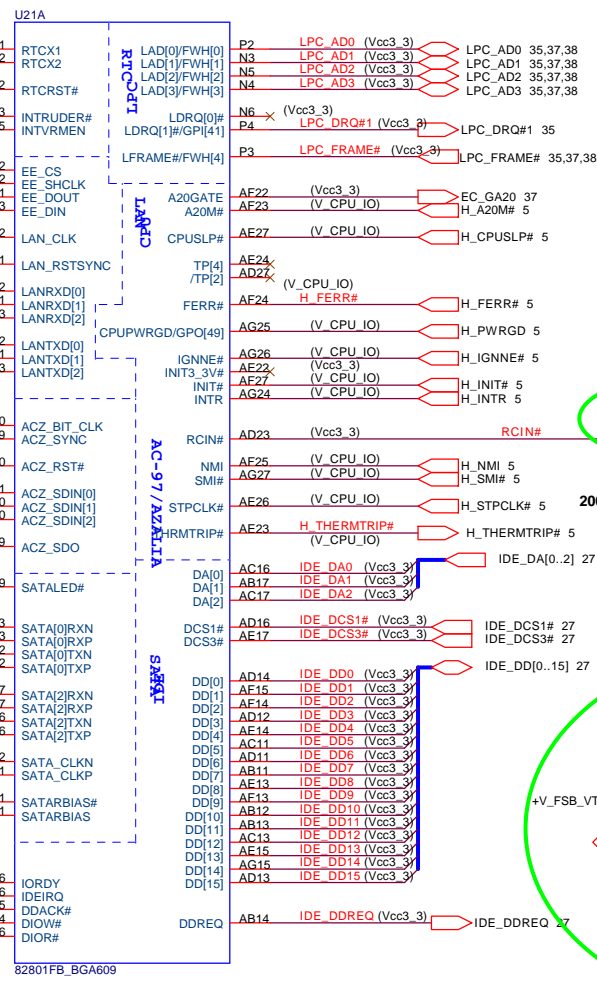
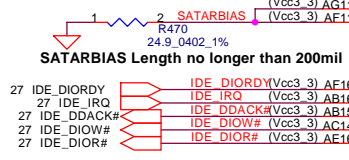
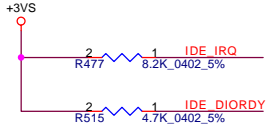
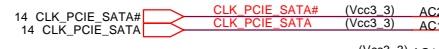
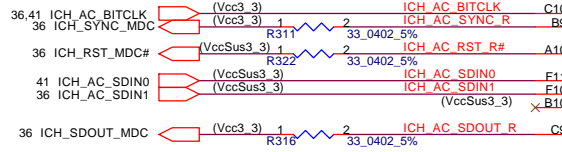
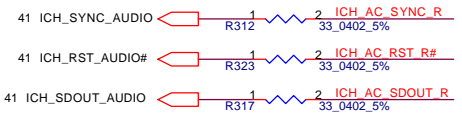
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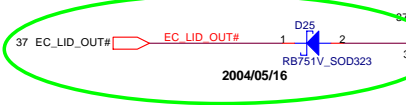
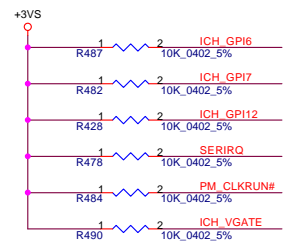
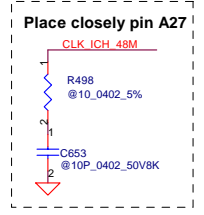
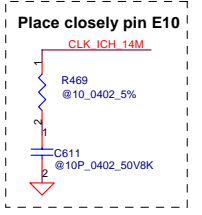
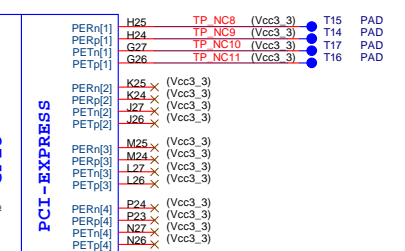
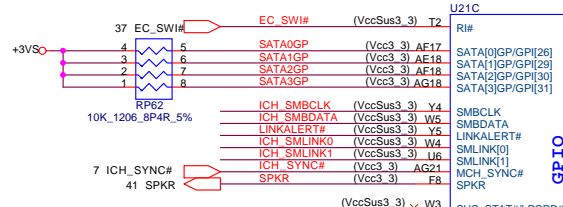
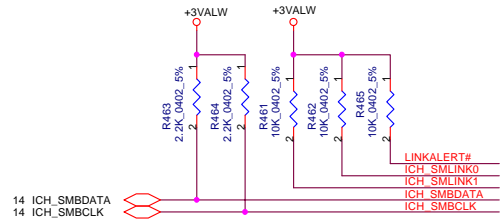
Place closely pin C10



INTVRMEN	
HIGH	ENABLE INTEGRATED VCCSUS1.5 VRM
LOW	DISABLE INTEGRATED VCCSUS1.5 VRM



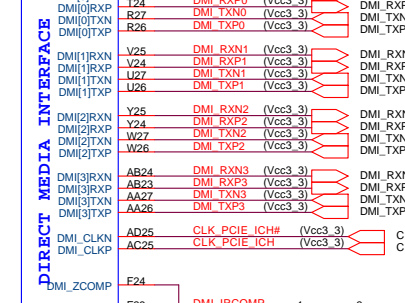
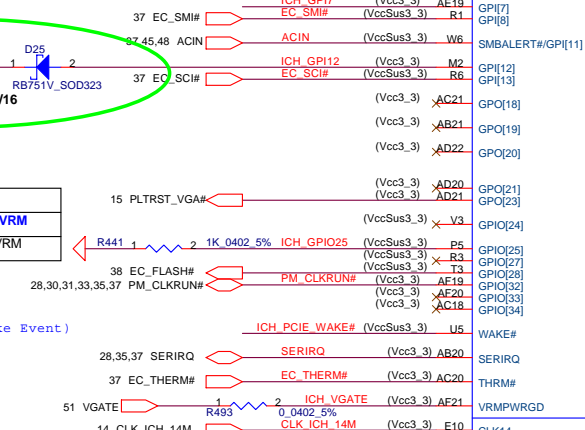
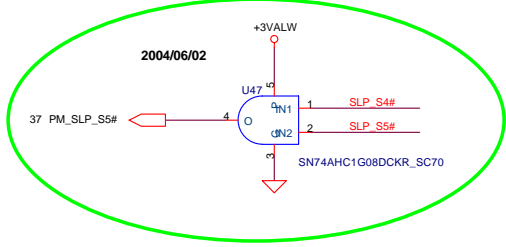
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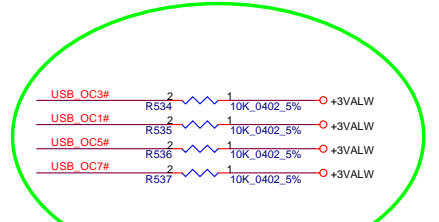
GPIO[25]	
LOW	ENABLE INTEGRATED VCC2_5_VRM
HIGH	DISABLE INTEGRATED VCC2_5_VRM

(PCI Express Wake Event)

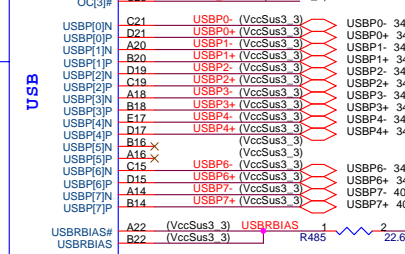
If PCI Express Port(s) will not be implemented on the platform:
 PETp/n[x] and PERp/n[x] signals may be left unconnected.
 Pull-up Wake# to VccSus3.3 via a 10KOhm resistor.
 Grantsdale Chipset Platform Design Guide REV1.0 REF. 14652 Page167



DMI Interface trace length maximum is 11 inched long.

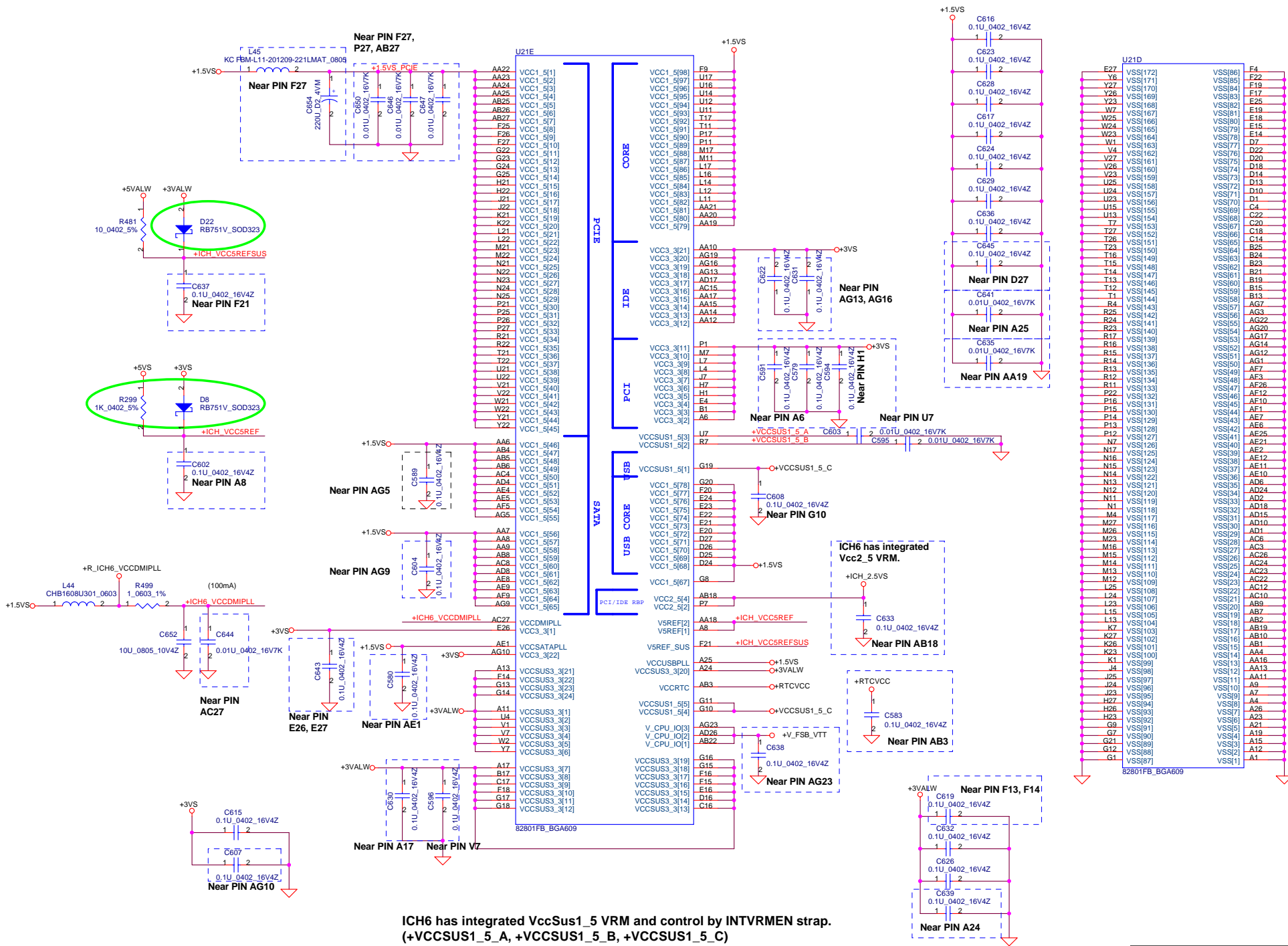


Delete RP62, RP64



USBBIAS Length no longer than 200mil

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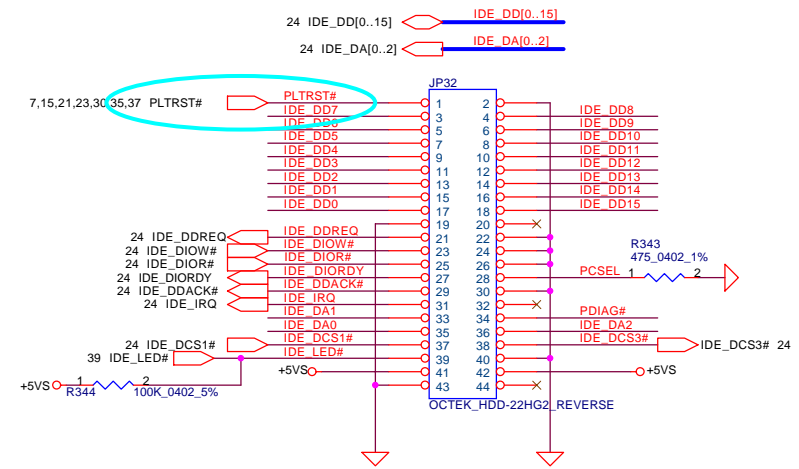


ICH6 has integrated VccSus1_5 VRM and control by INTVRMEN strap.
 (+VCCSUS1_5_A, +VCCSUS1_5_B, +VCCSUS1_5_C)

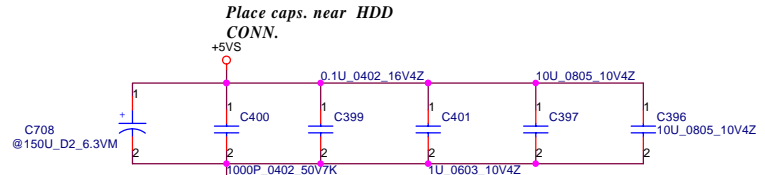
Compal Electronics, Inc. Title: ICH6(4/4)		Size: Document Number Customer: ECQ60 LA-2271	Rev: 1A
Date:	Monday, August 09, 2004	Sheet:	26 of 54

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HDD Connector

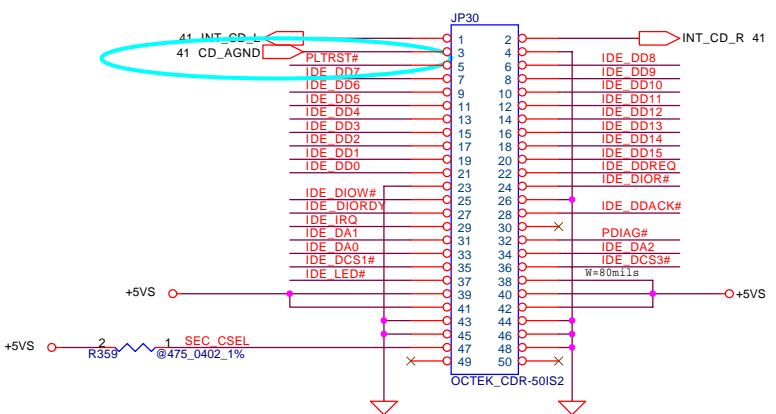


When Pin28 CSEL grounded,
the device recognizes itself as a master.

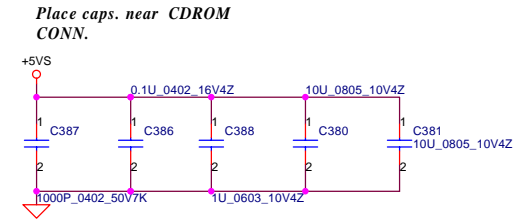


Place caps. near HDD
CONN.

CD-ROM Connector



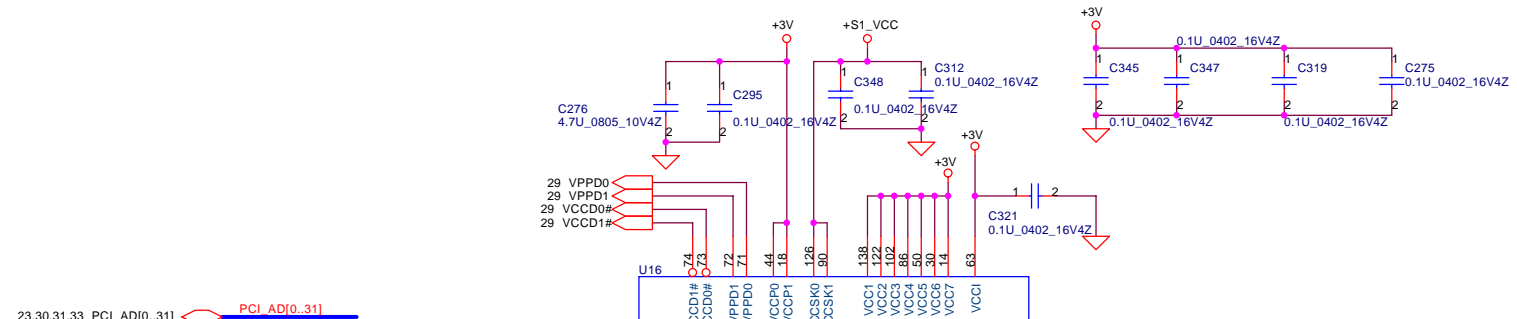
When Pin47 CSEL grounded,
the device recognizes itself as a master.
When CSEL open,
the device recognizes itself as a slaver.



Place caps. near CDROM
CONN.

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Compal Electronics, Inc.		
Title IDE CONNECTOR		
Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 27 of 54

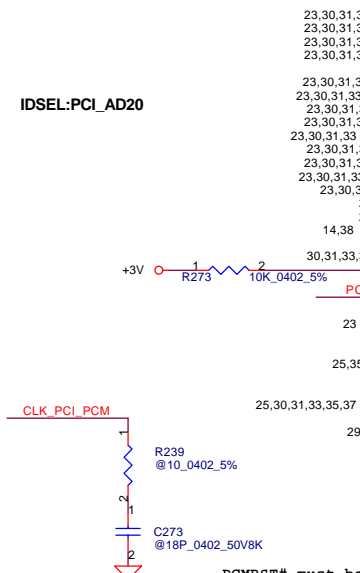


23.30,31,33 PCI_AD[0..31] PCI_AD[0..31]

PQFP 144
22.2 X 22.2 X 1.60

PCI_AD31	3	AD31	CAD31/D10	144	S1_D10
PCI_AD30	4	AD30	CAD30/D9	142	S1_D9
PCI_AD29	5	AD29	CAD29/D1	141	S1_D1
PCI_AD28	7	AD28	CAD28/D8	140	S1_D8
PCI_AD27	8	AD27	CAD27/D0	139	S1_D0
PCI_AD26	9	AD26	CAD26/A0	129	S1_A0
PCI_AD25	10	AD25	CAD25/A1	128	S1_A1
PCI_AD24	11	AD24	CAD24/A2	127	S1_A2
PCI_AD23	15	AD24	CAD24/A3	124	S1_A3
PCI_AD22	16	AD23	CAD23/A3	121	S1_A4
PCI_AD21	17	AD22	CAD22/A4	120	S1_A5
PCI_AD20	19	AD21	CAD21/A5	118	S1_A6
PCI_AD19	23	AD19	CAD19/A25	116	S1_A25
PCI_AD18	24	AD18	CAD18/A7	115	S1_A7
PCI_AD16	26	AD17	CAD17/A24	113	S1_A24
PCI_AD15	38	AD16	CAD17/A24	98	S1_A17
PCI_AD14	39	AD15	CAD16/A17	96	S1_IOWR#
PCI_AD13	40	AD14	CAD15/IOWR#	97	S1_A9
PCI_AD12	41	AD13	CAD14/A9	93	S1_IORD#
PCI_AD11	43	AD12	CAD13/IORD#	95	S1_A11
PCI_AD10	46	AD11	CAD12/A11	92	S1_OE#
PCI_AD9	47	AD10	CAD11/OE#	91	S1_CE2#
PCI_AD8	49	AD9	CAD10/CE2#	89	S1_A10
PCI_AD7	51	AD8	CAD9/A10	87	S1_D15
PCI_AD6	52	AD7	CAD8/D15	85	S1_D7
PCI_AD5	53	AD6	CAD7/D7	82	S1_D13
PCI_AD4	54	AD5	CAD6/D13	83	S1_D6
PCI_AD3	55	AD4	CAD5/D6	80	S1_D12
PCI_AD2	56	AD3	CAD4/D12	81	S1_D5
PCI_AD1	57	AD2	CAD3/D5	77	S1_D11
PCI_AD0	57	AD0	CAD2/D11	79	S1_D4
			CAD1/D4	76	S1_D3
			CAD0/D3		

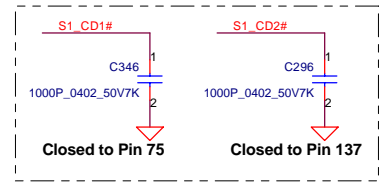
S1_A[0..25] S1_A[0..25] 29
S1_D[0..15] S1_D[0..15] 29



PCMRST# must be programmed to high before PCIRST#.
PCMRST# will be programmed to high after SUSP# but before PCIRST#.

CB1410_LQFP144

CC/BE3#/REG#	125	S1_REG#	S1_REG# 29
CC/BE2#/A12	112	S1_A12	
CC/BE1#/A8	99	S1_A8	
CC/BE0#/CE1#	88	S1_CE1#	S1_CE1# 29
CRST#/RESET	119	S1_RST	S1_RST 29
CFRAME#/A23	111	S1_A23	
CIRDY#/A15	109	S1_A22	
CTRDY#/A22	107	S1_A21	
ODEVSEL#/A21	105	S1_A20	
CSTOP#/A20	104	S1_A14	
CPERR#/A14	101	S1_WAIT#	S1_WAIT# 29
CSERR#/WAIT#	101	S1_A13	
CPAR/A13	101	S1_INPACK#	S1_INPACK# 29
CREQ#/INPACK#	102	S1_WE#	S1_WE# 29
CGNT#/WE#	108	A16_CLK	S1_A16
CCLK/A16	1	33_0402_5%	
CSTSCHG/BVD1	135	S1_BVD1	S1_BVD1 29
CCLKRUN#/W/P	136	S1_WP	S1_WP 29
CBLOCK#/A19	103	S1_A19	
CINT#/READY	132	S1_RDY#	S1_RDY# 29
SPKOUT	62	PCM_SPK#	PCM_SPK# 41
CAUDIO/BVD2	134	S1_BVD2	S1_BVD2 29
CCD2#/CD2#	137	S1_CD2#	S1_CD2# 29
CCD1#/CD1#	137	S1_CD1#	S1_CD1# 29
CVS2/VS2#	117	S1_VS2	S1_VS2 29
CVS1/VS1#	131	S1_VS1	S1_VS1 29
		S1_D2	
		S1_A18	
		S1_D14	



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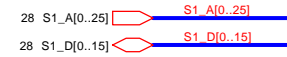
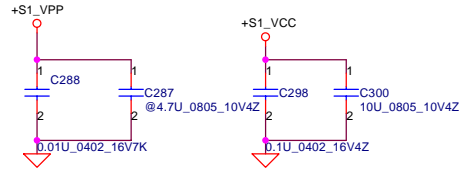
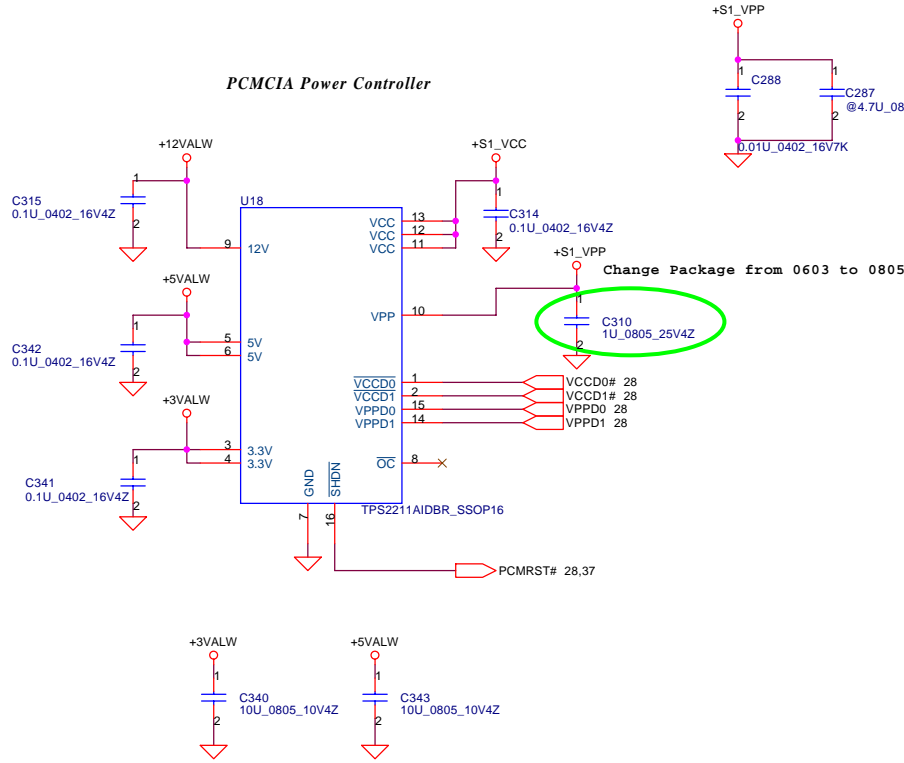
Compal Electronics, Inc.

Title: **PCMCIA controller ENE CB1410**

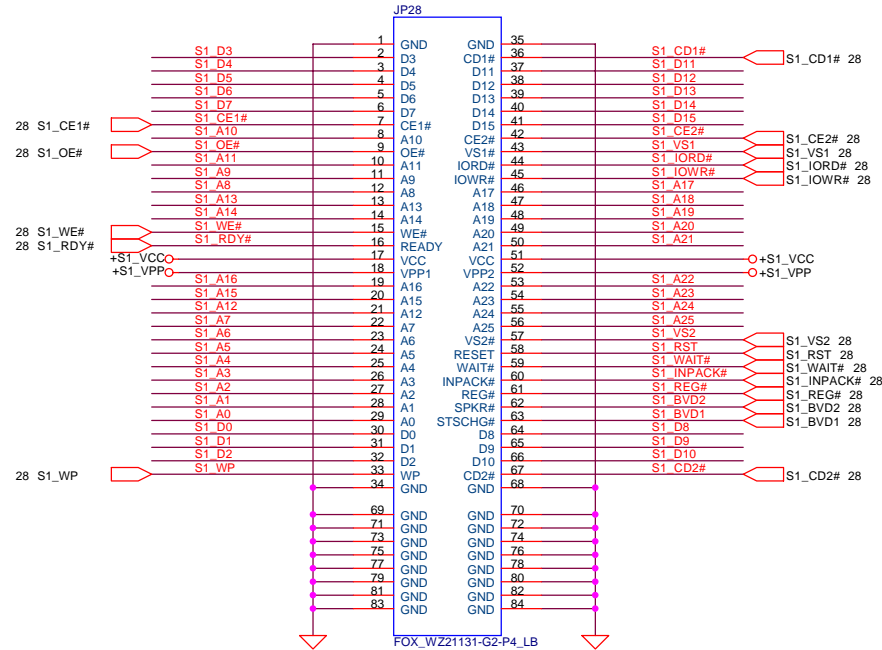
Size: Document Number
Customer: **ECQ60 LA-2271** Rev: 1A

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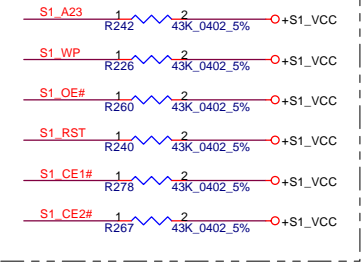
PCMCIA Power Controller



CardBus Socket



For CB1410 Rev.B0 (Place close to Connector)



SP01F002800(CL51/AT20)
PCB Footprint : FOX_1CA43532-TC-CQ_84P_RB

Compal Electronics, Inc.

Title: **CardBus Socket**

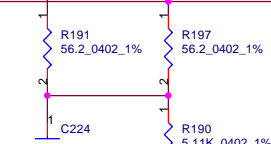
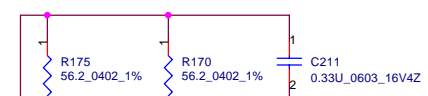
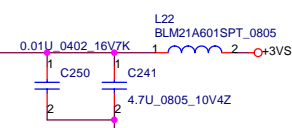
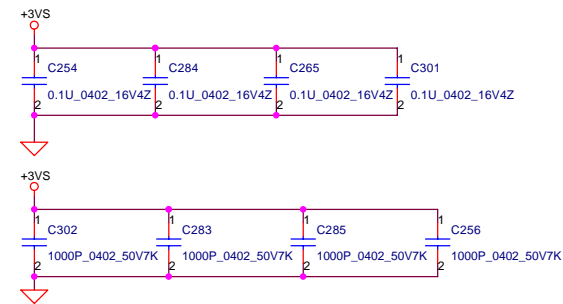
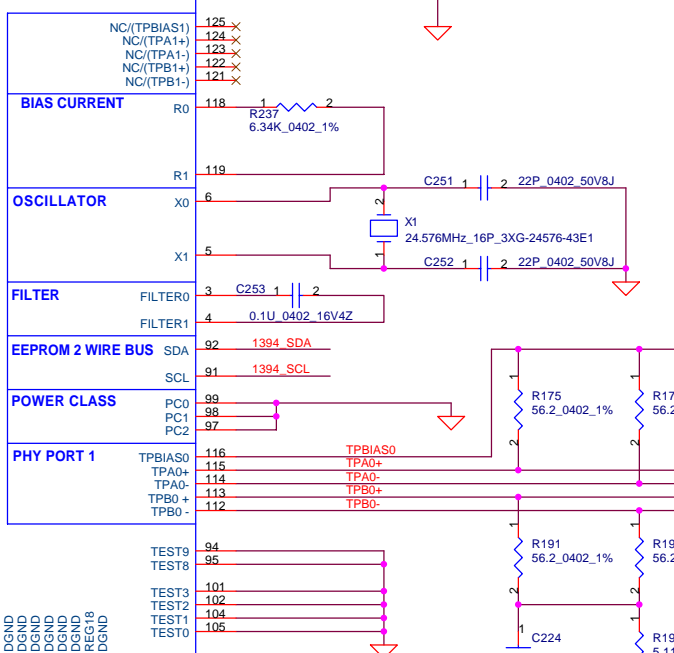
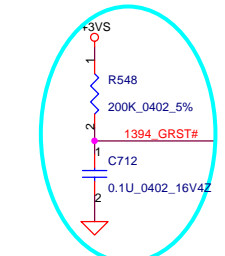
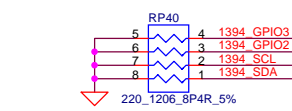
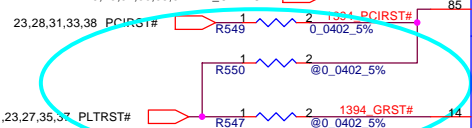
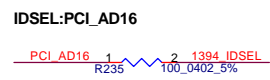
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Document Number: **ECC60 LA-2271**
Date: Monday, August 09, 2004
Sheet: 29 of 54
Rev: 1A

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TSB43AB21 (TSB43AB22)

PCI BUS INTERFACE

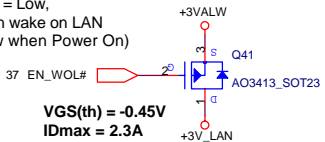
PCI_AD0	84	PCI_AD0
PCI_AD1	82	PCI_AD1
PCI_AD2	81	PCI_AD2
PCI_AD3	80	PCI_AD3
PCI_AD4	79	PCI_AD4
PCI_AD5	77	PCI_AD5
PCI_AD6	76	PCI_AD6
PCI_AD7	74	PCI_AD7
PCI_AD8	71	PCI_AD8
PCI_AD9	70	PCI_AD9
PCI_AD10	69	PCI_AD10
PCI_AD11	67	PCI_AD11
PCI_AD12	66	PCI_AD12
PCI_AD13	65	PCI_AD13
PCI_AD14	63	PCI_AD14
PCI_AD15	61	PCI_AD15
PCI_AD16	46	PCI_AD16
PCI_AD17	45	PCI_AD17
PCI_AD18	43	PCI_AD18
PCI_AD19	42	PCI_AD19
PCI_AD20	41	PCI_AD20
PCI_AD21	40	PCI_AD21
PCI_AD22	38	PCI_AD22
PCI_AD23	37	PCI_AD23
PCI_AD24	32	PCI_AD24
PCI_AD25	31	PCI_AD25
PCI_AD26	29	PCI_AD26
PCI_AD27	28	PCI_AD27
PCI_AD28	26	PCI_AD28
PCI_AD29	25	PCI_AD29
PCI_AD30	24	PCI_AD30
PCI_AD31	22	PCI_AD31
PCI_C/BE#3	34	PCI_C/BE#3
PCI_C/BE#2	47	PCI_C/BE#2
PCI_C/BE#1	60	PCI_C/BE#1
PCI_C/BE#0	73	PCI_C/BE#0
CLK_PCI_1394	16	CLK_PCI_1394
PCI_GNT#0	18	PCI_GNT#0
PCI_REQ#0	19	PCI_REQ#0
1394_IDSEL	36	PCI_IDSEL
PCI_FRAME#	49	PCI_FRAME#
PCI_IRDY#	50	PCI_IRDY#
PCI_TRDY#	52	PCI_TRDY#
PCI_DEVSEL#	53	PCI_DEVSEL#
PCI_STOP#	54	PCI_STOP#
PCI_PERR#	56	PCI_PERR#
PCI_PIRQE#	58	PCI_PIRQE#
1394_PME#	21	PCI_PME#
1394_SERR#	57	PCI_SERR#
1394_SDA	92	PCI_SDA
1394_SCL	91	PCI_SCL
25,28,31,33,35,37	12	PCI_CLKRUN
23,28,31,33,35,37	14	PCI_RST
PLLDND1	8	REG_EN
PLLDND2	9	AGND
PLLDND3	10	AGND
PLLDND4	11	AGND
PLLDND5	12	AGND
PLLDND6	13	AGND
PLLDND7	14	AGND
PLLDND8	15	AGND
PLLDND9	16	AGND
PLLDND10	17	AGND
PLLDND11	18	AGND
PLLDND12	19	AGND
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PLLDND20	27	DGND
PLLDND21	28	DGND
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PLLDND23	30	DGND
PLLDND24	31	DGND
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PLLDND70	77	DGND
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PLLDND73	80	DGND
PLLDND74	81	DGND
PLLDND75	82	DGND
PLLDND76	83	DGND
PLLDND77	84	DGND
PLLDND78	85	DGND
PLLDND79	86	DGND
PLLDND80	87	DGND
PLLDND81	88	DGND
PLLDND82	89	DGND
PLLDND83	90	DGND



Compal Electronics, Inc.		
Title: TI TSB43AB21A 1394A CONTROLLER		
Size: Custom	Document Number: ECQ60 LA-2271	Rev: 1A
Date: Monday, August 09, 2004	Sheet: 30	of 54

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EN_WOL# = Low,
System can wake on LAN
(keep Low when Power On)

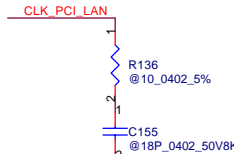


VGS(th) = -0.45V
IDmax = 2.3A

+3VS for BCM5788
+3V_LAN for BCM4401

23,28,30,33 PCI_AD[0..31] <-> PCI_AD[0..31]

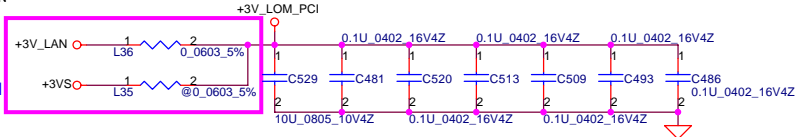
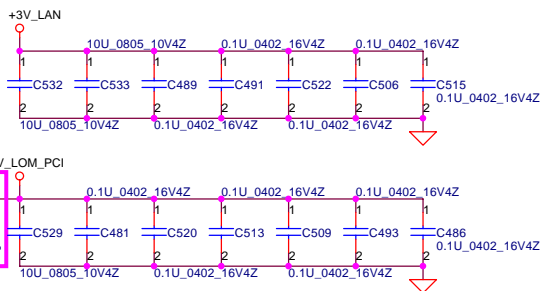
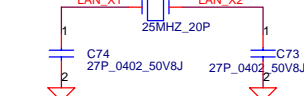
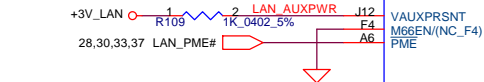
BCM5788:SA057880000
BCM4401:SA044010000



23,28,30,33 PCI_C/BE#3 <-> PCI_C/BE#3 C4
23,28,30,33 PCI_C/BE#2 <-> PCI_C/BE#2 E3
23,28,30,33 PCI_C/BE#1 <-> PCI_C/BE#1 L3
23,28,30,33 PCI_C/BE#0 <-> PCI_C/BE#0 M4

100_0402_5%
PCI_AD17 R139 1 2 LAN_IDSEL A4
23,28,30,33 PCI_FRAME# <-> F1
23,28,30,33 PCI_IRDY# <-> IRDY
23,28,30,33 PCI_TRDY# <-> G3
23,28,30,33 PCI_DEVSEL# <-> H3
23,28,30,33 PCI_STOP# <-> J1
23,28,30,33 PCI_PERR# <-> A2
23,28,30,33 PCI_SERR# <-> SERR
23,28,30,33 PCI_PAR <-> PAR
14 CLK_PCI_LAN <-> CLK_PCI_LAN A1
PCI_CLK

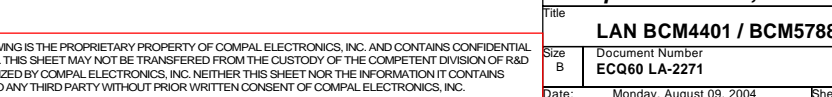
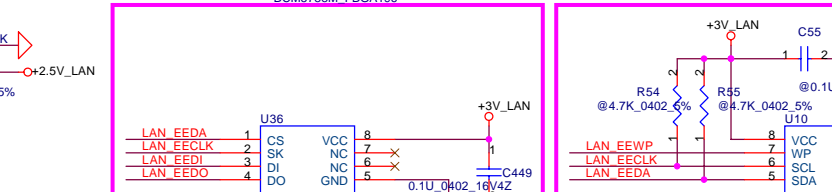
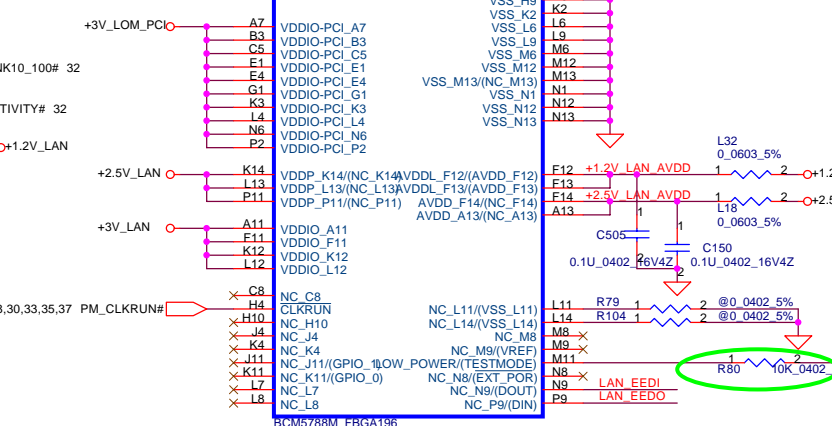
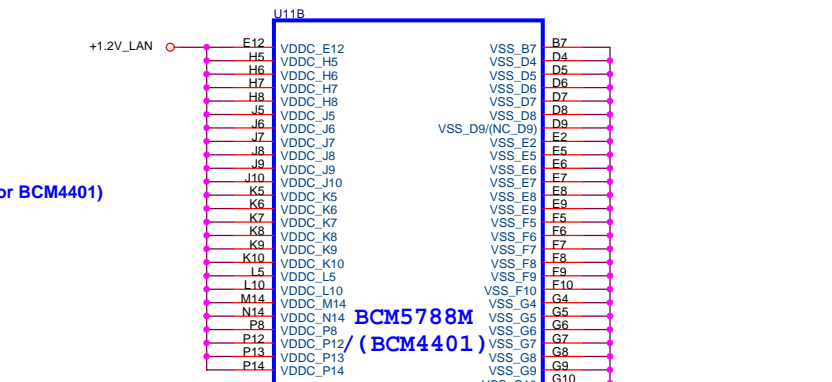
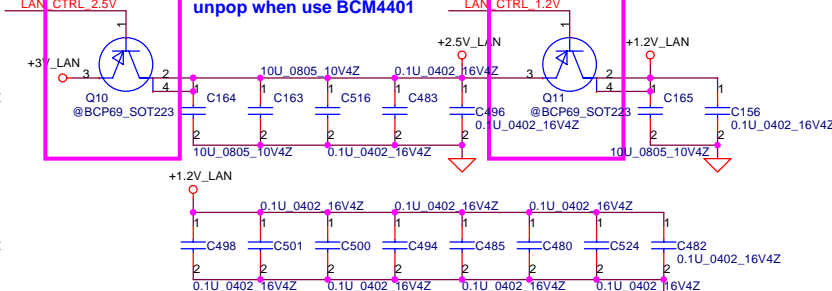
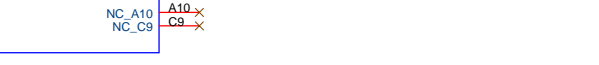
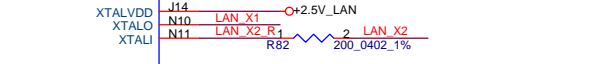
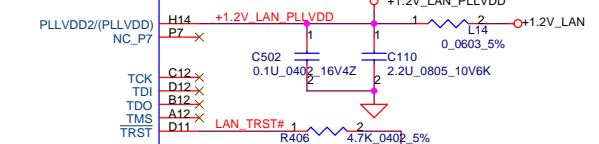
23 PCI_PIRQF# <-> H2
23,28,30,33 PCI_RST# <-> C2
23 PCI_GNT#3 <-> J3
23 PCI_REQ#3 <-> C3



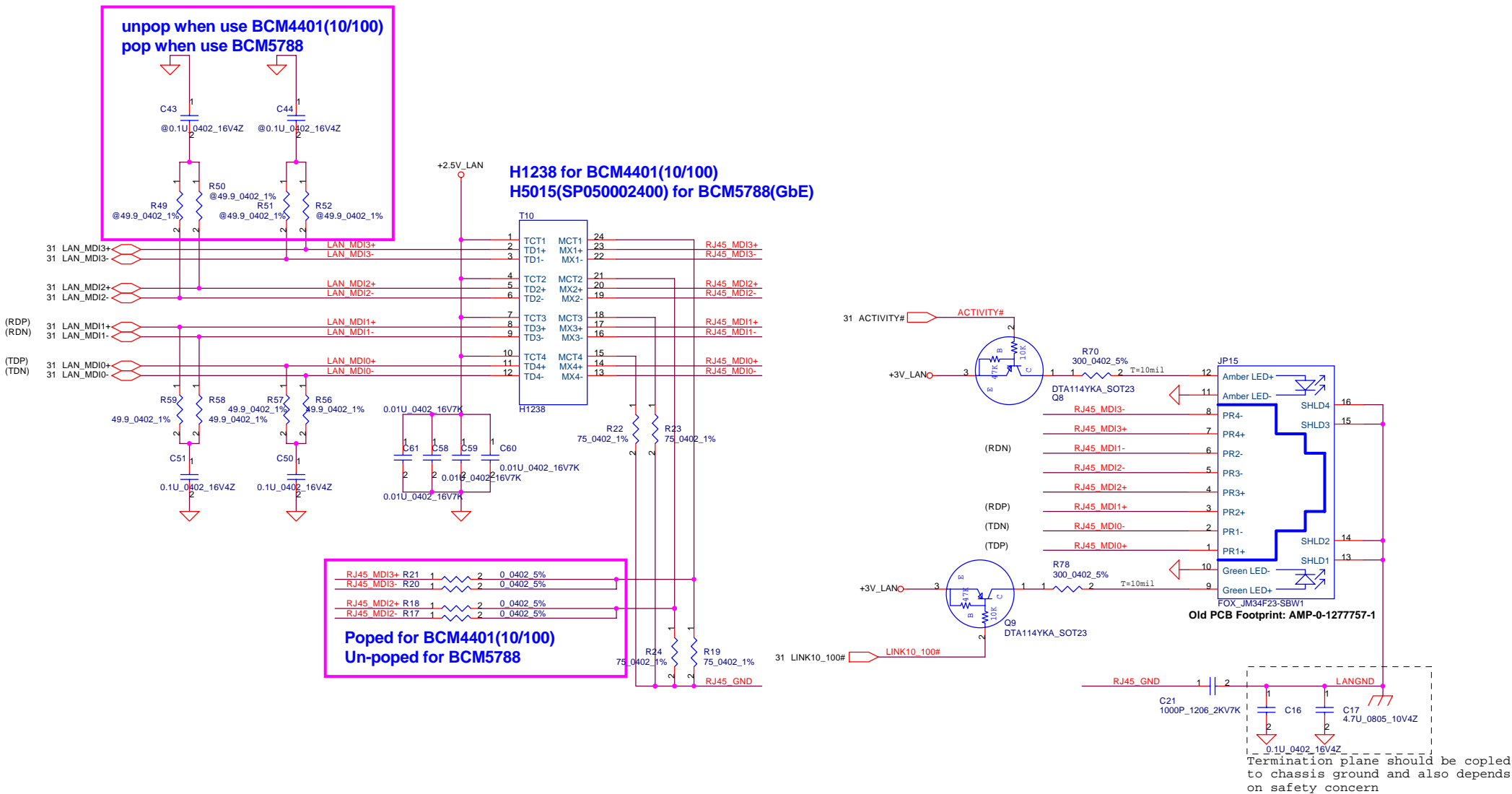
BCM5788M
/(BCM4401)

U11A
AD31 TRD3+/(NC_E13) E13 LAN_MD13+ LAN_MD13+ 32
AD30 TRD3-(NC_E14) E14 LAN_MD13- LAN_MD13- 32
AD29 TRD2+/(NC_D13) D13 LAN_MD12+ LAN_MD12+ 32
AD28 TRD2-(NC_D14) D14 LAN_MD12- LAN_MD12- 32
AD27 TRD1+(RDP) B13 LAN_MD11+ LAN_MD11+ 32 (RDP)
AD26 TRD1-(RDN) C14 LAN_MD11- LAN_MD11- 32 (RDN)
AD25 TRD0+(TDP) B13 LAN_MD10+ LAN_MD10+ 32 (TDP)
AD24 TRD0-(TDN) B14 LAN_MD10- LAN_MD10- 32 (TDN)
AD23
AD22
AD21
AD20 REGSUP12/(NC_B9) B9 LAN_CTRL_1.2V +2.5V_LAN
AD19 REGCTL12/(NC_E10) B10 LAN_CTRL_1.2V +2.5V_LAN
AD18 REGSEN12/(REG18OUT) A9 +1.2V_LAN (Output 1.8V for BCM4401)
AD17 REGSUP25/(REGSUP18) B11 LAN_CTRL_2.5V +3V_LAN
AD16 REGCTL25/(NC_C11) C11 LAN_CTRL_2.5V +3V_LAN
AD15 REGSEN25/(REGSUP18) C10 +2.5V_LAN
AD14
AD13 VESD1 P1 +3V_LAN
AD12 VESD2 G2
AD11 VESD3 A1
AD10 EEDATA/(SPROM_CS) P10 LAN_EEDA
AD9 EECLK/(SPROM_CLK) M10 LAN_EECLK
AD8
AD7
AD6 GPIO0/(NC_H12) H12 LAN_EEWP_1 +3V_LAN
AD5 GPIO1/(NC_K13) K13 LAN_EEWP_2 +3V_LAN
AD4 GPIO2/(NC_J13) J13 LAN_EEWP_3 +3V_LAN

LINKLED/(LINKLED10) G13 LINK10_100# 32
SPD100LED/(LINKLED100) H13 R115 1 2 @0.0402_5%
SPD1000LED/(COL_LED) G12 ACTIVITY# 32
TRAFFICLED/(ACT_LED) G14

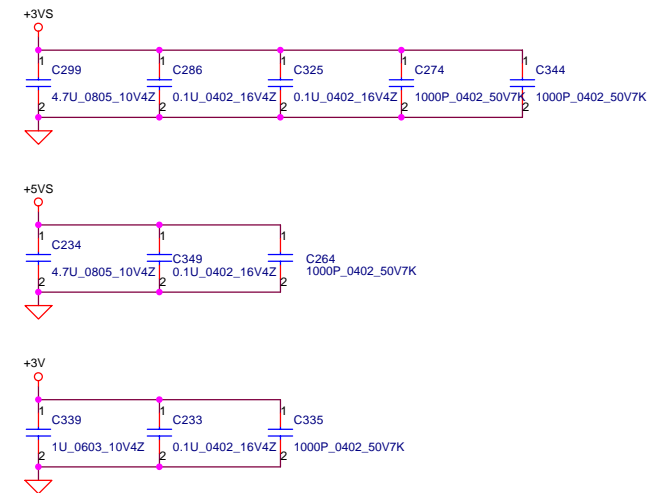
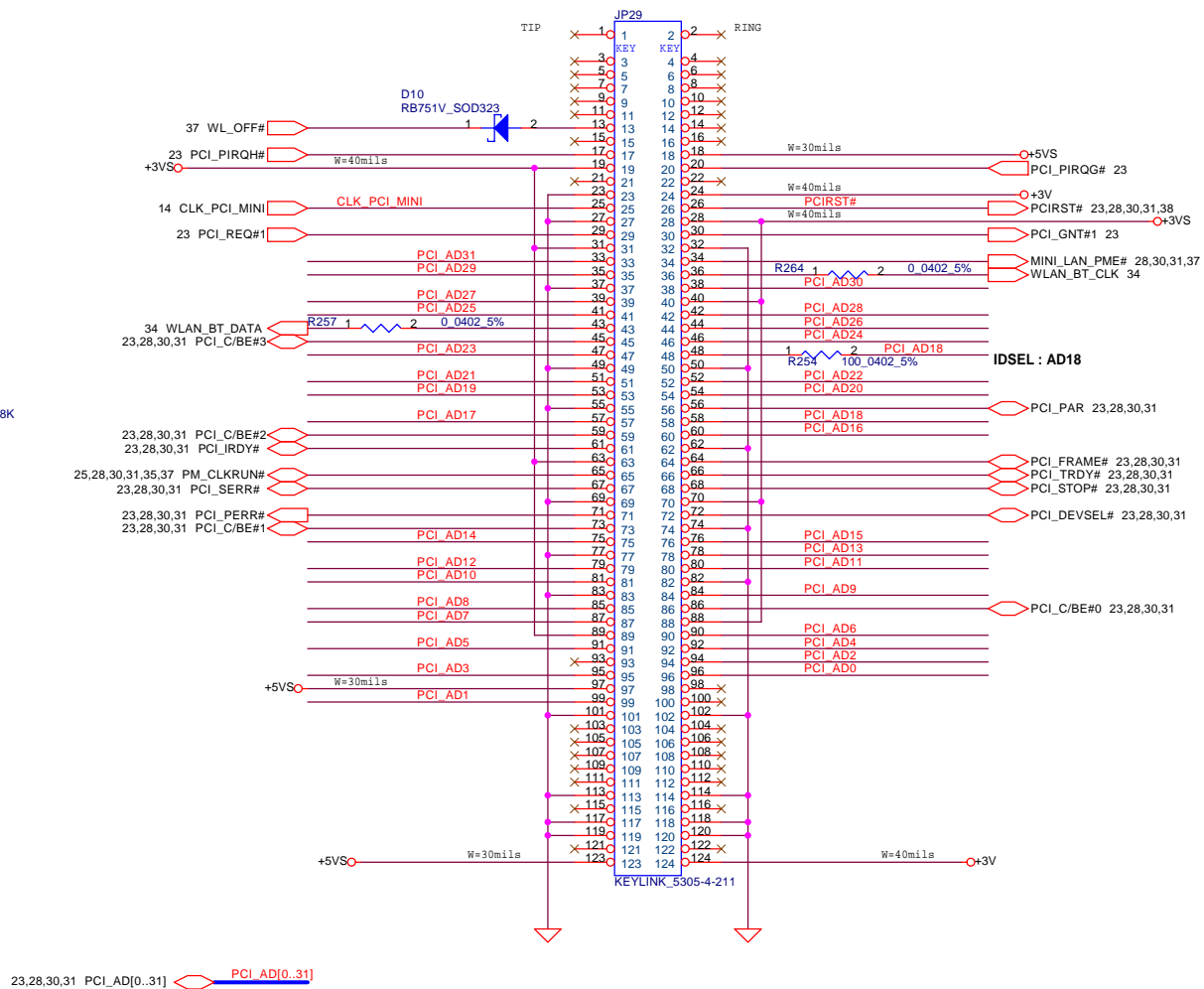


LAN BCM5788M/BCM4401KFB



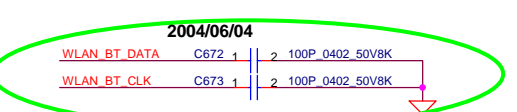
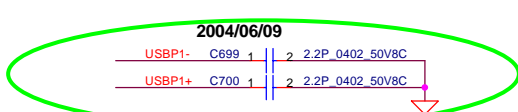
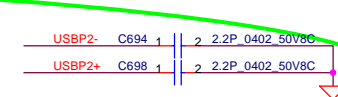
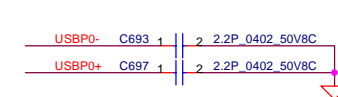
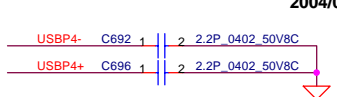
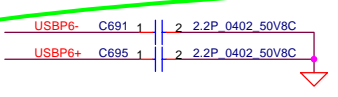
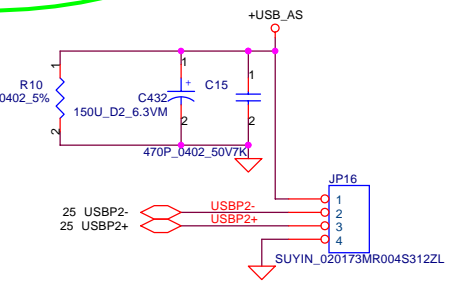
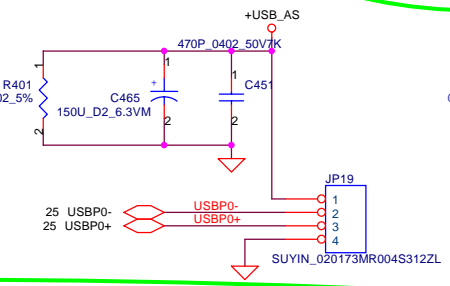
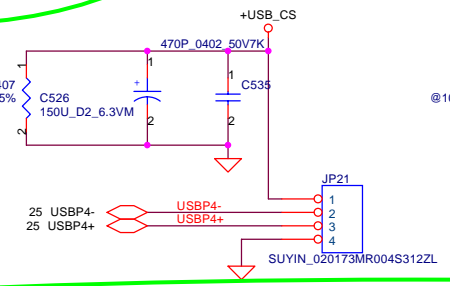
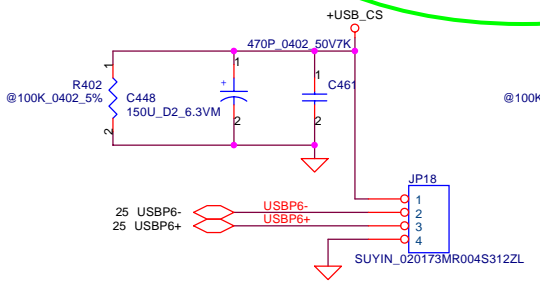
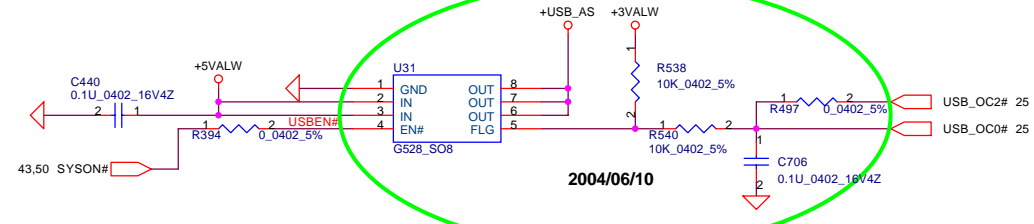
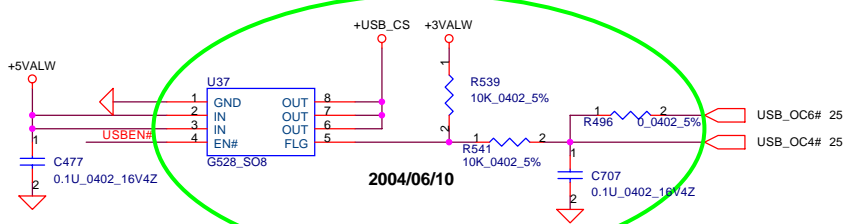
Compal Electronics, Inc.		
Title	LAN Magnetic & RJ45 / RJ11	
Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 32 of 54

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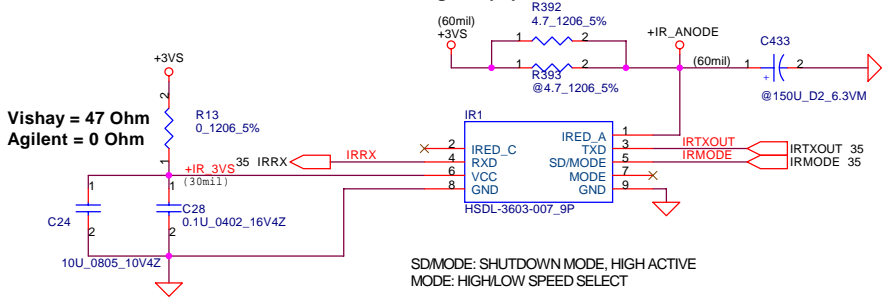
Compal Electronics, Inc.		
Title: Mini PCI Slot1		
Size: Custom	Document Number: ECQ60 LA-2271	Rev: 1A
Date: Monday, August 09, 2004	Sheet: 33	of 54

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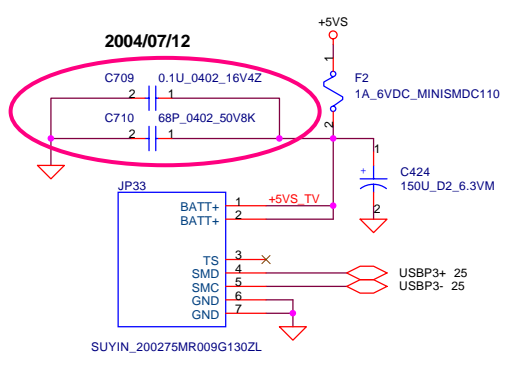


FIR Module

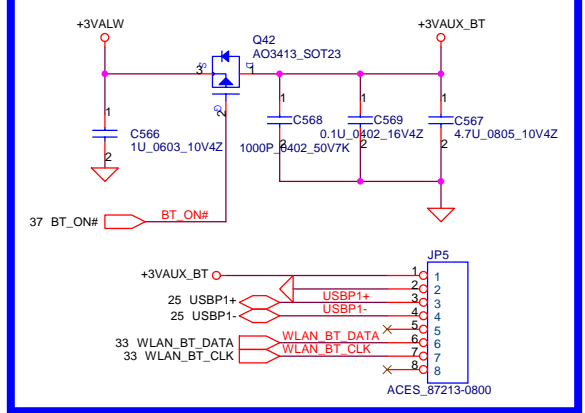
Vishay populate two 4.7 Ohm resistor
Agilent populate one 4.7 Ohm resistor



TV-Tuner Module Connector



Bluetooth Conn.



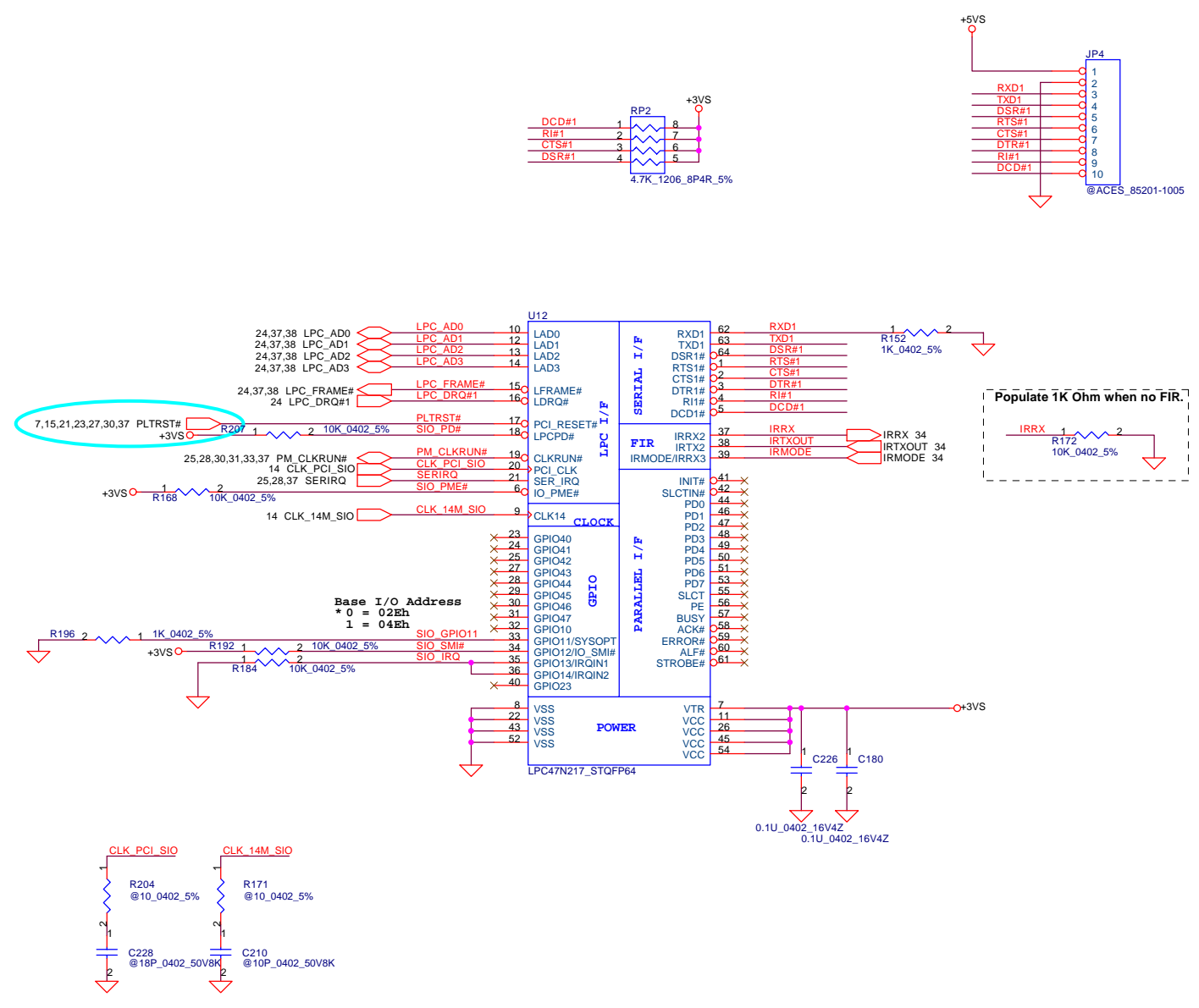
Compal Electronics, Inc.

Title
USB / FIR / BT / TV

Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 34 of 54

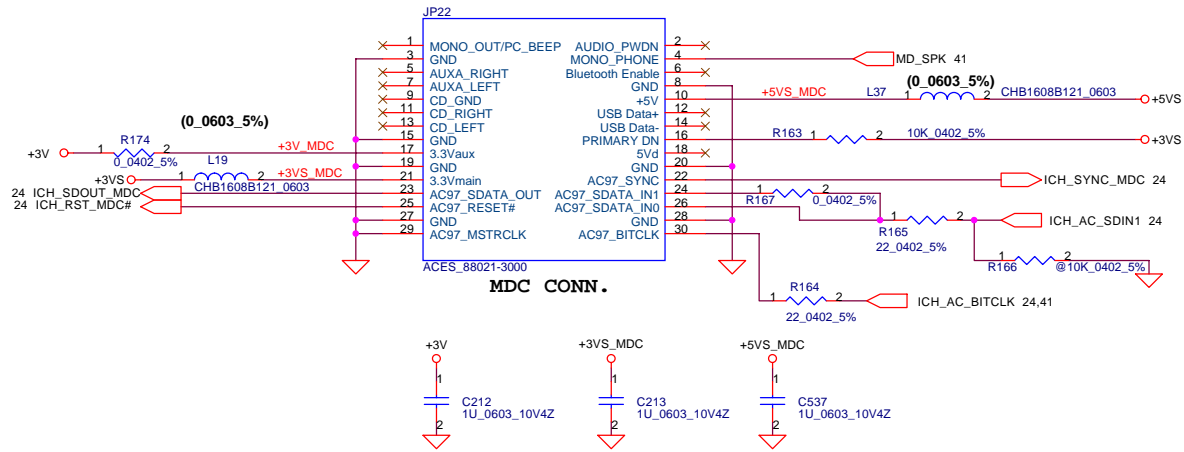
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SUPER I/O SMsC LPC47N217



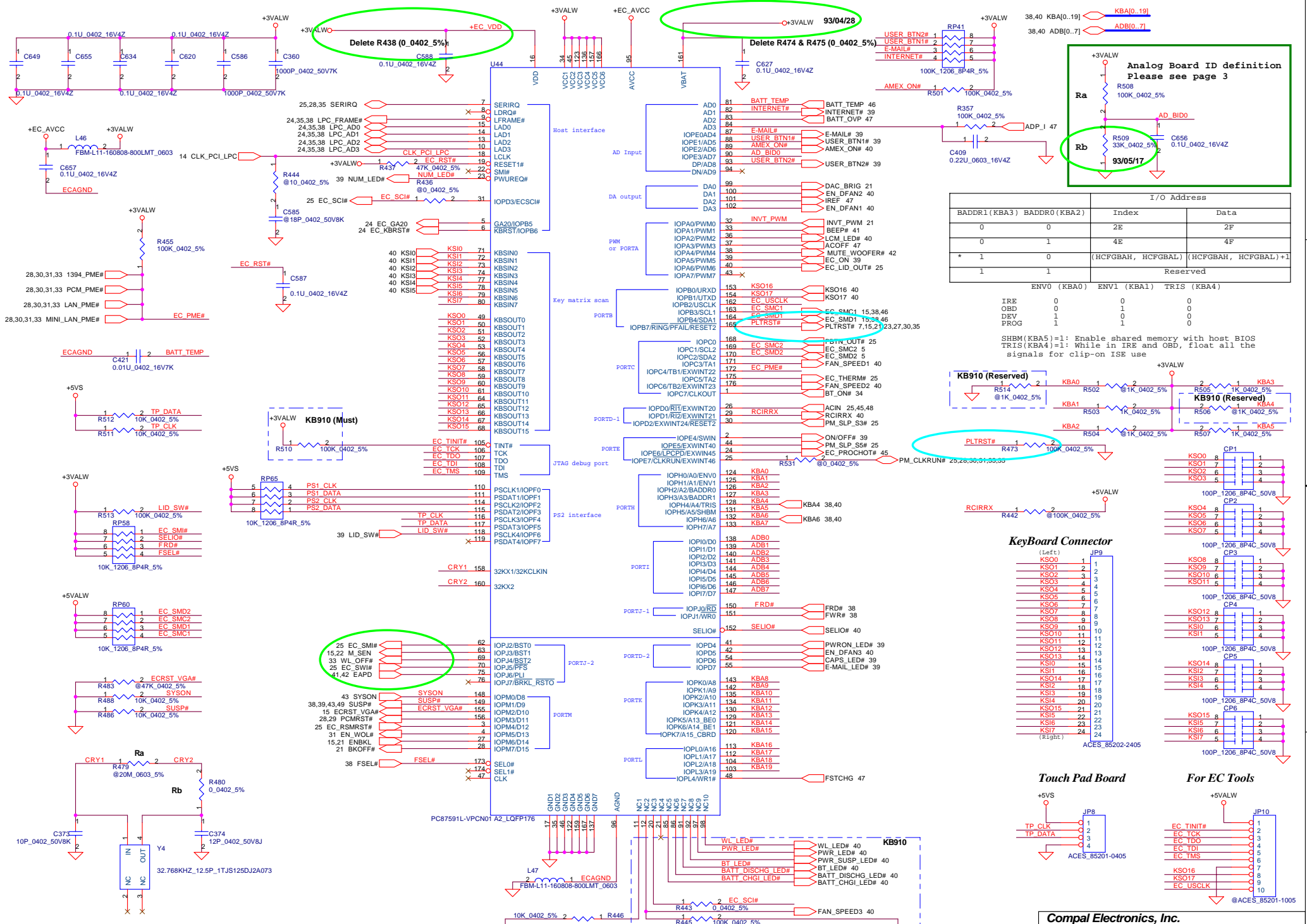
Compal Electronics, Inc.		
Title: SUPER I/O LPC47N217		
Size: ECQ60	Document Number: LA-2271	Rev: 1A
Date: Monday, August 09, 2004	Sheet: 35	of 54

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Compal Electronics, Inc.			
Title			
PARALLEL PORT / MDC			
Size	Document Number		Rev
Customer	ECQ60 LA-2271		1A
Date:	Monday, August 09, 2004	Sheet	36 of 54

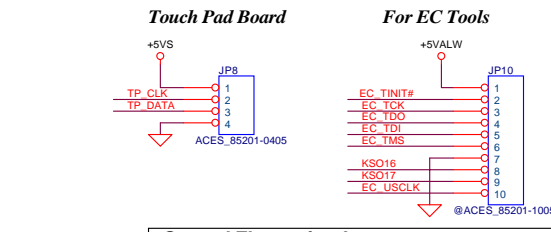
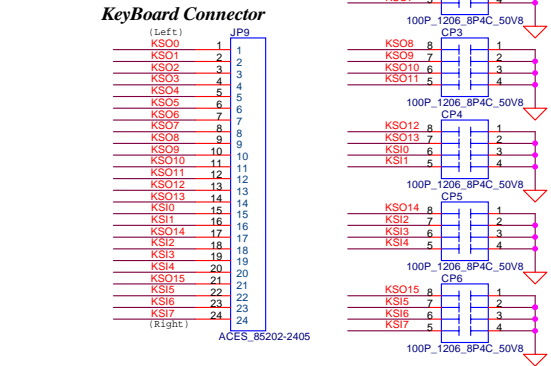
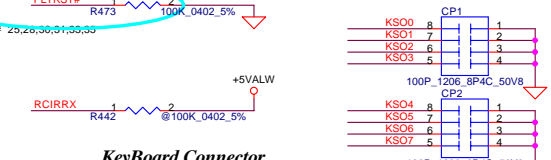
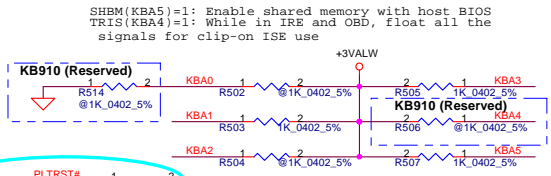
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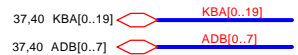
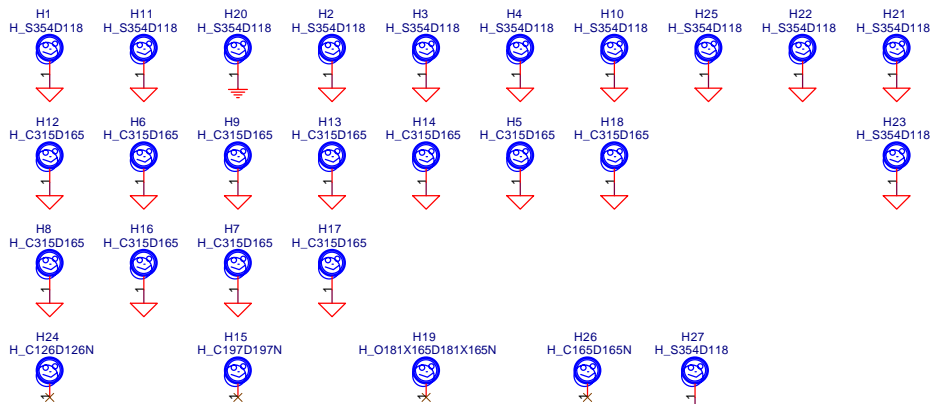
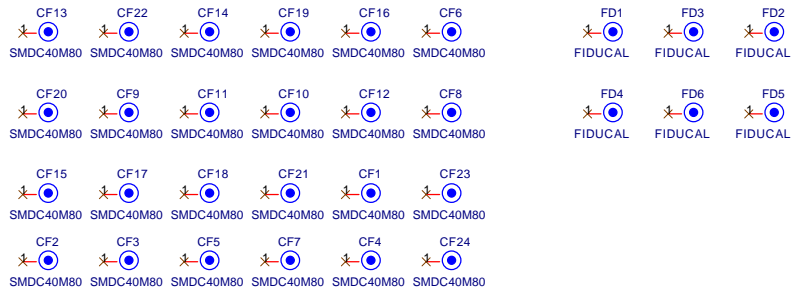
Analog Board ID definition
Please see page 3

Ra: 100K_0.402_5%
Rb: 33K_0.402_5%
93/05/17

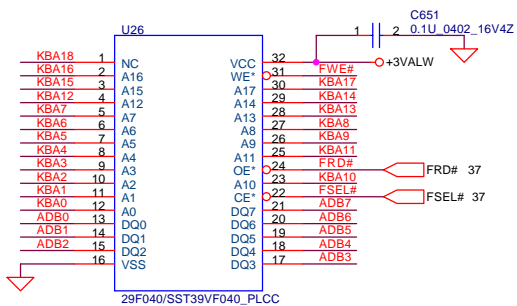
BADDR1 (KBA3)	BADDR0 (KBA2)	Index	Data
0	0	2E	2F
0	1	4E	4F
* 1	0	(HCFGBAH, HCFGBAL)	(HCFGBAH, HCFGBAL)+1
1	1	Reserved	



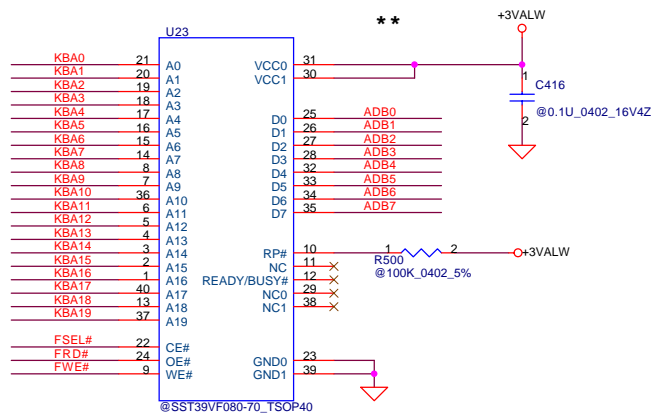
	NS87591L	KB910
Ra	20M_0603_5%	No Stuff
Rb	120K_0402_5%	0_0402_5%



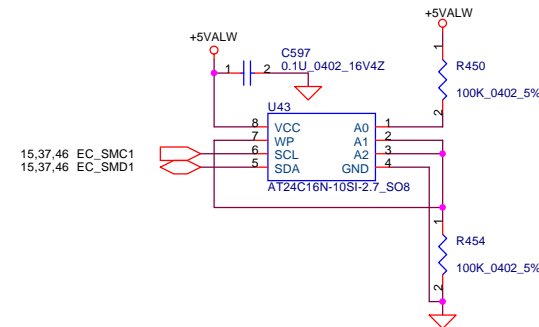
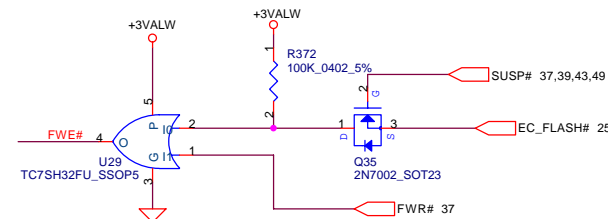
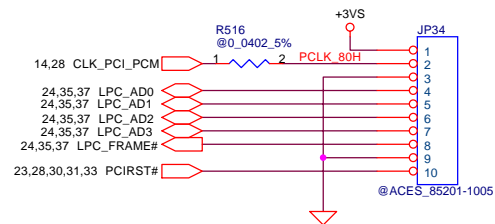
512KB Flash ROM



1MB Flash ROM



For LPC Debug Card

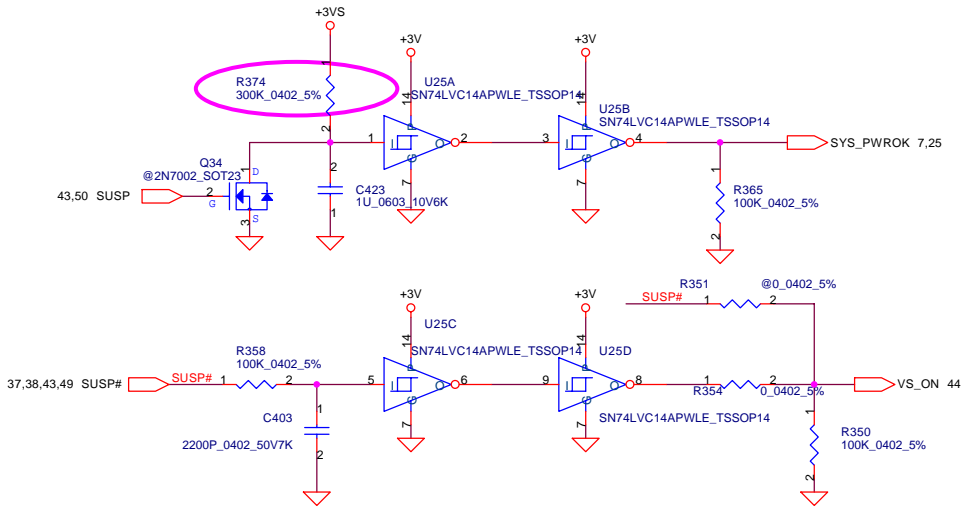


Compal Electronics, Inc.

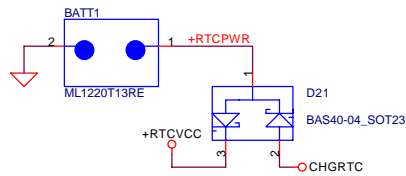
Title		BIOS & EXT. I/O PORT	
Size	Document Number	Rev	
Customer	ECQ60 LA-2271	1A	
Date:	Monday, August 09, 2004	Sheet	38 of 54

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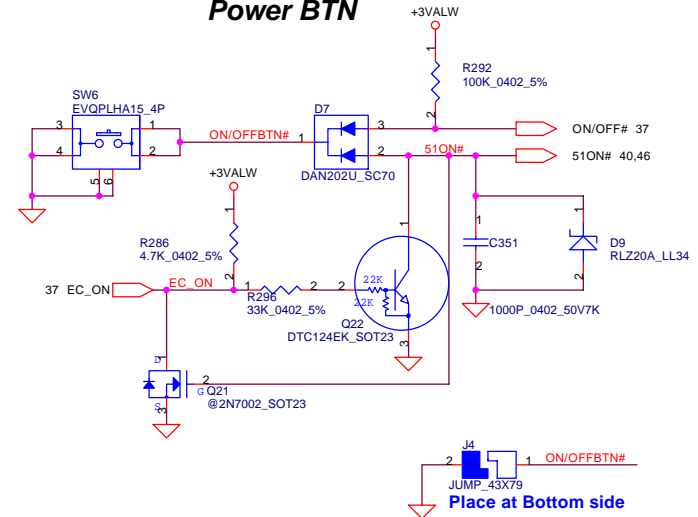
Power ON Circuit



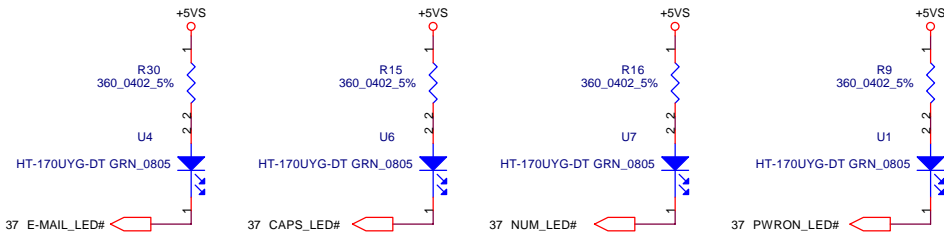
RTC Battery



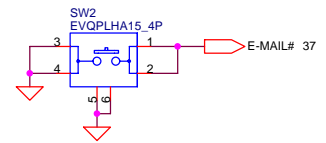
Power BTN



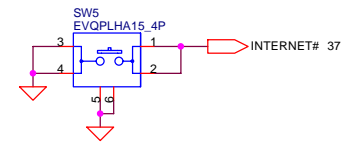
LED Indicator



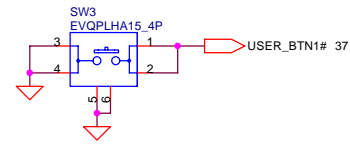
E-Mail_BTN



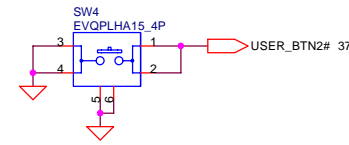
Internet_BTN



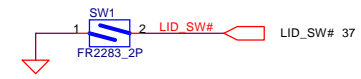
USER_BTN1



USER_BTN2



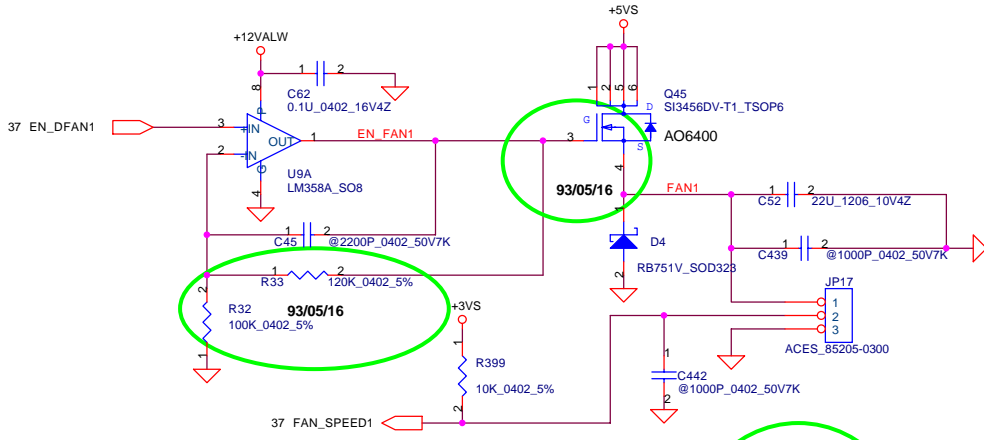
LID_Switch



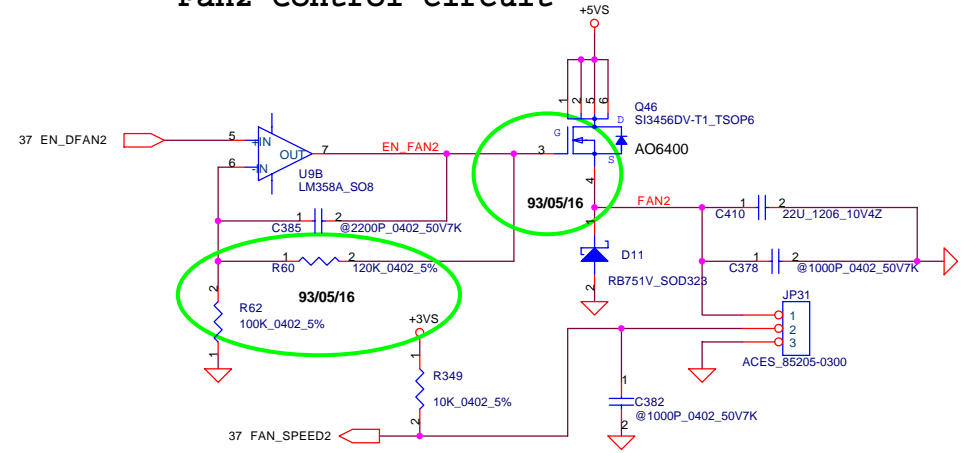
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Compal Electronics, Inc.		
Title Power OK/Reset/RTC battery		
Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 39 of 54

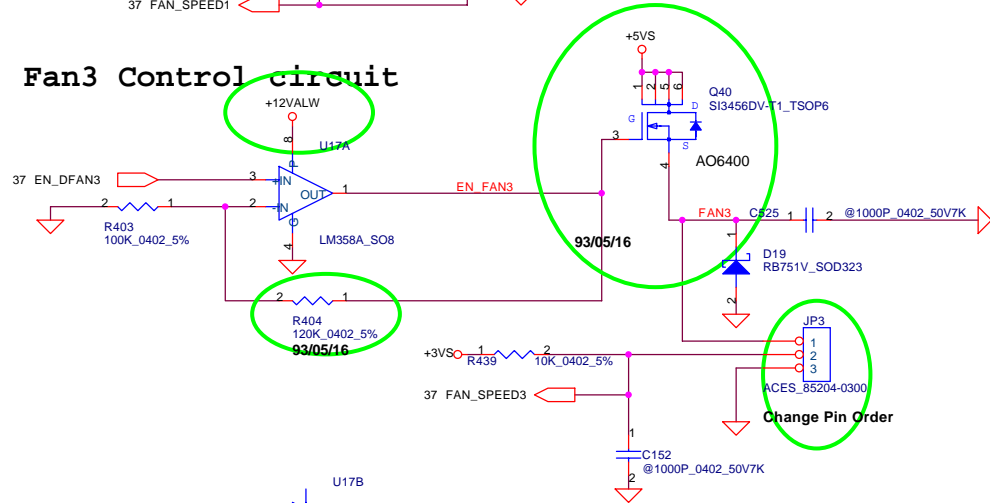
Fan1 Control circuit



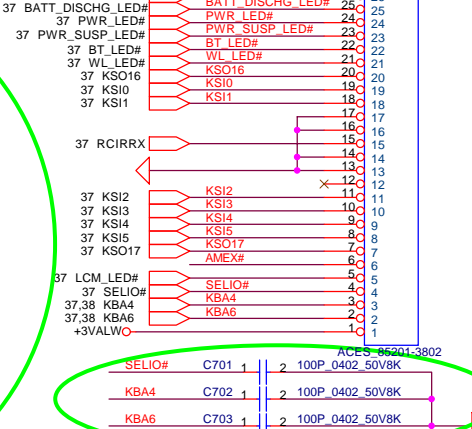
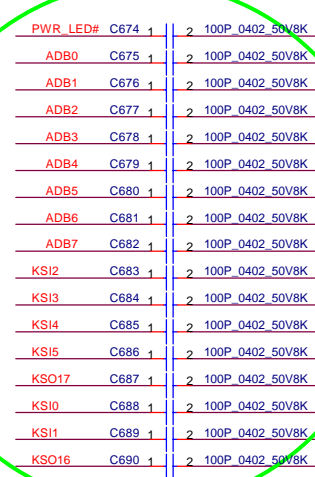
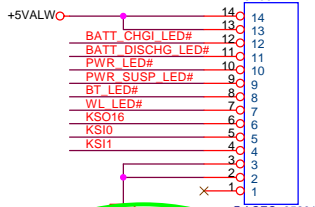
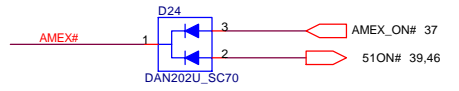
Fan2 Control circuit



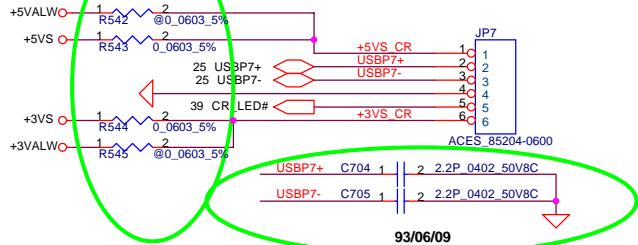
Fan3 Control circuit



AMEX# to support Instant On Media Play.



CARD READER CONNECTOR



	KSO16	KSO17
KSI0	BT_ENABLE	LEFT#
KSI1	WL_ENABLE	RIGHT#
KSI2	EC_PLAYBTN#	ENTER#
KSI3	EC_STOPBTN#	VOLUME_UP#
KSI4	EC_REVBTN#	VOLUME_DOWN#
KSI5	EC_FRDBTN#	

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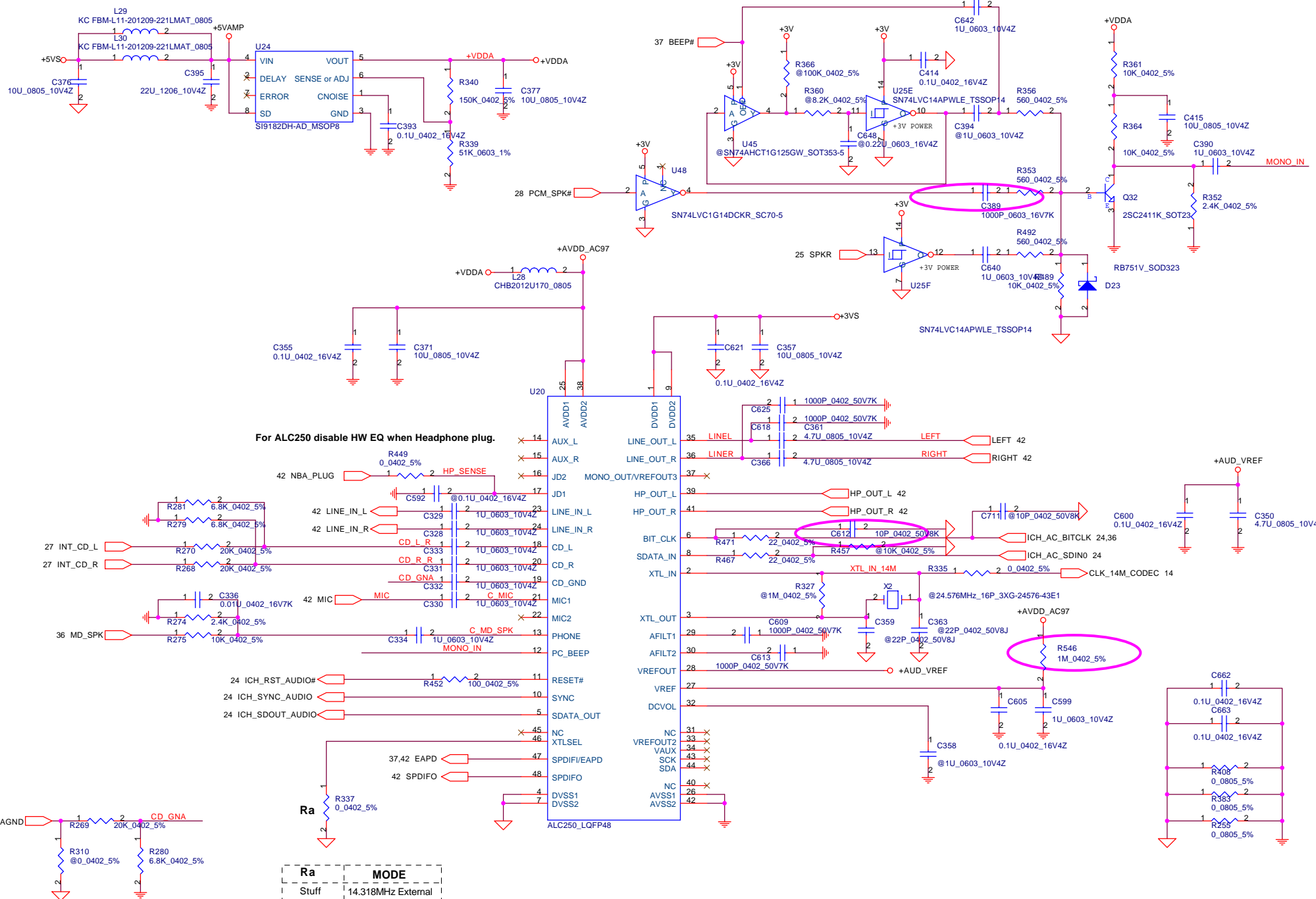
Compal Electronics, Inc.

Title: **FAN Control Circuit**

Size: Document Number
Customer: **ECQ60 LA-2271** Rev: 1A

Date: Monday, August 09, 2004 Sheet 40 of 54

AC97 Codec



Ra	MODE
Stuff	14.318MHz External
No-Stuff	24.576MHz Crystal or External Colp

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Compal Electronics, Inc.

Title: **AC97 Codec Realtek ALC250**

Size: **ECC60 LA-2271**

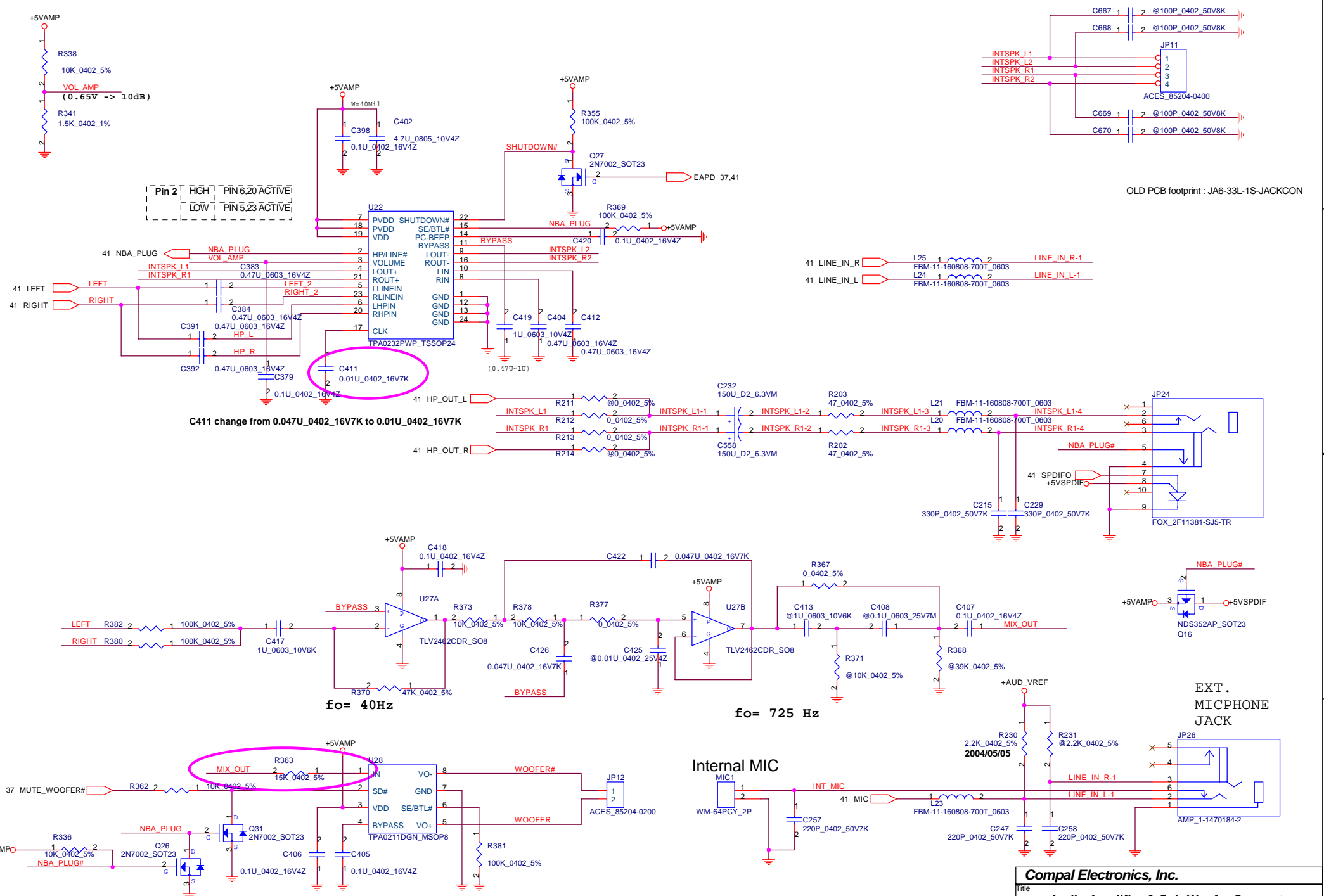
Document Number: **ECC60 LA-2271**

Customer: **ECC60 LA-2271**

Date: **Monday, August 09, 2004**

Sheet: **41** of **54**

Rev: **1A**



Pin 2 HIGH PIN 6,20 ACTIVE
LOW PIN 5,23 ACTIVE

C411 change from 0.047U_0402_16V7K to 0.01U_0402_16V7K

f_o = 40Hz

f_o = 725 Hz

2004/05/05

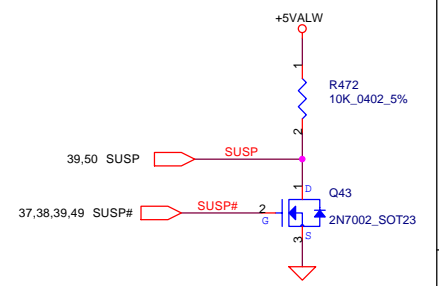
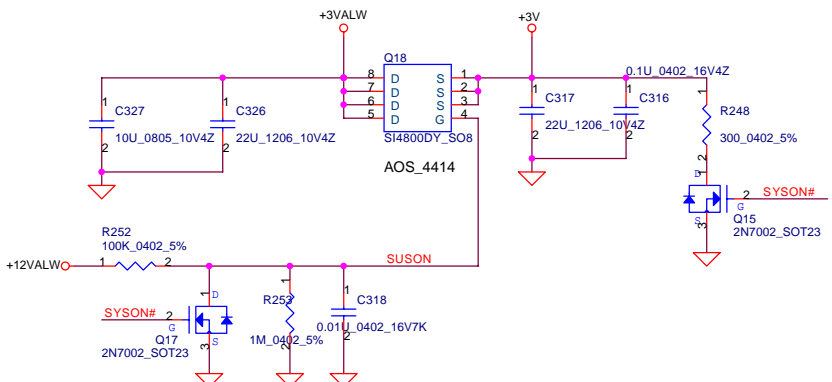
Compal Electronics, Inc.		
Title	Audio Amplifier & Sub-Wwoofer Connector	
Size	Document Number	Rev
Customer	ECC60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 42 of 54

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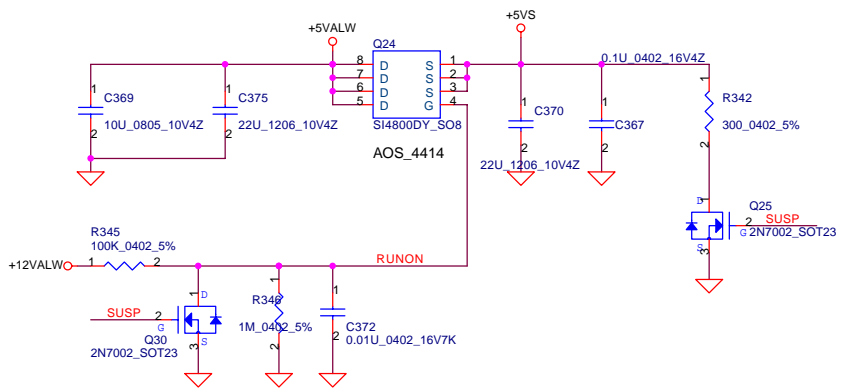
OLD PCB footprint : JA6-33L-1S-JACKCON

EXT. MICPHONE JACK

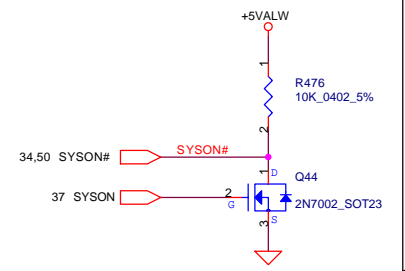
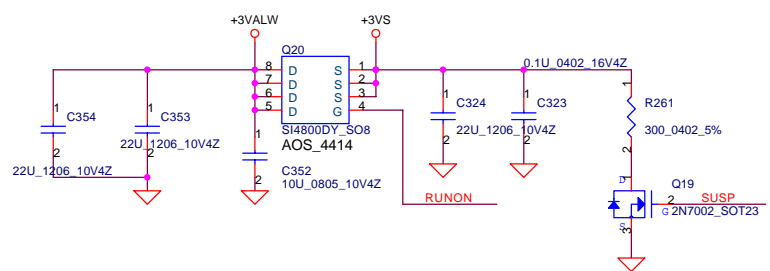
+3VALW to +3V Transfer



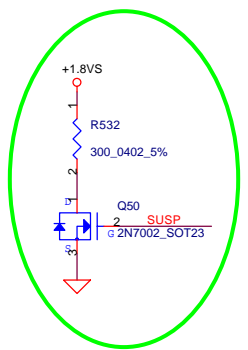
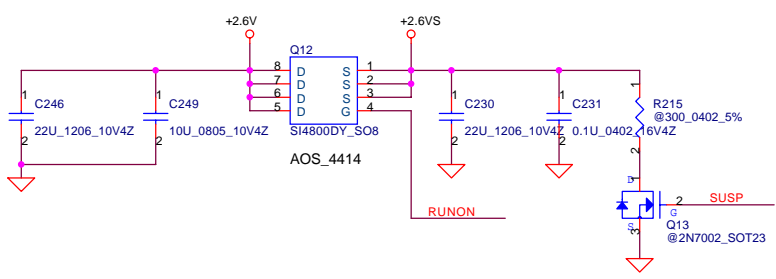
+5VALW to +5VS Transfer



+3VALW to +3VS Transfer

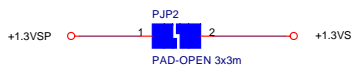
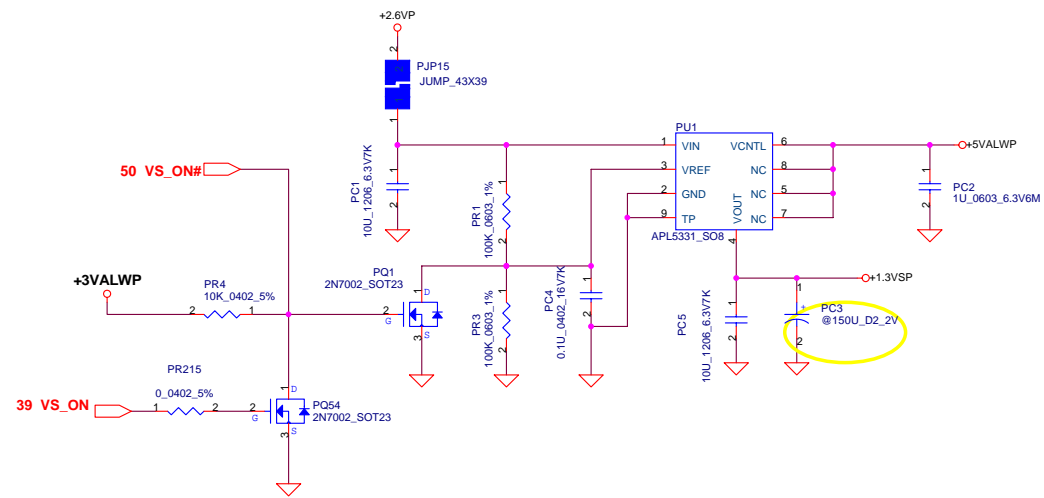


+2.6V to +2.6VS Transfer



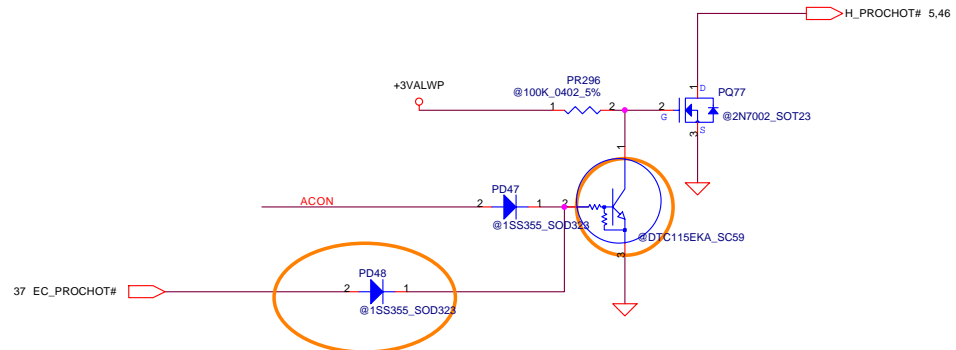
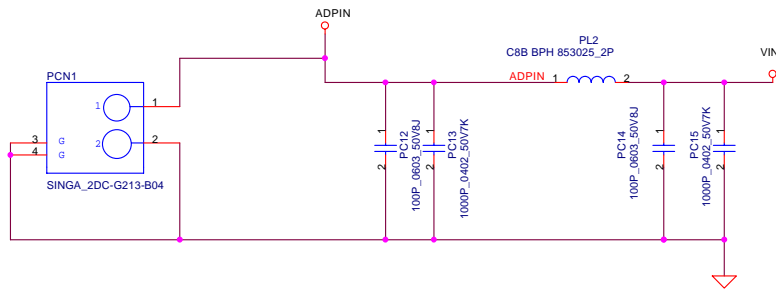
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Compal Electronics, Inc.		
Title DC/DC Circuit Interface		
Size Customer	Document Number ECQ60 LA-2271	Rev 1A
Date:	Monday, August 09, 2004	Sheet 43 of 54

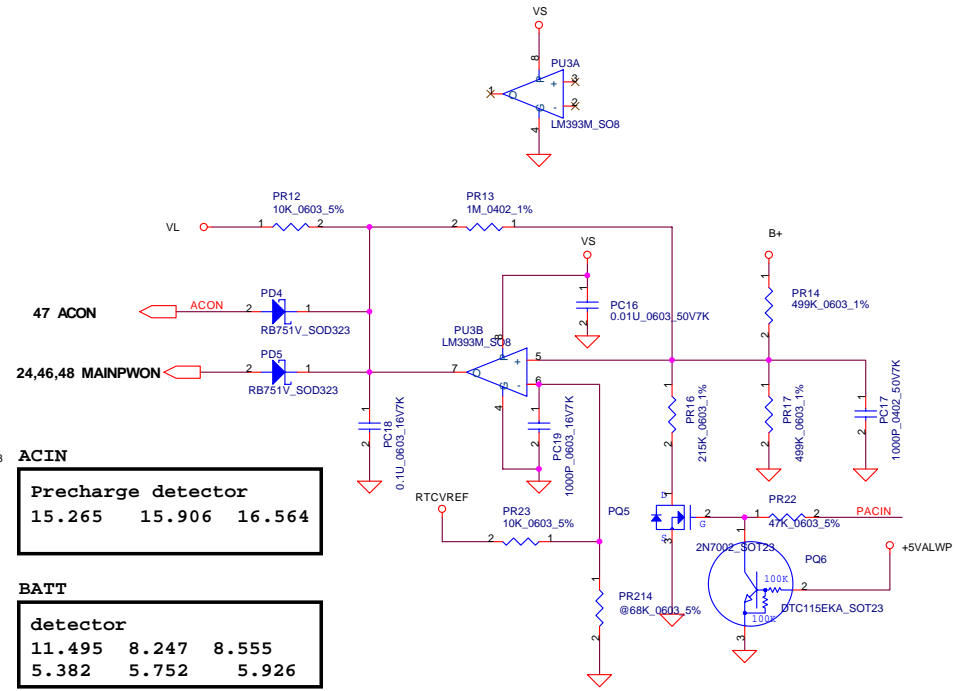
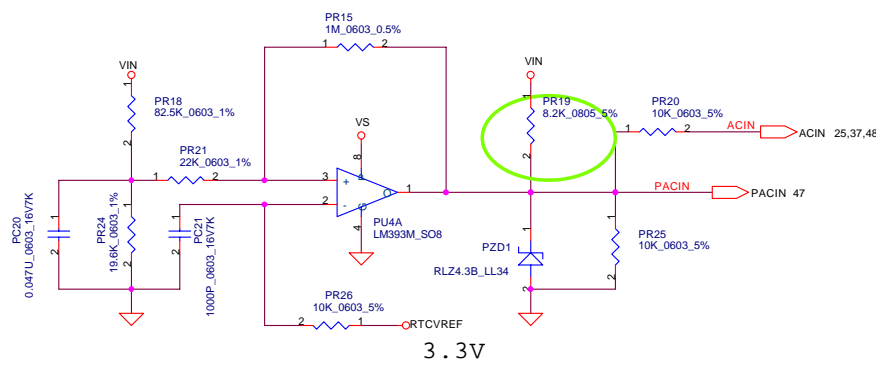


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Compal Electronics, Inc.		
Title		
12V(FAN)& 1.3V(DDR Termination)		
Size	Document Number	Rev
Cust	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 44 of 11



Vin Detector		
17.841	18.234	17.449
17.210	17.597	16.813

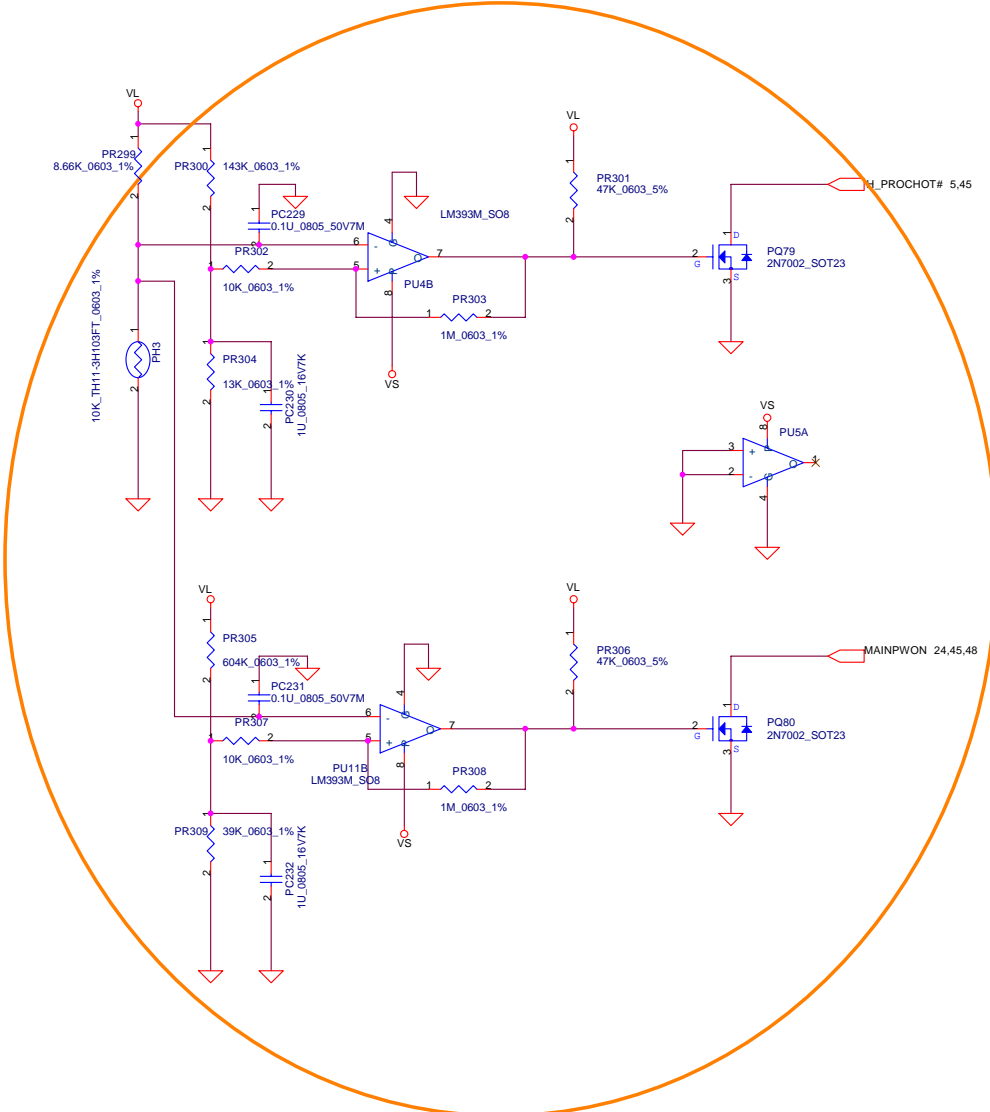
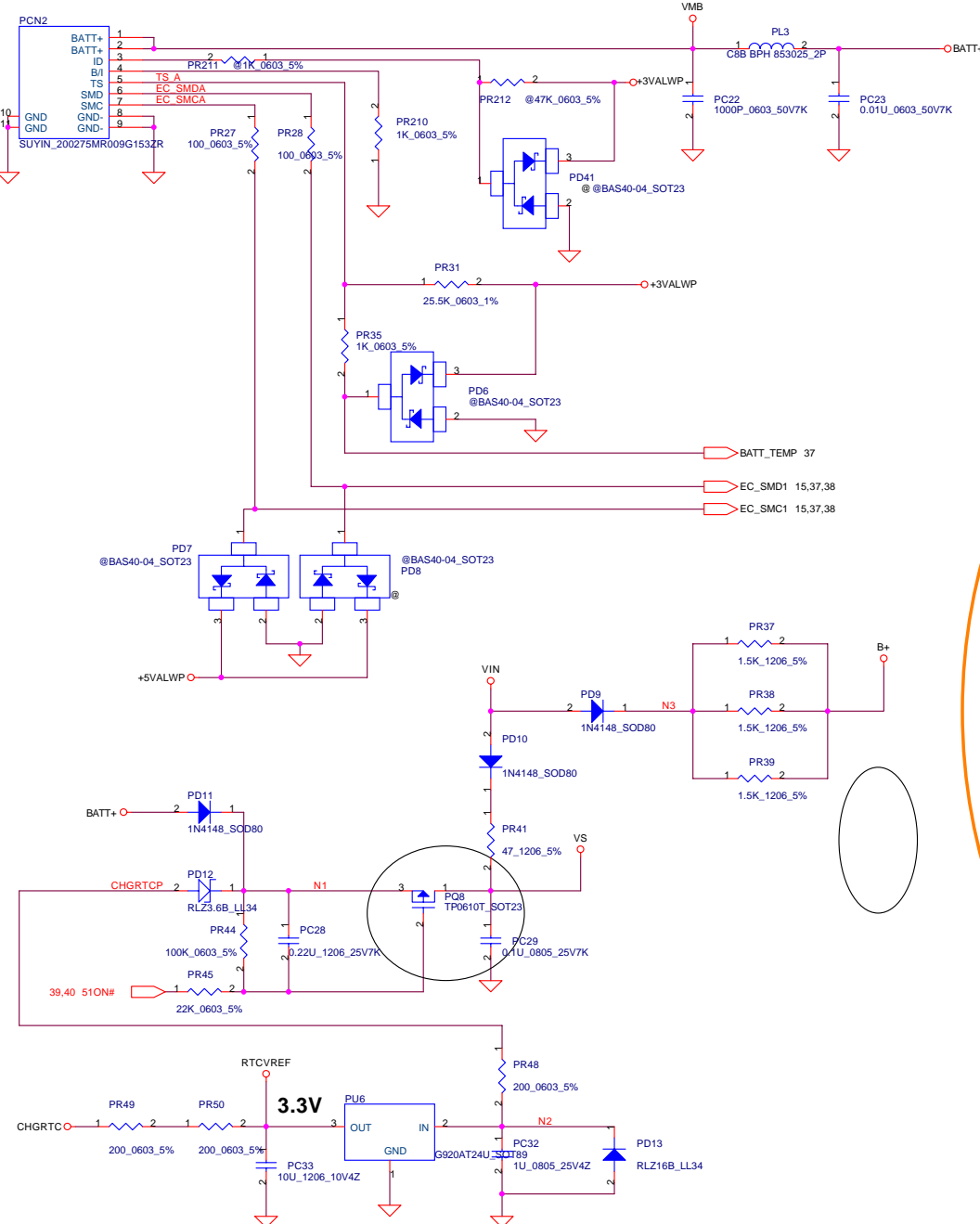


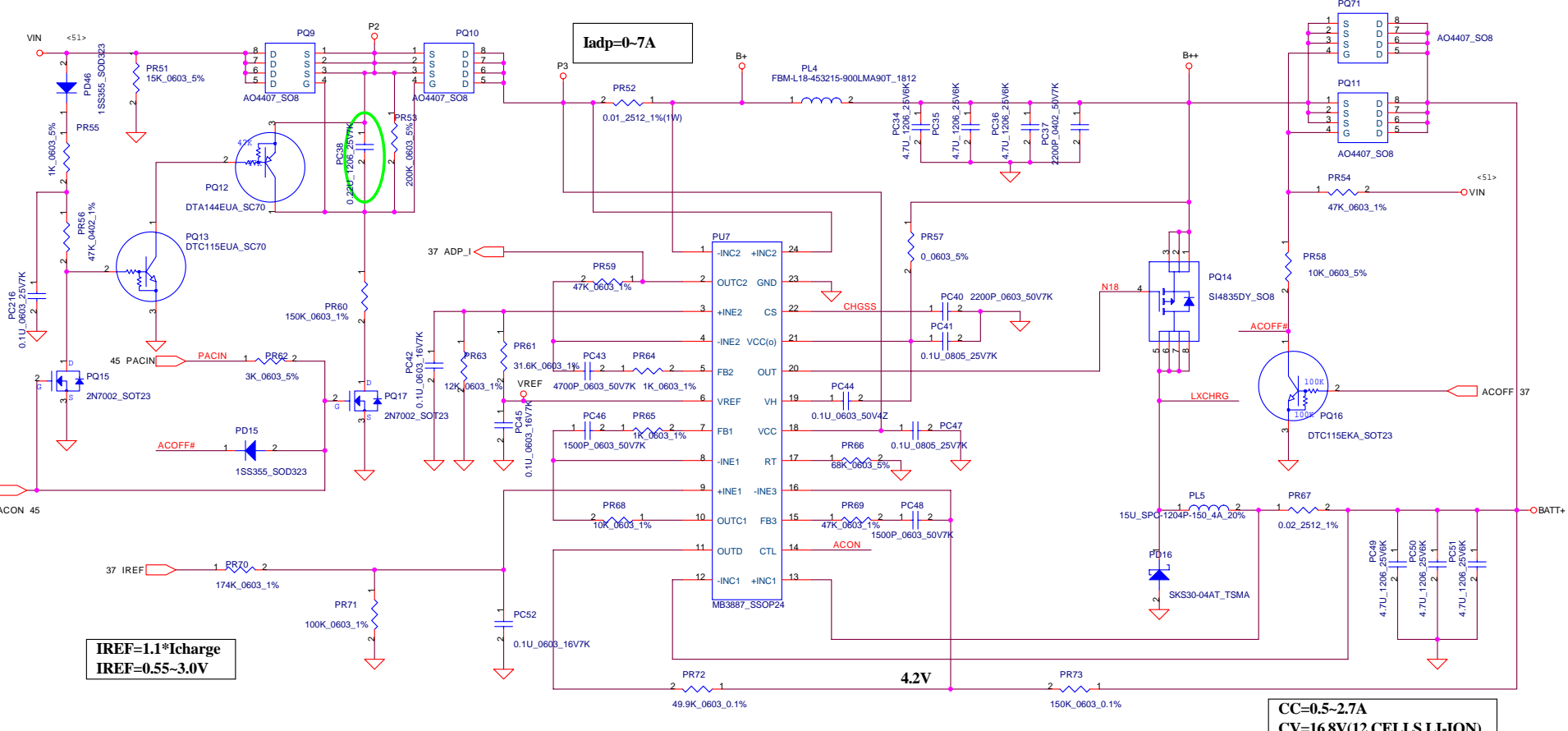
Precharge detector		
15.265	15.906	16.564

BATT detector		
11.495	8.247	8.555
5.382	5.752	5.926

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Compal Electronics, Inc.		
Title		
Detect		
Size	Document Number	Rev
Cust#	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 45 of 11



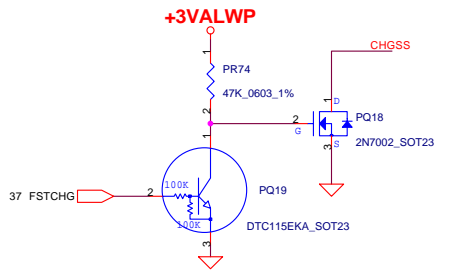
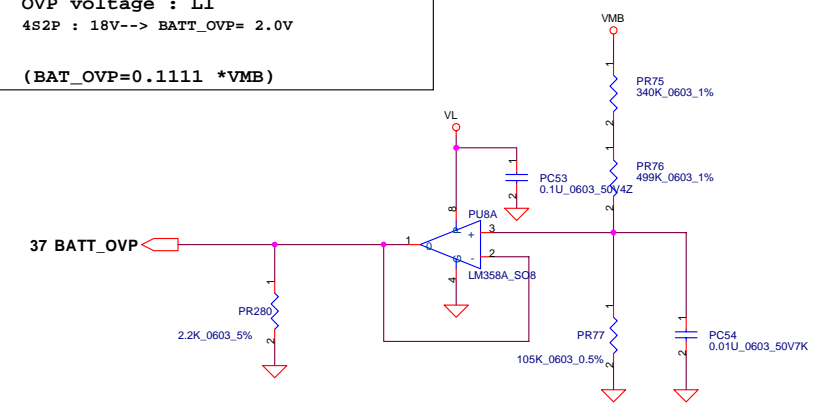


I_{adp}=0-7A

CC=0.5~2.7A
CV=16.8V(12 CELLS LI-ION)

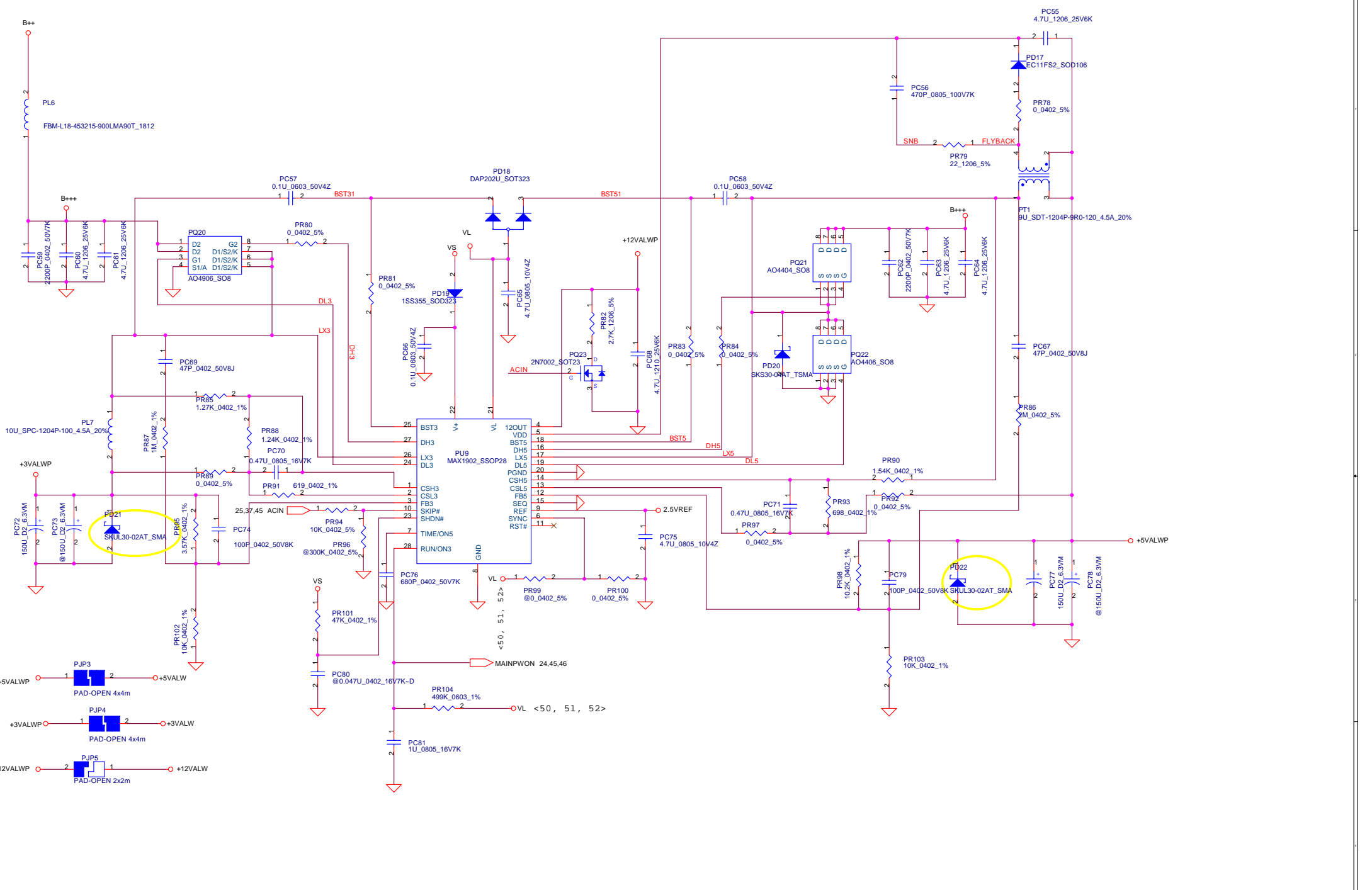
I_{REF}=1.1*I_{charge}
I_{REF}=0.55~3.0V

OVP voltage : LI
4S2P : 18V--> BATT_OVP= 2.0V
(BAT_OVP=0.1111 *VMB)



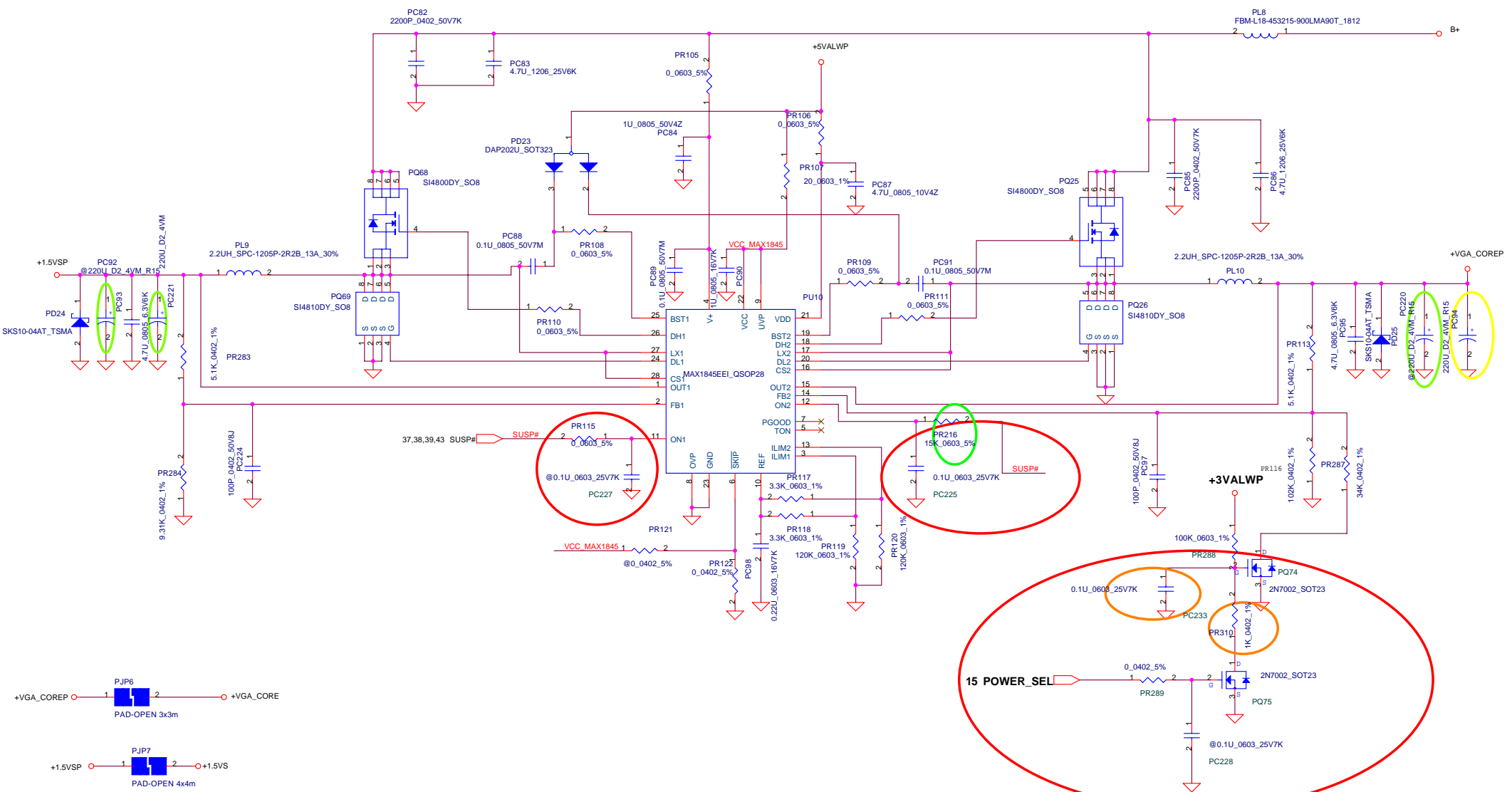
Compal Electronics, Inc.			
File	Charge		
Size	Document Number	Rev	
Customer	ECQ60 LA-2271	1A	
Date:	Monday, August 09, 2004	Sheet	47 of 11

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Compal Electronics, Inc.		
File		
5V/3.3V/12V		
Size	Document Number	Rev
Cust#	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 48 of 11

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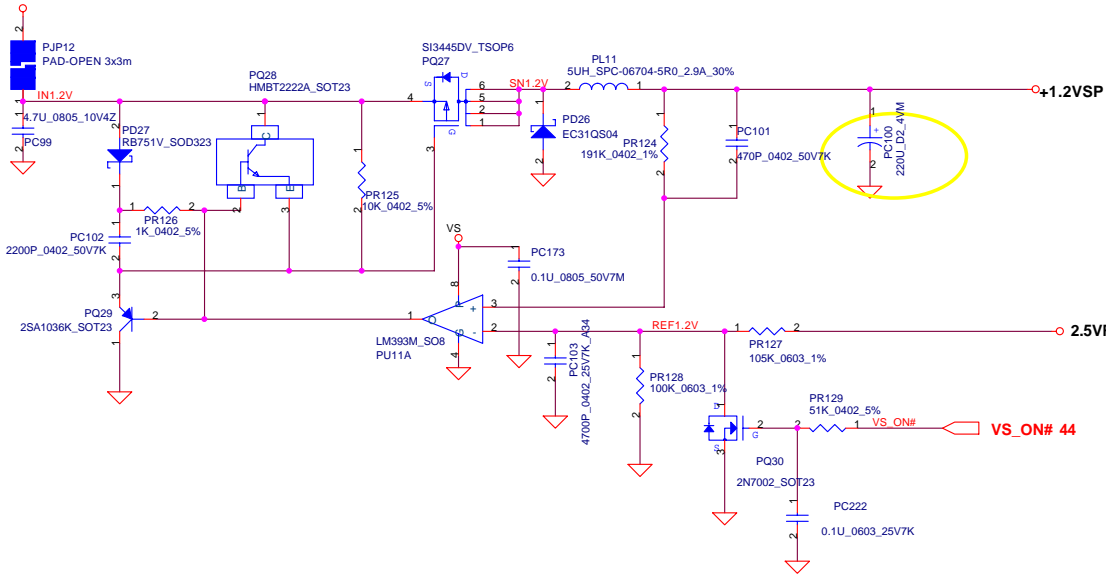


POWER_SEL: H-->VGAOUT 1.05V
 POWER_SEL: L-->VGAOUT 1.20V

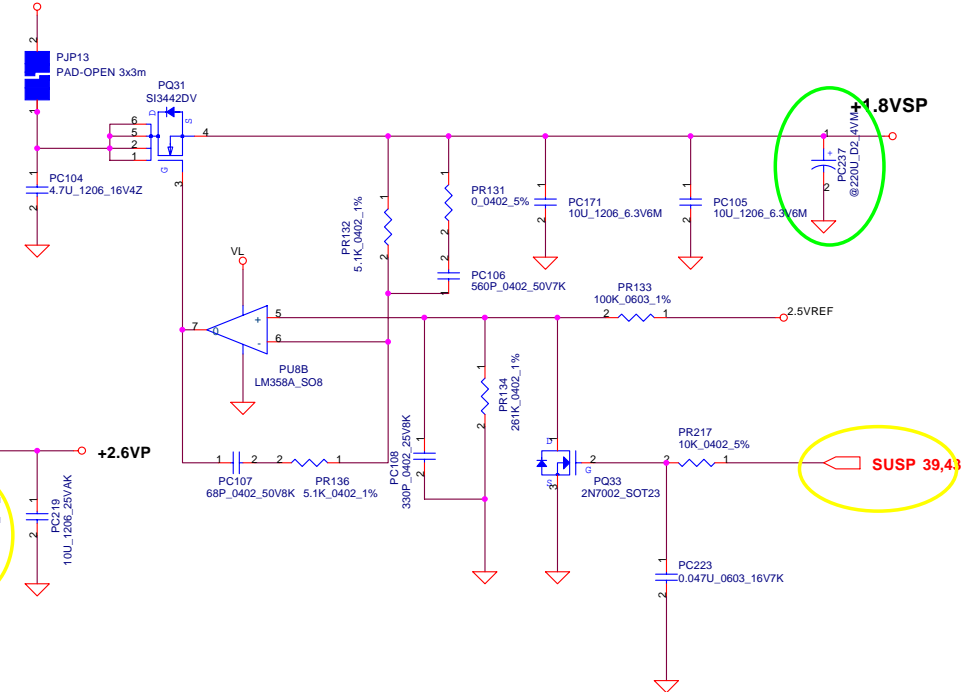
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Compal Electronics, Inc.		
Title		
VGA_CORE & 1.5VS		
Size	Document Number	Rev
Cust#	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 49 of 11

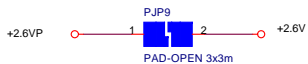
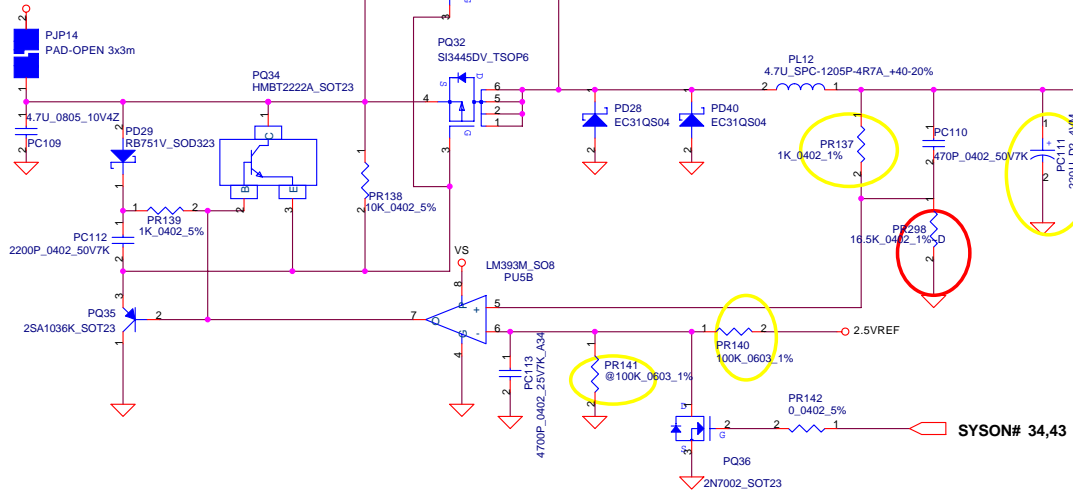
+3VALWP



+3VALW

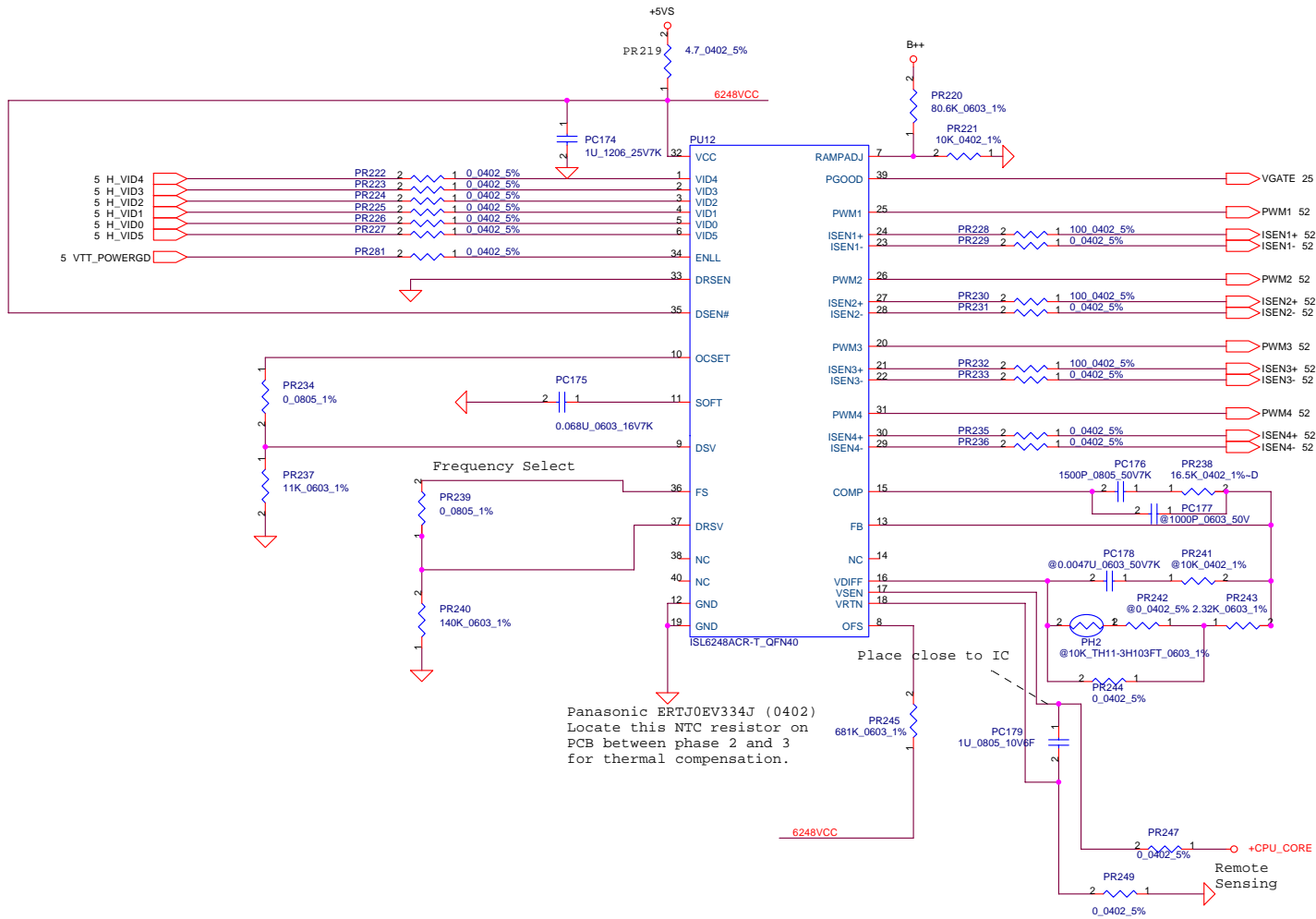


+5VALWP



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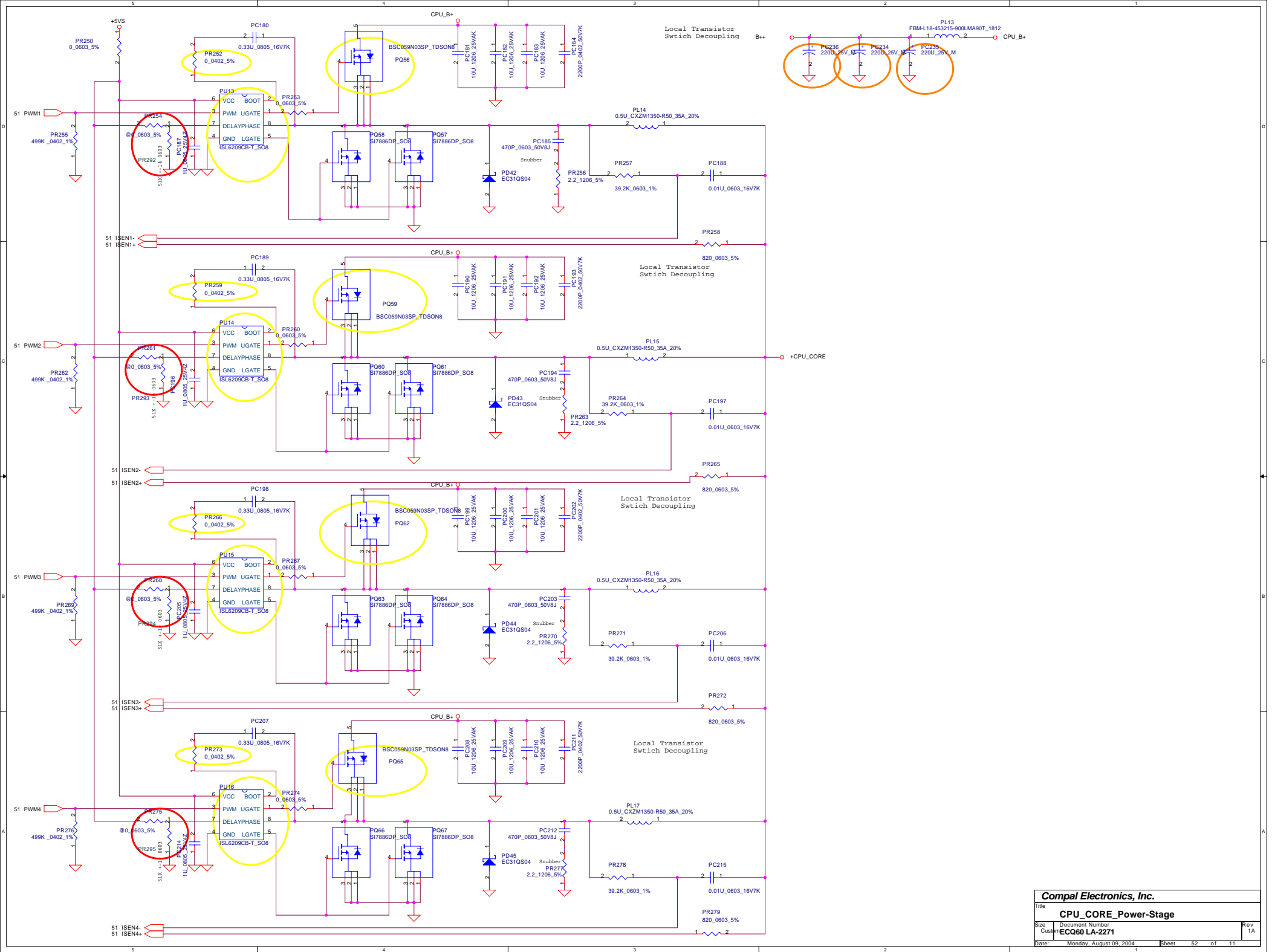
Compal Electronics, Inc.		
File		
1.2V(VTT)/1.8VS/2.6V		
Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 50 of 11



Panasonic ERTJ0EV334J (0402)
 Locate this NTC resistor on
 PCB between phase 2 and 3
 for thermal compensation.

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Compal Electronics, Inc.		
Title		
CPU_CORE_Controller		
Size	Document Number	Rev
Cust	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 51 of 11



HW PIR

B Test

- Page 5 Add R518, R519, C658, Q48, Q47, R517 for VTT_POWERGD timing
- Page 7 Add C664
- Page 8 Change Material R149, R151 to 80.6_0402_1%
- Page 9 Add C659, C660, C661 for EMI request
- Page 14 Change R347, R348 from 10K_0402_5% to 4.7K_0402_5%
- Page 15 Add C665, R210, R201, R411, R189
Change R414 to 71.5_0402_1%
R198, R177 to 1K_0402_1%

Delete R218, R219, R216, R209, R200
- Page 17 Delete D6
Change R45 from 16K_0402_1% to 10.7K_0603_1% for 1.2V +PCIE_1.2VS
Change L15, L16 from 0_0603_5% to CHB1608U301
- Page 23 Add R526 & C666
- Page 26 Delete D22, D8
Change R481, R299 from 1K_0402_5% to 10_0402_5%
- Page 31 Delete R81
- Page 38 Modify scroll hole pad size and add H27
- Page 41 Add C662, C663, R408, R383 for EMI request
- Page 42 Add R382, R380 for mix left & right channel
Change R370 from 100K_0402_5% to 47K_0402_5%
Change C422, C426 from 0.1U_0402_16V4Z to 0.047U_0402_16V7K
Reserved C667, C668, C669, C670 for EMI request
- ### B3 Test
- Page 9 Delete R395, C435, Q38, R26, Q39, R25, C29, U8, C34, R34, R43
Add R533
Change L34, R396, R48, R398, R53, R397 from SM010014500 to SD0020000T8
- Page 17 Change R45 to 16.9K_0603_1%
- Page 19 Add R40, R41, C30, R37, R38, C31 For VRAM Clock Termination
- Page 20 Add R114, R117, C95, R161, R158, C190 For VRAM Clock Termination
- Page 21 Change C320, C322 from 18P_0402_50V7K to 12P_0402_50V8J
Add R528, D26
- Page 22 Add D25 for EC_LID_OUT# to prevent power leakage
Add U47 to and SLP_S4# & SLP_S5#
Delete RP63, RP64 / Add R534, R535, R536, R537 for USB_OC# pull high
- Page 23 Change R299 from 10_0402_5% to 1K_0402_5%
Add D22, D8
- Page 29 Change C310 to 1U_0805_25V4Z
- Page 34 Change USB Power Switch to G528, (U31, U37)
Add C691, C692, C693, C694, C695, C696, C697, C698, C699, C700, C672, C673
- Page 37 Delete R438 R474, R475, R509,
- Page 40 Change R33, R60, R404 to 120K_0402_5%
Change R32, R62 to 100K_0402_5%
Change Q40 to AO6400 for 5V FAN
Add R543, R544 for Card Reader Power
Add C674, C675, C676, C677, C678, C679, C680, 681, C682, C683, C684, C685, C686
C687, C688, C689, C690, C701, C702, C703, C704, C705
- Page 41 Add U48 SN74LVC1G14DCKR_SC70-5
- Page 43 Add R532, Q50 for +1.8VS Power Down discharge

C Test

- Page 41 Change C389 from 1U_0603_10V4Z to 1000P_0603_16V7K
Add C612 10P_0402_50V8K for EMI.
Add R546 1M_0402_5% for ALC250 issue.
- Page 42 Change R363 from 10K_0402_5% to 15K_0402_5% to reduce woofer volume.
- Page 34 Add C709, C710 for EMI.
- ### Pre-MP
- Page 21 Delete R384 & add U30 to prevent ENVDD output high pulse before reset
Change C4 from 100P_0402_50V7K to 0.1U_0402_16V4Z to delay +LDCVDD turn on timing.
- Page 42 Change C411 from 0.047U_0402_16V7K to 0.01U_0402_16V7K for ALC250 issue.

Compal Electronics, Inc.

Title		
HW PIR		
Size	Document Number	Rev
Customer	ECQ60 LA-2271	1A
Date:	Monday, August 09, 2004	Sheet 53 of 54

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Item	Reason for change	PG#	Modify List	Date	B.Ver#
1	Update 12VSP(FAN) SUSP Function	44	1. Change PR8 to 10K from 1K 2. Change PR218 to 100K from 0 ohm 3. Add PQ70 (2N7002) 4. Add PR282 0 ohm 5. Update design circuit for 2.5VREF and SUSP# signal	2004.04.22	
	Delete VTT_Powergood control module	44	1. Delete PR213 1K 2. Delete PC170 0.1U		
3	For common parts design				
4	delete unnecessary 0ohm resister		delete PR247,PR19,PR109,PR113,PR115,PR116,PR117,PR118,PR107,PR125,PR127,PR132,PR135,PR137,PR254,PR79,PR260,PR292,PR119		
5	adjust Vin detector	34	change PR170 from 73.2K_0603_1% to 22K_0603_1% change PR172 from 40.2K_0603_1% to 36K_0603_1% change PR167 from 84.5K_0603_1% to 82.5K_0603_1%		
6	adjust PACIN voltage from 3.3V to 3.2V	34	change PR168 from 8.2K_0805_5% to 10K_0805_1%		
7	EMI test failure	40	change PR105 and PR140 from 0_0603_5% to 2.2_0603_5%		
8	PC60 rate voltage not enough	36	change PC60 from 0.047_0603_16V to 0.047U_0603_25V		
9	Vin detector issue	34	change PR172 form 36K to 34.8K		

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Compal Electronics, Inc.			
Title			
PWR PIR			
Size	Document Number	Rev	
Customer	ECQ60 LA-2271	1A	
Date:	Monday, August 09, 2004	Sheet	54 of 54